

## CY4500

# EZ-PD<sup>™</sup> Protocol Analyzer Guide

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## Safety Information



The CY4500 EZ-PD<sup>™</sup> Protocol Analyzer is intended for use as a development, demonstration, and debugging platform for hardware or software in a laboratory environment. The analyzer is not intended for general consumer use. It generates, uses, and can radiate radio frequency energy. It has not been tested for compliance with the limits applicable under any standard. Operation of the equipment may cause interference with radio communications, in which case users, at their own expense, will be required to take whatever measures may be required to correct this interference.

Attaching additional wiring to this product or modifying the product operation from the factory default may affect its performance and cause interference with other apparatus in the immediate vicinity. If such interference is detected, suitable mitigating measures must be taken.

The CY4500 EZ-PD<sup>™</sup> Protocol Analyzer as shipped from the factory has been verified to meet with requirements of CE as a Class A product.



The CY4500 EZ-PD <sup>™</sup> Protocol Analyzer board contains electrostatic discharge (ESD)- sensitive devices. Electrostatic charges readily accumulate on the human body and any equipment, which can cause a discharge without detection. Permanent damage may occur to devices subjected to high-energy discharges. Proper ESD precautions are recommended to avoid performance degradation or loss of functionality. Store unused CY4500 EZ-PD <sup>™</sup> Protocol Analyzer board in the protective shipping package.
End-of-Life/Product Recycling The end-of-life cycle for this analyzer is five years from the date of manufacture mentioned on the back of the box. Contact the nearest recycler to discard the analyzer.

### **General Safety Instructions**

### **ESD** Protection

ESD can damage boards and associated components. If an ESD workstation is not available, use appropriate ESD protection by wearing an antistatic wrist strap attached to the chassis ground (any unpainted metal surface) on the board when handling parts.



### **Handling Boards**

The board provided with CY4500 EZ-PD<sup>™</sup> Protocol Analyzer is sensitive to ESD. This also applies to the boards that are provided with a plastic casing when they are removed from the casing. Hold the boards only by the edges. After removing a board from the box/casing, place it on a grounded, static-free surface. Use a conductive foam pad, if available. Do not slide the board over any surface.

## 1. Introduction



Cypress's CY4500 EZ-PD™ Protocol Analyzer supports protocol analysis of the USB-PD and USB Type-C specifications. It performs non-intrusive probing and captures accurate protocol messages on the CC line. This analyzer consists of Cypress's programmable MCU (PSoC<sup>®</sup> 5LP), which monitors data on the CC lines and sends this data to the host application over a USB interface. The Type-C plug and Type-C receptacle on this analyzer provide a pass-through for the Power Delivery (PD) Packets transmitted between each Type-C PD connection. The processor MCU taps these Power Delivery Packets without disturbing the system and transfers them over the USB interface to a PC running the host application.

## 1.1 Analyzer Contents

The CY4500 EZ-PD<sup>™</sup> Protocol Analyzer consists of the following contents:

- CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board enclosed in a transparent casing
- USB 2.0 Type-A to Micro-B Cable
- Jumper wires (6 in number)
- Quick Start Guide

### 1.1.1 Hardware Not Included With the Analyzer

The CY4500 EZ-PD<sup>™</sup> Protocol Analyzer Guide references the CY4531 CCG3 EVK as a Type-C host device for all the examples covered in this document. While it is highly recommended to have the CY4531 CCG3 EVK as a Type-C host device, it is not required and can be replaced with any USB Type-C host device, such as a notebook with a Type-C port. The following items are not included with the Analyzer:

- A USB Type-C host device (for example, a notebook with a Type-C port, CY4531 EZ-PD<sup>™</sup> CCG3 Evaluation Kit (EVK)) and a USB Type-C client device.
- Type-C Power Adapter that supports USB-PD. Recommended power adapters are listed in the Recommended Adapters and Cables section.
- As an option, Type-C to DisplayPort/HDMI/VGA adapters that can be used as Type-C devices for running the examples in this user guide. Recommended adapters are listed in the Recommended Adapters and Cables section.



## 1.2 Getting Started

Once the steps to install the analyzer as described in Analyzer Installation chapter are completed, refer to the Analyzer Operation chapter for instructions on how to run a quick demonstration and observe analyzer functionality.

### 1.2.1 CY4500 EZ-PD™ Protocol Analyzer Installation

The CyInstaller installs the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer along with the EZ-PD<sup>™</sup> Analyzer Utility software and all necessary CYUSB3.0 drivers. Refer to the Analyzer Installation chapter for more details.

## 1.3 List of Recommended Hardware

### 1.3.1 Recommended Host and Client Devices

See Table 1-1 for different options on choosing USB Type-C host and client devices. This analyzer is not shipped with any of the listed devices. A Type-C Power Adapter is required to run the example in the Analyzer Operation chapter.

No.	Description	Manufacturer	Vendor Link
	Host Devices		
1	Apple MacBook	Apple	Apple Store Link
2	Google Chrome book	Google	Google Store Link
3	CY4501 CCG1 Development Kit (Host Board)	Cypress	Cypress Link
4	CY4531 EZ-PD™ CCG3 EVK	Cypress	Cypress Link
5	CY4541 EZ-PD™ CCG4 EVK	Cypress	Cypress Link
	Client Devices		
1	Type-C Power Adapter that supports USB-PD	Apple	Apple Store Link
2	Type-C to Digital AV Multiport Adapter	Apple	Apple Store Link
3	CY4501 CCG1 Development Kit (Client Board)	Cypress	Cypress Link

Table 1-1. List of Recommended Host & Client Devices

## 1.3.2 Recommended Adapters and Cables

See Table 1-2 for different options to obtain a set of cables recommended to work with Type-C host/client devices. This analyzer is not shipped with these adapters or cables.

No.	Description	Manufacturer	MPN	Vendor Link
1	Type-C to Display Port Adapter	Belinda	-	Amazon Link
2	Type-C to HDMI Adapter	Cable Matters	-	Amazon Link
3	Type-C to VGA Adapter	Cable Matters	-	Amazon Link
4	DisplayPort to DisplayPort Cable (6", gold plated)	Cable Matters	102005-6	Amazon Link
5	Mini DisplayPort to DisplayPort Cable (3", gold plated)	Cable Matters	101007-BLACK-3	Amazon Link

Table 1-2. List of Recommended Adapters and Cables

# 2. Analyzer Installation



This chapter describes the steps to install the CY4500 EZ-PD™ Protocol Analyzer.

## 2.1 CY4500 EZ-PD™ Protocol Analyzer Software Installation

To install the analyzer software, follow these steps:

1. Download the latest analyzer software setup *CY4500Setup.exe* file from the Cypress website http://www.cypress.com/CY4500. This package contains the software EZ-PD Analyzer Utility for running the analyzer and the relevant documentation (User Guide, Quick Start Guide, and Release Notes). Double-click on the executable to start the installation. Click **Next** when the screen shown in Figure 2-1 appears.



Figure 2-1. CY4500 EZ-PD<sup>™</sup> Protocol Analyzer Installer Screen

2. Select the required **Installation Type** and click **Next** to start the install (Figure 2-2). For first-time installation, it is recommended that you select "Typical" as the **Installation Type**.



Figure	2-2.	Installation	Wizard
--------	------	--------------	--------

Choose the type of installation Product: CY4500 EZ-PD Protocol Analyzer Installation Type: Typical Installs the most common features of CY4500 EZ-PD Protocol Analyzer.	USB Type-C www.cypress.com/CCG
--	--------------------------------------

3. Accept the license agreement for the software components and click Next (Figure 2-3).

Figure 2-3. License Agreement



4. Figure 2-4 shows the installation progress.







5. Enter Contact Information or select Continue Without Contact Information and click Finish (Figure 2-5).

Figure 2-5. Software Installation Complete

Supervised States Cylocol Analyzer	े <b>२</b>
	Contact Information Name: * Company: Email: * * Indicates a required field Privacy Policy
Cypress Semiconductor Corporation All rights reserved	<ul> <li>Launch EZ-PD Analyzer Utility</li> <li>Open Release Notes</li> <li>Open Kit User Guide</li> <li>Launch Update Manager</li> <li>Continue Without Contact Information</li> </ul>
Contact Us	Einish



6. When installation is complete, you have the option to launch **Cypress Update Manager** (Figure 2-6) to ensure you have the latest software package. Click the **Check for updates** button at the bottom of the window. If "No Updates" appears adjacent to the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer, click the **Exit** button. If there are updates, click the **Update** button to download and install the latest analyzer package.

👶 Cypress Update Manager	-	_ <b>_</b> X
Cypress Update Manager detected the following ins	stalled software	
💮 CY4500 EZ-PD Protocol Analyzer 1.0 Rev. **	Release Notes No Update	es Configure Uninstall
No Updates Available		
	Check for updates	Preferences Exit

### Figure 2-6. Cypress Update Manager

Note: You can launch the Cypress Update Manager at any time from Start > All Programs > Cypress > Cypress Update Manager.

7. After the installation is complete, the contents are available at the following location: <Install Directory>\CY4500 EZ-PD Protocol Analyzer\1.0.

**Note:** On the Windows 32-bit platform, the default <Install Directory> is C:\Program Files\Cypress; on the Windows 64-bit platform, it is C:\Program Files(x86)\Cypress.

# 3. CY4500 EZ-PD<sup>™</sup> Protocol Analyzer Hardware



The CY4500 EZ-PD<sup>™</sup> Protocol Analyzer consists of a PSoC 5LP controller that probes into the USB Type-C signals over I<sup>2</sup>C transpiring between the USB Type-C plug and receptacle, and then transfers them over to a host PC via a USB Micro-B cable. The EZ-PD Analyzer Utility must already be running on the host PC to receive these messages, decode them, and show them as an output. The block diagram for the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board is shown in Figure 3-1.





The board comprises of a Type-C plug at one end and a Type-C receptacle at the other, which is used to connect to the Type-C device or system under test. The board is powered over the USB micro-B port, which provides 4V, 500mA. The board also consists of one LED to indicate the power status. Besides this, it has connector headers brought out for the GPIOs, CC lines, VBUS, and SBU lines from the USB-PD system. Figure 3-2, Figure 3-3 and Figure 3-4 show the front view, back view and side view of CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board respectively. Table 3-1 contains their detailed description. Headers J1 and J8 are located on the back side of the board. Users need to take precaution and ensure that the exposed header pins of J1 and J8 do not get shorted. Shorting these exposed pins may result in a functional failure or unexpected behavior of the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer.



Figure 3-2: CY4500 EZ-PD™ Protocol Analyzer Board (Front View)



Figure 3-3. CY4500 EZ-PD<sup>™</sup> Protocol Analyzer Board (Back View)



J1 & J8: Signal Names

Figure 3-4. CY4500 EZ-PD<sup>™</sup> Protocol Analyzer Board (Side View)





### CY4500 EZ-PD™ Protocol Analyzer Hardware

Connector	Description					
	USB Type-C Signal Header					
	Pin 1: CC1					
	Pin 2: CC2					
J1	Pin 3: SBU1					
	Pin 4: SBU2					
	Pin 5: VBUS					
	Pin 6: GND					
J2	Type-C Receptacle					
J3	Type-C Plug					
J7	USB Micro-B Connector					
	Message Trigger Header					
10	Pin 1: SOM					
78	Pin 2: EOM					
	Pin 3: MTR					

### Table 3-1: CY4500 EZ-PD™ Protocol Analyzer Board Interfaces

**Note 1:** Pins of header J8 are used for the triggering functionality. They can be connected using the jumper cables provided with the kit to an oscilloscope or other hardware that needs to be triggered. See the EZ-PD<sup>TM</sup> Analyzer Utility User Guide's section 2.3 for more details on how to use this functionality.

**Note 2**: The EZ-PD<sup>™</sup> Protocol Analyzer Board is protected using a transparent casing. The board should not be removed from the casing any time while using the Analyzer.

# 4. Analyzer Operation



This chapter describes how to run the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer with the EZ-PD Analyzer Utility and also how to update the firmware of the PSoC 5LP device of the analyzer. You must complete the installation procedure explained in the Analyzer Installation chapter before proceeding.

## 4.1 Capturing PD Packets Using CY4500 EZ-PD™ Protocol Analyzer

The steps to run the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer in order to capture the Power Delivery Packets on the CC bus are as follows:

- 1. Using a USB Micro-B cable, connect the USB Micro-B receptacle (Connector J7) of the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board to the host PC. LED1 of the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board starts blinking in white color.
- Connect the USB Type-C plug (Connector J3) of the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board to the Type-C host device. In this user guide, the CY4531 EZ-PD<sup>™</sup> CCG3 Evaluation Kit (EVK) (not provided with this analyzer) is used as a Type-C host device as an example.
- 3. Verify that your setup looks similar to the image shown in Figure 4-1.

Figure 4-1: Setup for Operating CY4500 EZ-PD™ Protocol Analyzer



4. On the host PC, launch the EZ-PD Analyzer Utility from **Start** > **All Programs** > **Cypress EZ-PD Analyzer Utility**. The EZ-PD Analyzer Utility's start-up page will look similar to the image shown in Figure 4-2.



Figure 4-2: EZ-PD Analyzer Utility Start-up Page

EZ-PD™ Analyzer Utility					
<u>File</u> <u>Actions</u> <u>H</u> elp					
				VBUS Voltage: 0 V	VBUS Current: 0 A
T Status: None - SOP: None	Message:	Msg ID: Obj	Count: Data Role:	Power Role:	
SL# Status SOP Message Id Bole	Power Obj Data	Start Time	Duration Delta (us) Voltage	Vbus Detailed View Trigger	
		()	(ab) Fundga	Description	Value
EZ-PD Analyzer connected. PD Packet Co	ount:0				

- 5. Click on the **Start Capturing** icon ►. The icon changes to the **Stop Capturing** icon ■. Alternatively, you can start capturing data by clicking on **Actions > Start Capturing** from the menu bar. The status bar at the bottom of the utility window shows the message "EZ-PD Analyzer is running" (similar to the status bar at the bottom of Figure 4-4).
- Connect a USB Type-C power adapter (not provided with the analyzer) to the USB Type-C receptacle (Connector J2) of the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board. Verify that your setup looks similar to the image shown in Figure 4-3. The USB Type-C power adapter mentioned here is just an example. Any USB Type-C device can be used in its place.





### Figure 4-3: Completing the Setup for Operating CY4500 EZ-PD™ Protocol Analyzer

7. Upon connecting the USB Type-C power adapter, the utility running on the host PC automatically displays a string of messages on the screen. Make sure that the hardware connections are not loose or do not get disconnected in the middle of a data capturing event. The running EZ-PD Analyzer Utility, along with the messages, will look similar to the image shown in Figure 4-4.

😁 EZ-	📮 EZ-PD <sup>™</sup> Analyzer Utility														
Eile Actions Help															
P H		<b>Ro Ro</b> (	¢.											VBUS Voltage: 14.88 V	VBUS Current: 0.06 A
T S	atus: No	ne	▼ SOP: Nor	ne		▼ M	essage:	Msg ID:		Obj Count:	1	Data Role:	Por	wer Role:	
				Msc	Data	Power	Obi		Start Time	Duration		Vbus	Vbus	Detailed View Trigger	
SL#	Status	SOP	Message	ld	Role	Role	Count	Data	(us)	(us)	Delta (us)	Voltage(V)	Current(A)	Description	Value
1	ок	SOP	Source_Cap	0	DFP	Source	2	0x2161 0x80190F0 0x4A0C8	5,423,420	767	0	5,214	N/A	SOP Type	SOP
2	ОК	SOP	Source_Cap	0	DFP	Source	2	0x2161 0x80190F0 0x4A0C8	5,425,841	768	1,654	5,214	N/A	Header	0x2161
3	ОК	SOP	Source_Cap	0	DFP	Source	2	0x2161 0x80190F0 0x4A0C8	5,427,826	767	1,217	5,219	N/A	Reserved (15)	0
4	ок	SOP	Source_Cap	0	DFP	Source	2	0x2161 0x80190F0 0x4A0C8	5,579,209	767	150,616	5,225	N/A	Data Objs (1412)	2
5	ок	SOP	Source_Cap	0	DFP	Source	2	0x2161 0x80190F0 0x4A0C8	5,582,613	768	2,637	5,192	N/A	Message Id (119)	0 SOURCE (1)
6	ОК	SOP	Source_Cap	0	DFP	Source	2	0x2161 0x80190F0 0x4A0C8	5,584,598	767	1,217	5,197	N/A	Spec Rev (7, 6)	SOURCE (1) Rev 2.0 (1)
7	ОК	SOP	Source_Cap	0	DFP	Source	2	0x2161 0x80190F0 0x4A0C8	5,735,913	767	150,548	5,214	N/A	Port Data Role (5)	DEP (1)
8	ок	SOP	GoodCRC	0	UFP	Sink	0	0x41	5,736,825	498	145	5,214	N/A	Reserved (4)	0
9	ок	SOP	Request	0	UFP	Sink	1	0x1042 0x2201685A	5,737,855	631	532	5,203	N/A	Msg Type (30)	Source Capabilities
10	ОК	SOP	GoodCRC	0	DFP	Source	0	0x161	5,738,547	501	61	5,219	N/A	Power Data Obj-Source 1	0x80190F0
11	ок	SOP	Accept	1	DFP	Source	0	0x363	5,739,556	501	508	5.186	N/A	Type (3130)	Fixed
12	ок	SOP	GoodCRC	1	UFP	Sink	0	0x241	5,740,203	498	146	5.219	N/A	Use Suspend Supported (29)	No (U)
13	OK	SOP	PS RDY	2	DEP	Source	0	0x566	5 831 079	501	90.378	14 905	N/A	Externally Provered (27)	Yes (1)
14	OK	SOP	GoodCRC	2	LIED	Sink	0	0×441	5 831 726	498	146	14.888	N/A	USB Communications Capable	
15	OK	SOR	VDM	2	DEP	Source	1	0+1765 0+55002001	5,031,720	624	102	14,000	N/A	(26)	No (U)
10	OK	SOP	CandODC	2	LED	Cials	0	0.041	5,032,320	400	147	14,000	N/A	Data Role Swap (25)	No (0)
17	OK	30P	KOM	-		Ointe	4	0.4045 0.55000041 0.0000	5,655,107	400	147	14,077	IN/A	Reserved (2422)	0
17	UK	50P	VDM	1	UFP	SINK	4	0x424F 0xFF00804T 0x9000	5,634,270	1,020	664	14,699	N/A	Peak Current (2120)	IOC (default)
18	OK	SOP	GoodCRC	1	DFP	Source	0	0x361	5,835,362	501	64	14,872	N/A	Max Current in 10mA (9_0)	240(2 404)
19	ок	SOP	VDM	4	DFP	Source	1	0x196F 0xFF008002	5,836,375	634	512	14,877	N/A	Power Data Obi-Source 2	0x4A0C8
20	ок	SOP	GoodCRC	4	UFP	Sink	0	0x841	5,837,157	498	148	14,905	N/A	Type (3130)	Fixed
21	ок	SOP	VDM	2	UFP	Sink	1	0x144F 0xFF008082	5,838,207	631	552	14,883	N/A	Dual-Role Power (29)	No (0)
22	ОК	SOP	GoodCRC	2	DFP	Source	0	0x561	5,838,904	501	66	14,861	N/A	USB Suspend Supported (28)	No (0)
														Externally Powered (27)	No (0)
														(26) (26)	No (0)
														Data Role Swap (25)	No (0)
														Reserved (2422)	0
														Peak Current (2120)	IOC (default)
														Volt in 50mV (1910)	296(14.80V)
														Max Current in TUmA (90)	200(ZA)
EZ-PI	Analy	zer is	running. P	PD Pa	cket C	ount:2	2							-	

Figure 4-4: EZ-PD Analyzer Utility Showing PD Packets Captured on CC Line



- 8. The EZ-PD Analyzer Utility displays the messages that have been captured (sent and received) between the USB Type-C host device and the USB Type-C client device on the CC line. Click on the icon to stop capturing messages. Alternatively, you can stop capturing data by clicking on Actions > Stop Capturing from the menu bar. Upon clicking this icon, the status bar at the bottom of the utility window displays EZ-PD Analyzer is stopped.
- You can save the captured set of messages by clicking on File > Save. These files can be saved in the .ccgx, .xlsx or .csv formats, but only the file saved in .ccgx format can be opened using the EZ-PD Analyzer Utility by clicking File > Open.
- 10. To clear a captured group of messages, you can either click the Clear Data sicon or click on Actions > Clear Data from the menu bar. This clears all the captured group of messages. If they are not saved prior to clicking the Clear Data icon, then these messages are lost.
- 11. If the hardware shown in Figure 4-3 is not stable or gets disconnected at any point, the status bar at the bottom of the utility window shows the message **EZ-PD Analyzer is disconnected.**
- 12. For more information on how to use the EZ-PD Analyzer Utility, refer to the EZ-PD<sup>™</sup> Analyzer Utility User Manual by clicking **Help > User Manual**.

# 4.2 Updating PSoC 5LP Device Firmware on the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer

The firmware of the PSoC 5LP device present on the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board can be updated using the EZ-PD Analyzer Utility.

### 4.2.1 Updating Firmware Using EZ-PD Analyzer Utility

- 1. Using a USB Micro-B cable, connect the USB Micro-B receptacle (Connector J7) of the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board to the host PC. LED1 of the CY4500 EZ-PD<sup>™</sup> Protocol Analyzer board starts blinking in white color.
- 2. On the host PC, launch the EZ-PD Analyzer Utility from **Start > All Programs > Cypress EZ-PD Analyzer Utility**. The utility's start-up page will look similar to the image shown in Figure 4-2.
- 3. Check the current firmware loaded on the EZ-PD Protocol Analyzer Hardware by selecting Actions > Get Firmware Version as shown in Figure 4-5.

EZ-PD <sup>™</sup> Analyzer Utility																
Γ	File	Acti	ons Help													
6	-		Stop Captu	ring	Ctrl+R										VBUS Voltage: 14.89 V	VBUS Current: 0.06 A
E	<b>T</b> St	6	Clear Data	Ctrl	+Alt+C		▼ Mes	age:	Msg ID:	Obj Count:		Data Role	:	Po	wer Role:	
h	ei #	(ئ	Download	irmware		ata	Power	Obj	Data	Start	Duration	Dolto (uo)	Vbus	Vbus	Detailed View Trigger	
14	SL#	0	Get Firmwa	re Version		ole	Role	Count	Data	Time (us)	(us)	Delta (us)	oltage(\	Current(A)	Description	Value
1		Ro.	Add Marke		Ctrl+M	Р	Source	2	0x2161 0x80190F0 0x4A0C8	4,281,48	767	0	5,214	N/A	SOP Type	SOP
2			Remove Ma	arker Ctrl+S	hift+M	Р	Source	2	0x2161 0x80190F0 0x4A0C8	4,281,48	767	1,659	5,225	N/A	Header	0x2161
3		6	Novt Marke	r Ctrl	Pight	Р	Source	2	0x2161 0x80190F0 0x4A0C8	4,281,48	767	1,218	5,181	N/A	Reserved (15)	0
4		P	INCAL IVIAI NO	P		P	Source	2	0x2161 0x80190F0 0x4A0C8	4,281,63	767	150,642	5,219	N/A	Data Objs (1412)	2
1 5			Set Capture	Limit		Р	Source	2	0x2161 0x80190F0 0x4A0C8	4,281,64	767	2,640	5,186	N/A	Message Id (119)	0
e			Clear Filter	s)		Р	Source	2	0x2161 0x80190E0 0x4A0C8	4 281 64	767	1 216	5 197	N/A	Port Power Role (8)	SOURCE (1)
			с. т. н:			P	Source	2	0x2161 0x80190E0 0x440C8	4 281 79	767	150 643	5 208	N/A	Spec Rev (76)	Rev 2.0 (1)
			GO TO Line.	. Ctri-	+Alt+G		Sink	0	041	4 201,70	409	146	5 214	NIA	Port Data Role (5)	DFP (1)
			CC Termina	tion Settings.			OTTA			4,201,73	400	140	5,214	IN/A	Meg Type (3, 0)	Source Capabilities
	-	OI	001	nequest		-orP	Sink	1	0x1042 0x2201685A	4,281,79	631	595	5,208	N/A	Power Data Obi-Source 1	0x80190E0
12	0	ок	SOP	GoodCRC	0	DFP	Source	0	0x161	4,281,79	501	61	5,219	N/A	Type (3130)	Fixed
	1	OK	SOP	Accept	1	DFP	Source	0	0x363	4,281,79	500	492	5,192	N/A	Dual-Role Power (29)	No (0)
1	2	OK	SOP	GoodCRC	1	UFP	Sink	0	0x241	4,281,79	498	147	5,214	N/A	USB Suspend Supported (28)	No (0)
1	3	ОК	SOP	PS_RDY	2	DFP	Source	0	0x566	4,281,88	500	90,379	14,899	N/A	Externally Powered (27)	Yes (1)
1	4	ок	SOP	GoodCRC	2	UFP	Sink	0	0x441	4,281,89	499	147	14,888	N/A	USB Communications Capable (26)	No (0)
	5	ОК	SOP	VDM	3	DFP	Source	1	0x176F 0xFF008001	4,281,89	634	102	14,861	N/A	Data Role Swap (25)	No (0)

### Figure 4-5: Get the current firmware version

4. Wait for the firmware version dialog box to appear as shown in Figure 4-6.



Figure 4-6: Firmware version dialog box

EZ-PD™ An	alyzer Utility
1	Firmware version : 1.0.0.31
	ОК

Click **OK** to return to the main menu. Proceed with rest of the procedure to download the firmware incase a newer version of the firmware is available.

- 5. Click on Actions > Download Firmware to initiate the firmware update. If the analyzer hardware is not connected, the status bar at the bottom of the utility window shows the message EZ-PD Analyzer is not connected. Ensure that the board connections are secure. If the utility window has any unsaved messages, a new window will pop up with a message "Data is about to clear from the window. Do you want to save?". Users can select Yes or No based on their requirement.
- 6. Upon executing step 5, a new window, **Download firmware,** opens as shown in Figure 4-7. The device enters the bootloader mode by default.

Download Firmware	x
Download Status	
Device(0x4b4, 0xb71d) is connected. Device is in Bootloader Mode.	*
Load File Program Exit	

Figure 4-7: "Download Firmware" Window

 Click Load File... as shown in Figure 4-7 and select the CY4500 firmware file in .cyacd format as shown in Figure 4-8. Click Open. The binary file provided with the analyzer is available at the following location: <Install Directory>\CY4500 EZ-PD Protocol Analyzer\1.0\Firmware.

**Note:** On a Windows 32-bit platform, the default <Install Directory> is C:\Program Files\Cypress; on the Windows 64-bit platform, it is C:\Program Files(x86)\Cypress.



Open CYACD File							
😋 🔍 🗢 🔰 « Windows7_OS	(C:) ▶	Program Files (x86)      Cypress      CY4500 EZ-PD Protocol Analy	zer 🕨 1.0 🕨 Firmwar	re 🕶 🗲	Search Firmware		٩
Organize 🔻 New folder					•== •		0
Jag gg	*	Name	Date modified	Туре	Size		
Jintel		CY4500_EZ-PD_ProtocolAnalyzer_FW_1_0_0_31.cyacd	8/26/2016 9:10 PM	CYACD File	81 KB		
Latest							
LatestButOne	=						
🥼 mfg 🐌 MSOCache							
PADSViewer Projects PerfLogs							
Program Files							
Program Files (x86)							
SWTOOLS							
鷆 temp							
Users							
Windows	_						
File name:	CY450	0 EZ-PD ProtocolAnalyzer FW 1 0 0 31.cvacd		•	cyacd file (*.cyacd)		•
				(	Open 🔽	Cancel	

Figure 4-8. Selecting the CY4500 Firmware File

### 8. Click Program to initiate the firmware download process as shown in Figure 4-9.

Figure 4-9. Initiating Firmware Download

Download Firmware	x
Download Status	
Firmware file :C:\Program Files (x86)\Cypress\CY4500 EZ-PD Protocol Analyzer\1.0\Firmware\CY4500_EZ- PD_ProtocolAnalyzer_FW_1_0_0_31.cyacd	4 III 4
Load File Program Exit	

9. This starts the firmware download process. Click Exit when complete as shown in Figure 4-10.

Figure 4-10. Completion of Firmware Download

Download Firmware	X
Download Status	
Firmware downloaded successfully. Download ended at 8/12/2016 9:53:02 PM Bootloader HID Device(0x4b4, 0xb71d) is disconnected.	
Load File Program	Exit

10. The steps to update the firmware are now complete. The CY4500 EZ-PD<sup>™</sup> Protocol Analyzer hardware restarts with the firmware used in step 7 and is ready to use.

# 5. Appendix A: Terminology



This guide assumes that the user of the CY4500 EZ-PD Protocol Analyzer is familiar with the fundamentals of Type-C connectivity and the USB Power Delivery protocol. A brief description of Type-C terms is provided here for reference.

- Alternate Modes: A feature of a USB Type-C system whereby one or both of the SuperSpeed lanes may be repurposed for use with a different serial protocol, such as a DisplayPort, eSATA, or Thunderbolt.
- Client Device: A USB peripheral such as a hub, docking station, or monitor.
- Configuration channel (CC): A USB Type-C bus wire used to transmit protocol signals. This is a half-duplex 300-kHz signal.
- Consumer: A Type-C port that sinks power from VBUS.
- DisplayPort: A digital display interface standard developed by the Video Electronics Standards Association. It is used primarily to connect a video source to a display such as a computer monitor.
- Downstream facing port (DFP): A USB Type-C port on a host or a hub to which devices are connected.
- Dp, Dn: USB Type-C bus wires used to transmit and receive USB 2.0 data.
- Dual-role port (DRP): A USB Type-C port that can operate as either a DFP or a UFP.
- Electronically Marked Cable Assembly (EMCA): A USB cable that includes an IC that reports cable characteristics (such as current rating) to the Type-C ports.

### Figure 5-1. Type-C Plug & Receptacle pins

USB3.0 Type-C Plug





- Host Device: A USB Host system such as a PC, notebook, laptop, and so on.
- Provider: A Type-C port that sources power over VBUS.
- Sideband use (SBU): A USB Type-C bus wire used for non-USB control signals, such as DisplayPort control signals.
- Type-C Transceiver: A transmitter/receiver that communicates over the CC.
- TX1p, TX1n, RX1p, RX1n, TX2p, TX2n, RX2p, and RX2n: USB Type-C bus wires used to transmit and receive SuperSpeed USB and PCIe or DisplayPort data.
- Upstream facing port (UFP): A USB Type-C port on a device or a hub that connects to a host or the DFP of a hub.
- USB Power Delivery (USB PD, PD): A new USB standard that increases maximum power delivery over USB from 7.5 W to 100 W.
- USB Type-C (Type-C): A new standard with a slimmer USB connector and a reversible cable, capable of sourcing up to 100 W of power and supporting Alternate Modes.
- VBUS: A USB Type-C bus wire used for power; initially 5 V, but increased up to 20 V on USB PD systems.
- VCONN: A USB Type-C bus wire used to power the IC in the EMCA.

# **Revision History**



## **Document Revision History**

Document Title: CY4500 EZ-PD™ Protocol Analyzer Guide			
Document Number: 002-12170			
Revision	Issue Date	Origin of Change	Description of Change
**	7/8/2016	VGT	New user guide for CY4500 EZ-PD <sup>™</sup> Protocol Analyzer.
*A	11/9/2016	VGT	Added CE logo, updated images for CY4500 EZ-PD <sup>™</sup> Protocol Analyzer hardware in Chapters 3 and 4. Updated GUI snapshots in Chapter 4, added jumper wires in Section 1-1, added Note 1 under Table 3-1.
*B	11/11/2016	SELV	Fixed formatting issues
*C	05/26/2017	NIBK	Updated Cypress Logo and Copyright.



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