

SAM9G15-EK
SAM9G25-EK
SAM9G35-EK
SAM9X25-EK
SAM9X35-EK

User Guide





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Section 1

Introduction

1.1 Scope

This User Guide introduces the Evaluation Kit and describes the development and debugging capabilities running on an AT91SAM9 ARM[®]-based Embedded MPUs as listed below:

- SAM9G15
- SAM9G25
- SAM9X25
- SAM9G35
- SAM9X35

The User Guide pertains to the following Evaluation Kit references:

- SAM9G15-EK
- SAM9G25-EK
- SAM9X25-EK
- SAM9G35-EK
- SAM9X35-EK

This User Guide gives design details on the Evaluation Kit and is made up of 5 sections:

- Section 1 includes a photo of the board, device and kit references and applicable documents.
- Section 2 describes the kit contents, its main features.
- Section 3 provides instructions to power up the Evaluation Kit and describes how to use it.
- Section 4 describes the CPU Module (CM), the Main Board (MB) and optional Display Module (DM).

Figure 1-1. Board Photo (Display module is optional)



1.2 Applicable Documents

Table 1-1. Applicable Documents

Reference	Title	Comment
Atmel lit° 11052	SAM9G15 datasheet	These documents provide technical support for each one of the Atmel ARM-based Embedded MPU products supported by these Evaluation Kits. The datasheets can be found on www.atmel.com in the SAM9G/SAM9X product families by means of the link below:
Atmel lit° 11032	SAM9G25 datasheet	
Atmel lit° 11054	SAM9X25 datasheet	
Atmel lit° 11053	SAM9G35 datasheet	
Atmel lit° 11055	SAM9X35 datasheet	

http://www.atmel.com/products/at91/default.asp?category_id=163&family_id=605&source=global_nav



Section 2

Kit Contents

2.1 Deliverables

The Evaluation Kits include:

- Board
 - One EK board
 - One of the five available CPU modules (CM)
 - SAM9G15-CM
 - SAM9G35-CM
 - SAM9X35-CM
 - SAM9G25-CM
 - SAM9X25-CM
 - One optional DM board featured in SAM9G15, SAM9G35, SAM9X35 kits only.
- Power supply
 - Universal input AC/DC power supply with US, Europe and UK plug adapters
 - One 3V Lithium Battery type CR1225
- Cables
 - One serial RS232 cable
 - One micro A/B-type USB cable
 - One RJ45 crossed cable
- A Welcome Letter

Figure 2-1. Unpacked Evaluation Kit



Unpack and inspect the kit carefully. Contact your local Atmel distributor, should there be issues concerning the contents of the kit.

2.2 Evaluation Board Specifications

Table 2-1. Evaluation Kit Specifications

Characteristics	Specifications
Clock speed	400 MHz PCK, 133 MHz MCK
Ports	Ethernet, USB, RS232, DBGU, JTAG, CAN, Audio, SD Card
Board supply voltage	5 VDC from connector
Temperature - operating - storage	-10° to +50° C -40° to +85° C
Relative humidity	0 to 90% (non condensing)
Dimensions - EK (Evaluation Kit) - CM (Computer Module) - DM (Display Module)	165 mm x 135 mm 67.6 mm x 35 mm 135 mm x 80 mm
RoHS status	Compliant



2.3 Electrostatic Warning

The Evaluation Kit is shipped in a protective anti-static package. The board system must not be subjected to high electrostatic potentials. A grounding strap or similar ESD protective device should be worn when handling the board in hostile ESD environments (offices with synthetic carpet, for example). Avoid touching the component pins or any other metallic element on the board.



Section 3

Power Up

3.1 Power Up the Board

Unpack the board taking care to avoid electrostatic discharge. Unpack the power supply, select the right power plug adapter corresponding to that of your country, and insert it in the power supply.

Connect the power supply DC connector to the board and plug the power supply to an AC power plug.

The board LCD should light up and display a welcome page. Then, click or touch icons displayed on the screen and enjoy the demo.

3.2 DevStart

The on-board NAND Flash contains an installation guide named: "SAM9x5-EK DevStart".

It is stored in the "SAM9x5-EK DevStart" folder on the USB Flash disk available when the Evaluation Kit is connected to a host computer.

Click the file "welcome.html" in this folder to launch the SAM9x5-EK DevStart.

DevStart guides the user through the installation processes of IAR™ EWARM, Keil™ MDK and GNU toolkits. Then, it gives step-by-step instructions on how to rebuild a single example project and how to program it into the Evaluation Kit. Optionally, if the user has a SAM-ICE™ interface, instructions are also given about how to debug the code.

It is strongly recommended that users backup the "SAM9x5-EK DevStart" folder on their computer before launching it.

3.3 Recovery Procedure

The DevStart ends by giving step-by-step instructions on how to recover the Evaluation Kit to the state as it was when shipped by Atmel.

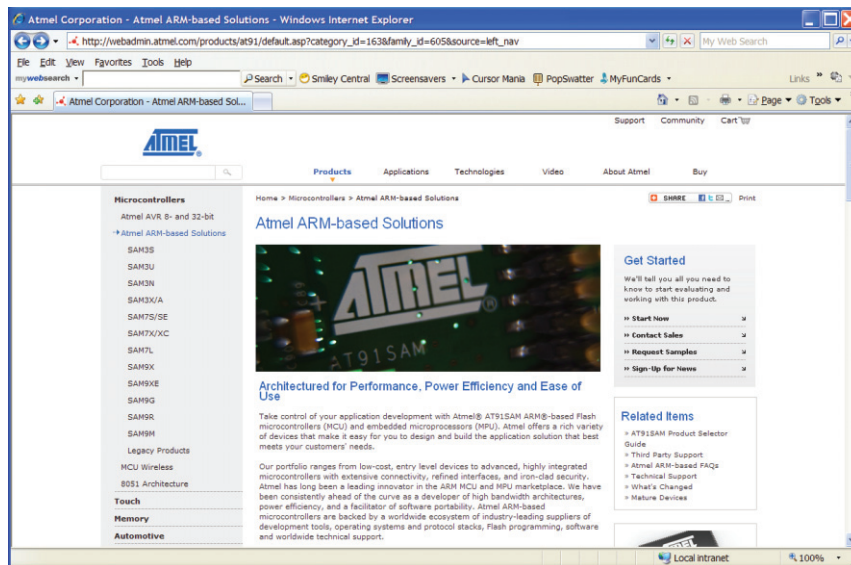
Follow the instructions if contents of the NAND Flash or the SPI DataFlash® have been deleted, in order to recover from this situation.

3.4 Sample Code and Technical Support

After boot up, designers can run sample code or their own application, on the development kit. Users can download sample code and get technical support from the Atmel web site:

http://www.atmel.com/products/at91/default.asp?category_id=163&family_id=605&source=global_nav

Figure 3-1. Atmel Web Site



Note: Different interfaces on the EK boards share the same connections to the CPU module. Therefore the actual usage depends on the CPU module featured in the evaluation kit.



Section 4

Evaluation Kit Hardware

4.1 Introduction

The Evaluation Kit is a fully-featured evaluation platform for the Atmel MPU. The Evaluation Kit enables users to extensively evaluate, prototype and create application-specific designs.

The Evaluation Kit is a new platform architecture based on a Main Board (MB), a CPU Module (CM) equipped with one of the five processors and an optional Display Module (DM).

The Evaluation Kit consists of three boards:

1. The CPU Module (CM) board, is a single-board computer that integrates all the core components and is mounted onto an application-specific carrier board (EK board). The CPU Module has specified pinouts based on the SODIMM200 connector. It provides the functional requirements for an embedded application. These functions include, but are not limited to, graphics, audio, mass storage, network and multiple serial and USB ports. A single SODIMM200 connector provides an interface for the carrier board to carry all the I/O signals to and from the CPU Module.
2. The Evaluation Kit board (EK Main Board) provides all the interface connectors required to attach the system to the application specific peripherals. This versatility allows the designer to create a densely packed solution, which results in a more reliable product while simplifying system integration.
3. The optional Display Module (DM) board integrates LCD, TouchScreen and Qtouch® technology.

[Table 4-1](#), on the page that follows, lists the features provided on the Evaluation Kit:

Table 4-1. Evaluation Kit Features

Supported modules			SAM9 products				
Expansion Slot	SO-DIMM200						
Processor options			SAM9 G15	SAM9 G25	SAM9 G35	SAM9 X25	SAM9 X35
LAN	MII/RMII Ethernet 10/100 w/PHY and three Led status	ETH0		X	X	X	X
	RMII Ethernet 10/100 w/PHY and three Led status	ETH1				X	
USART/UART	RS232 four wires/RS485 Shared interface	COM0	X	X	X	X	X
	RS232 four wires	COM3		X		X	
	RS232 two wires	DBGU	X	X	X	X	X
CAN	CAN interface Shared interface	CAN0				X	X
		CAN1				X	X
USB	2 * USB 2.0 Host		X	X	X	X	X
	1 * USB 2.0 Host/Device		X	X	X	X	X
SMD	Software Modem Device		X	X	X	X	X
Memory Card Support	µSD Card Slot Onboard	HSMCI 0	X	X	X	X	X
	MMC/MMC+/SD/SDIO/CE-ATA	HSMCI 1	X	X	X	X	X
ISI				X			
LCD + Touch Screen	24-bit Output Mode		X		X		X
ZigBee®			X	X	X	X	X
SPI			X	X	X	X	X
TWI			X	X	X	X	X
DEBUG	JTAG Test Access Port		X	X	X	X	X

4.2 Computer Module (CM)

4.2.1 CM Board Overview

The CM board is the CPU module at the heart of the system. It connects to the EK board through a SO-DIMM200 interface. It carries the processor and external memories. The CM board serves as a minimal CPU sub-system. All five processors: SAM9G15, SAM9G25, SAM9X25, SAM9G35 and SAM9X35 share the same CM circuitry with minor configuration settings.

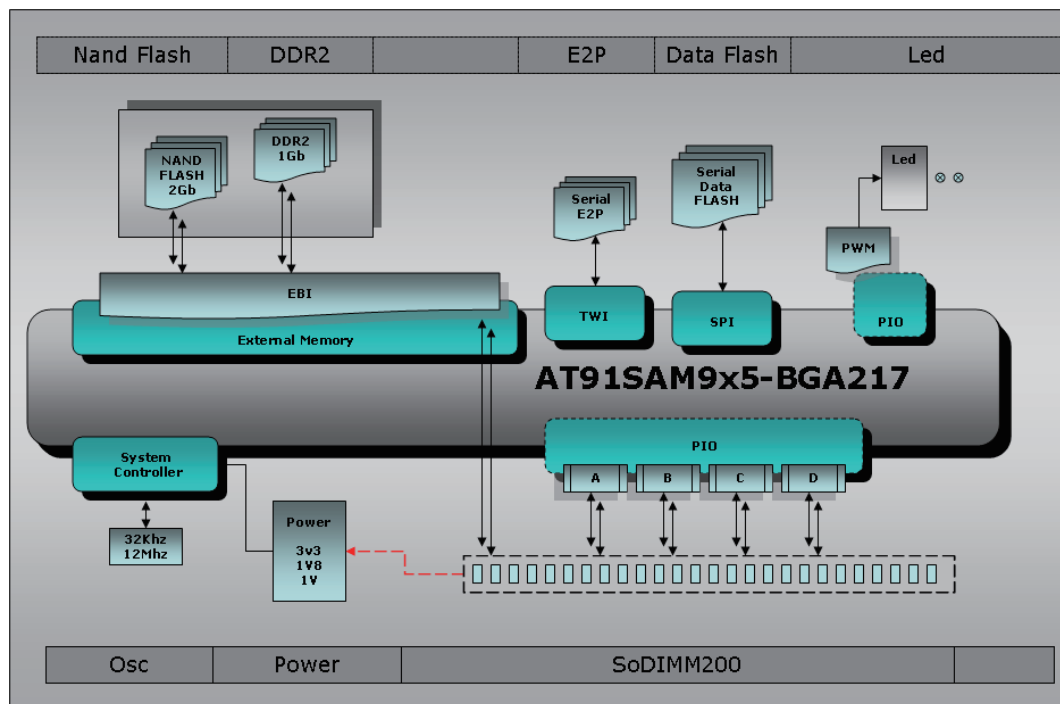
Note: There are three CM boards from three different manufacturers. The five processors are implemented as shown in [Table 4-2](#) below:

Table 4-2. CM Board Implementation

Manufacturer & Module kind	SAM9G15-CM	SAM9G25-CM	SAM9G35-CM	SAM9X25-CM	SAM9X35-CM
mfg 1	x		x		
mfg 2				x	x
mfg 3	x	x	x	x	x

The three CM boards share the same circuitry design but with different designator information and PCB layouts. The circuitry reference in this guide, for common design parts, refers to schematics from SAM9G25-CM (mfg 3). All the other schematics are provided in [Section 4.2.6 "Schematics"](#).

Figure 4-1. Board Architecture



4.2.2 Equipment List

The CM board is built around the integration of an ARM926-based microcontroller (BGA217 package) with external memory and optional Ethernet PHYSical Layer Transceiver.



4.2.2.1 Devices

Following is the list of the CM board components:

- One SAM9 Embedded MPU from the list below
 - SAM9G15
 - SAM9G25
 - SAM9G35
 - SAM9X25
 - SAM9X35
- 12 MHz crystal
- 32.768 KHz crystal
- 1 Gbit DDR2 memory
- 2 Gits NAND Flash memory with Chip Selection control switch
- 32 Mbits SPI Serial DataFlash with Chip Selection control switch
- 512 Kbits EEPROM
- 1 Kbyte 1-Wire EEPROM
- On-board power regulation
- Two user LEDs
- Optional PHY

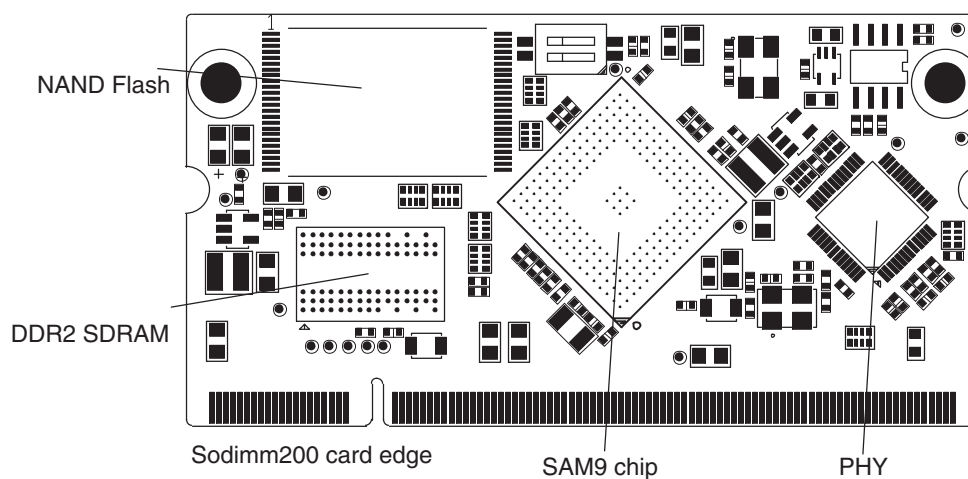
4.2.2.2 Interface Connection

- SODIMM200 card edge interface

4.2.2.3 Configuration Items

- Dual ON/OFF switch for NAND Flash and SPI DataFlash Chip Select connection

Figure 4-2. CM Board Layout Commented



4.2.3 Function Blocks

4.2.3.1 Processor

The CM Board is equipped with an Atmel ARM-based embedded MPU, as listed below, in a 217-ball BGA package. The five devices share an identical footprint. All five share the same CM Board PCB with minor configuration differences.

The five devices are:

- SAM9G15
- SAM9G25
- SAM9G35
- SAM9X25
- SAM9X35

As different interfaces can be defined using the same pins, it depends on the actual configuration of the CPU as to which functions are in fact available to the EK board.

Refer to [Section 4.2.4.1 "Chip Identification"](#) for details. The processor runs at a nominal frequency of 400 MHz for the core and 133 MHz for the system bus.

The peripheral configuration possibilities and implementation requirements of the CM are dependent on the module's chipset. Two configuration resistors are implemented on board in order to select the mode of configuration.

4.2.3.2 Clock Circuitry

The CM includes 3 clock sources:

- Two are alternatives for the processor main clock
- One crystal and one crystal oscillator are used for the Ethernet MII/RMII chip

Table 4-3. Main Components Associated with the Clock Systems

Quantity	Description	Component assignment
1	Crystal for Internal Clock, 12 MHz	Y1
1	Crystal for RTC Clock, 32.768 kHz	Y2
1	Oscillator for Ethernet Clock RMII, 50 MHz	Y3

4.2.3.3 Reset Circuitry

The reset sources for the CM board are:

- Power on reset
- Push button reset (Push button is equipped on EK board)
- JTAG reset from an in-circuit emulator (JTAG interface is equipped on EK board)



4.2.3.4 Power Supplies

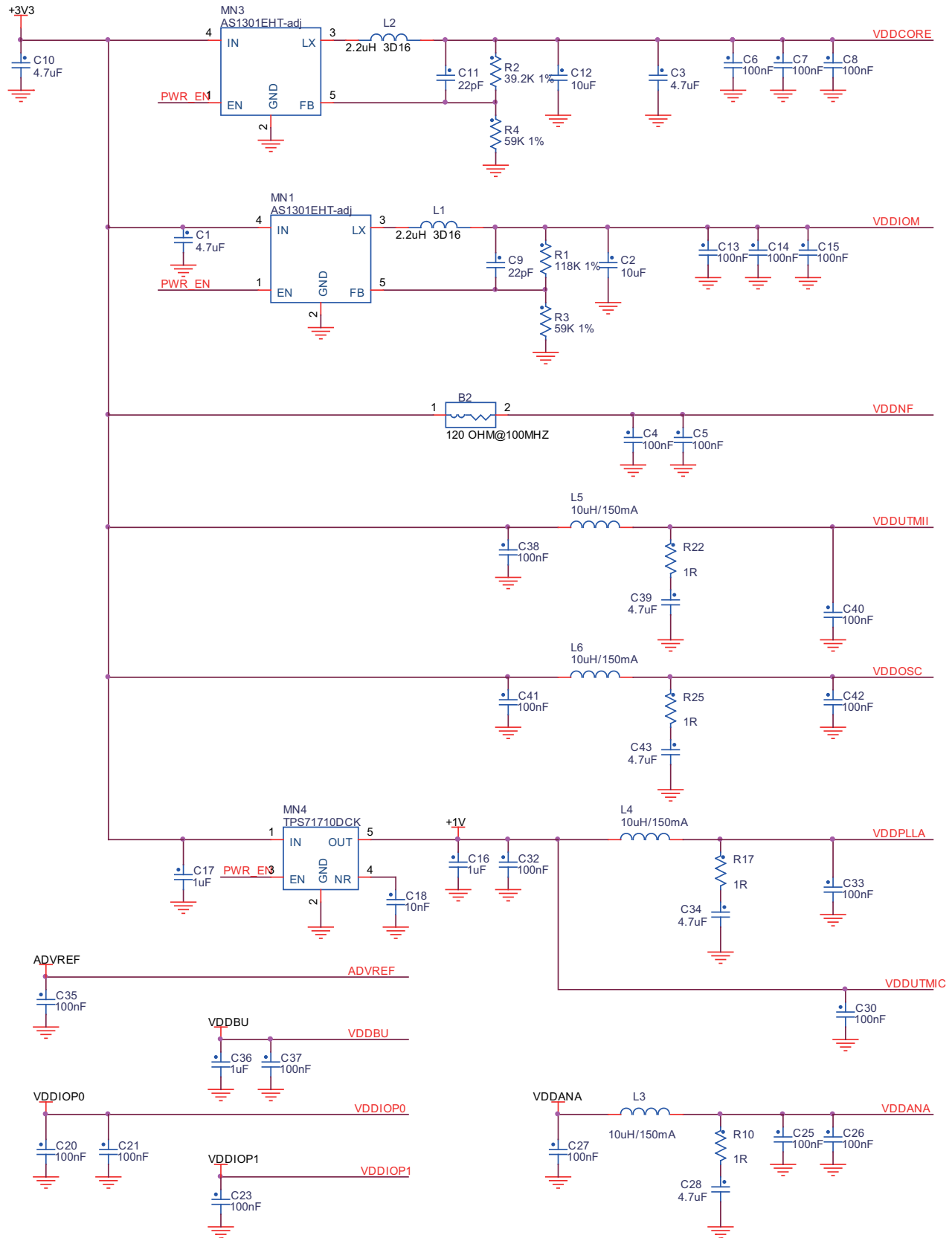
The CM Board is driven by +3V3 input power rail from the EK board through the SODIMM200 connector. The CM Board embeds all the necessary power rails required for the micro processor.

When additional voltages are required, for example VDDCORE, they are generated on board from the 3.3V supply. The detailed power supply requirements for any given module are specified within the corresponding product documentation. The following table summarizes the power specifications.

Table 4-4. Power Rails Associated with the Systems

Nominal	Name	Powers	Component
3.3v	VDDNF	the NAND Flash I/O and control, D16-D32 and multiplexed SMC lines	From SODIMM200 connector
3.3v	VDDIOP0	Partial Peripheral I/O lines	From SODIMM200 connector
3.3v	VDDIOP1	Partial Peripheral I/O lines	From SODIMM200 connector
3.0v	VDDBU	the Slow Clock oscillator, the internal 32 kHz RC, the internal 12 MHz RC and a part of the System Controller	From SODIMM200 connector
3.3v	VDDUTMII	the USB device and host UTMI+ interface	From SODIMM200 connector
3.3v	VDDOSC	the Main Oscillator cells	From SODIMM200 connector
3.3v	VDDANA	the Analog to Digital Converter	From SODIMM200 connector
1.8v	VDDIOM	the External Memory Interface I/O lines	on-board
1.0v	VDDUTMIC	DC Supply UDPHS and UPHPS UTMI+ Core	on-board
3.3v	VDDPLLUTMI	DC Supply UDPHS and UPHPS UTMI+ Interface	From SODIMM200 connector
1.0v	VDDPLLA	the PLLA cell	on-board
1.0v	VDDCORE	the core, including the processor, the embedded memories and the peripherals	on-board
3.0V or 3.3V configurable	ADVREF	ADC Reference voltage	From SODIMM200 connector

Figure 4-3. CM Power Supply



4.2.3.5 Memory

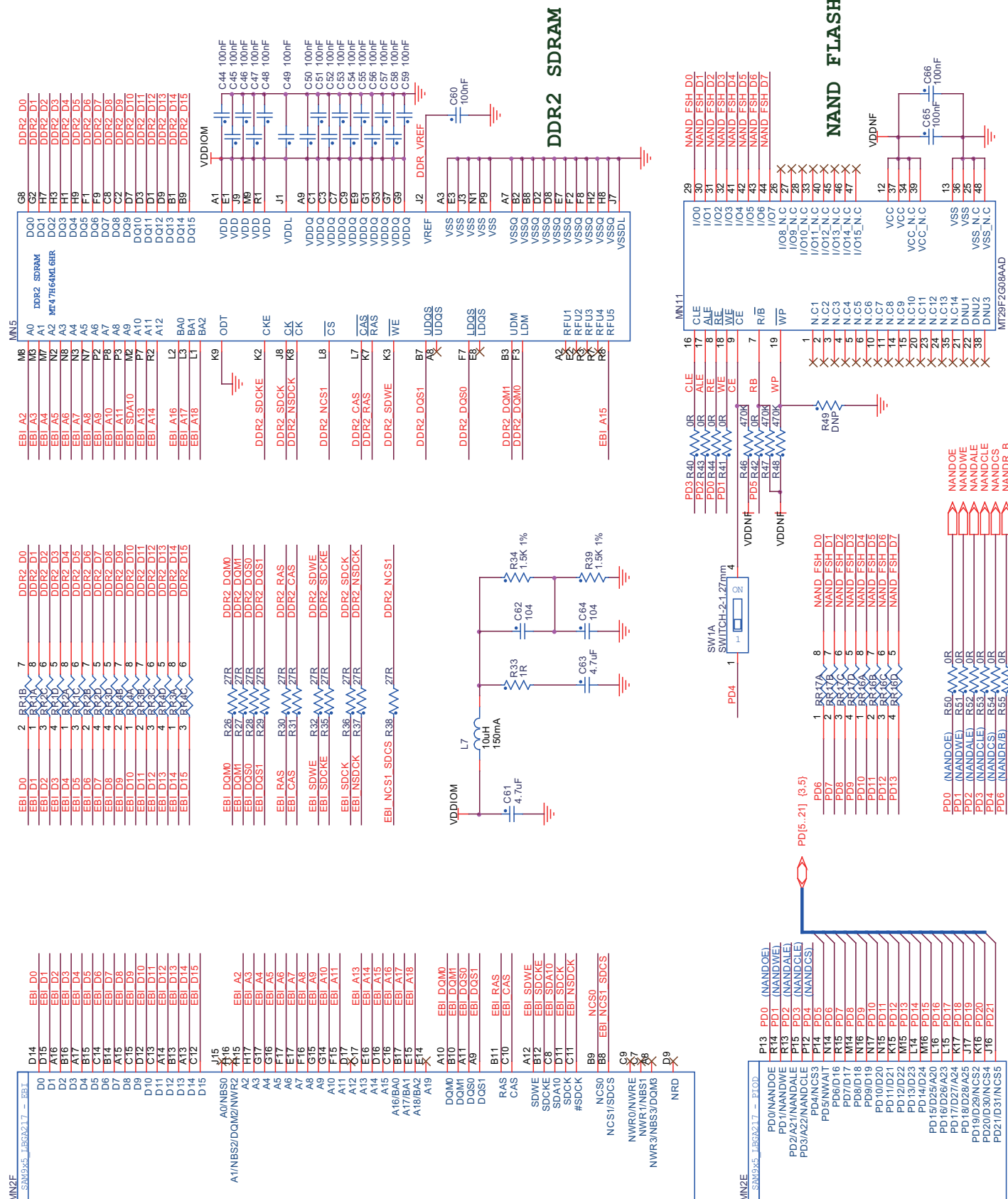
The Device serial processor features a DDR/SDR memory interface and an External Bus Interface (EBI) to enable interfacing to a wide range of external memories and to almost any kind of parallel peripheral.

The External Bus Interface (EBI) is connected to two kinds of memory devices:

- One 1 Gbyte DDR2 SDRAM
- One 2 Gbytes (or 4 Gbytes depending on supplier) NAND Flash



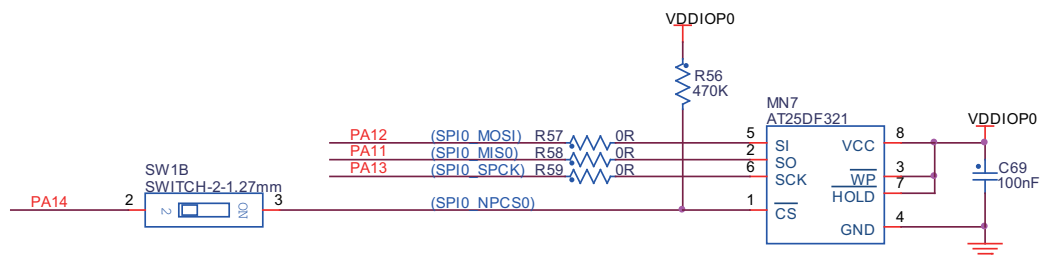
Figure 4-4. CM Board External Memory



4.2.3.6 Serial Peripheral Interface (SPI) Controller

The serial processor provides two high-speed Serial Peripheral Interface (SPI) controllers. One port is used to interface with the on-board serial DataFlash®.

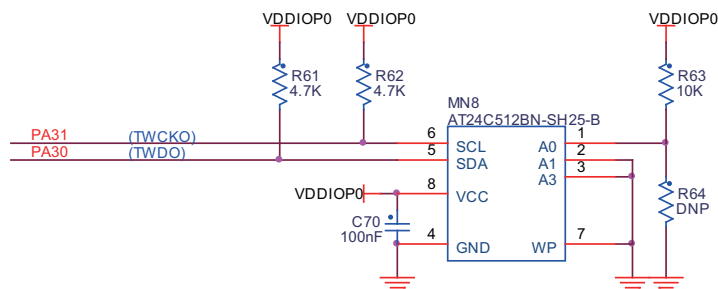
Figure 4-5. SPI



4.2.3.7 Two Wire Interface (TWI)

The serial processor has a full speed (400 kHz) master/slave TWI Serial Controller. The controller is mostly compatible with industry standard I2C and SMBus Interfaces. This port is used to interface with the on-board Serial EEPROM, ISI, QTouch device and audio codec interface.

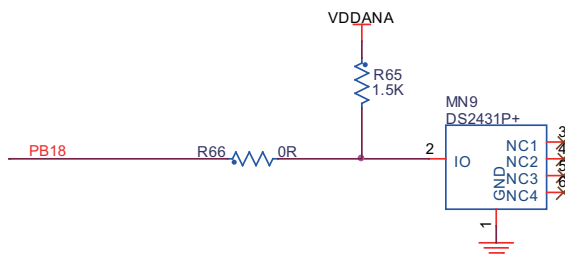
Figure 4-6. TWI



4.2.3.8 1-Wire EEPROM

The Evaluation Kit uses a 1-Wire device as “firmware label” to store the information such as chip type, manufacturer’s name, production date etc.

Figure 4-7. 1-Wire Device

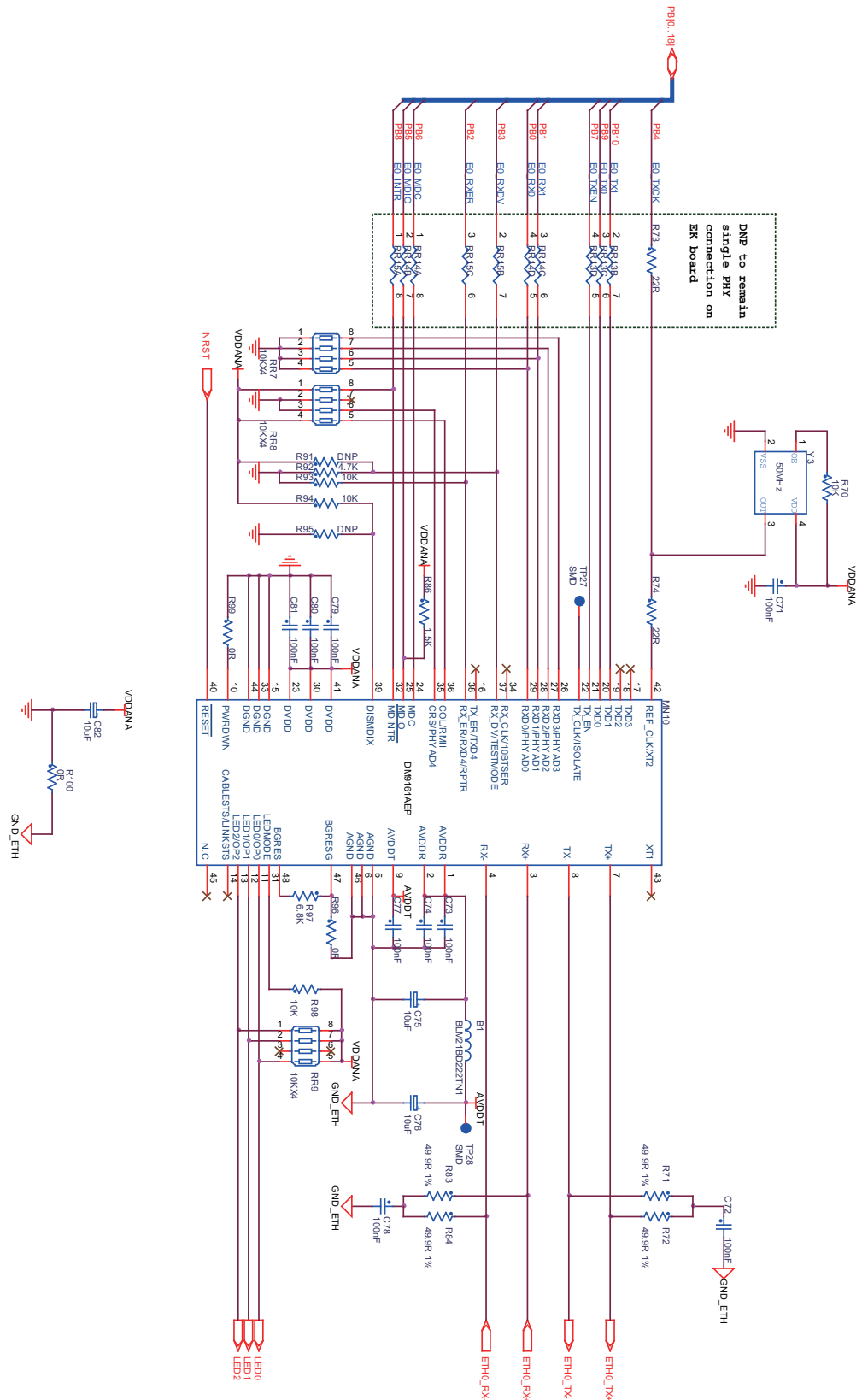


4.2.3.9 Optional PHY

Some of the device modules provide a location for a 10/100 Ethernet MAC/PHY interface.

For more information about the Ethernet controller device, refer to the Davicom DM9161 controller manufacturer's datasheet.

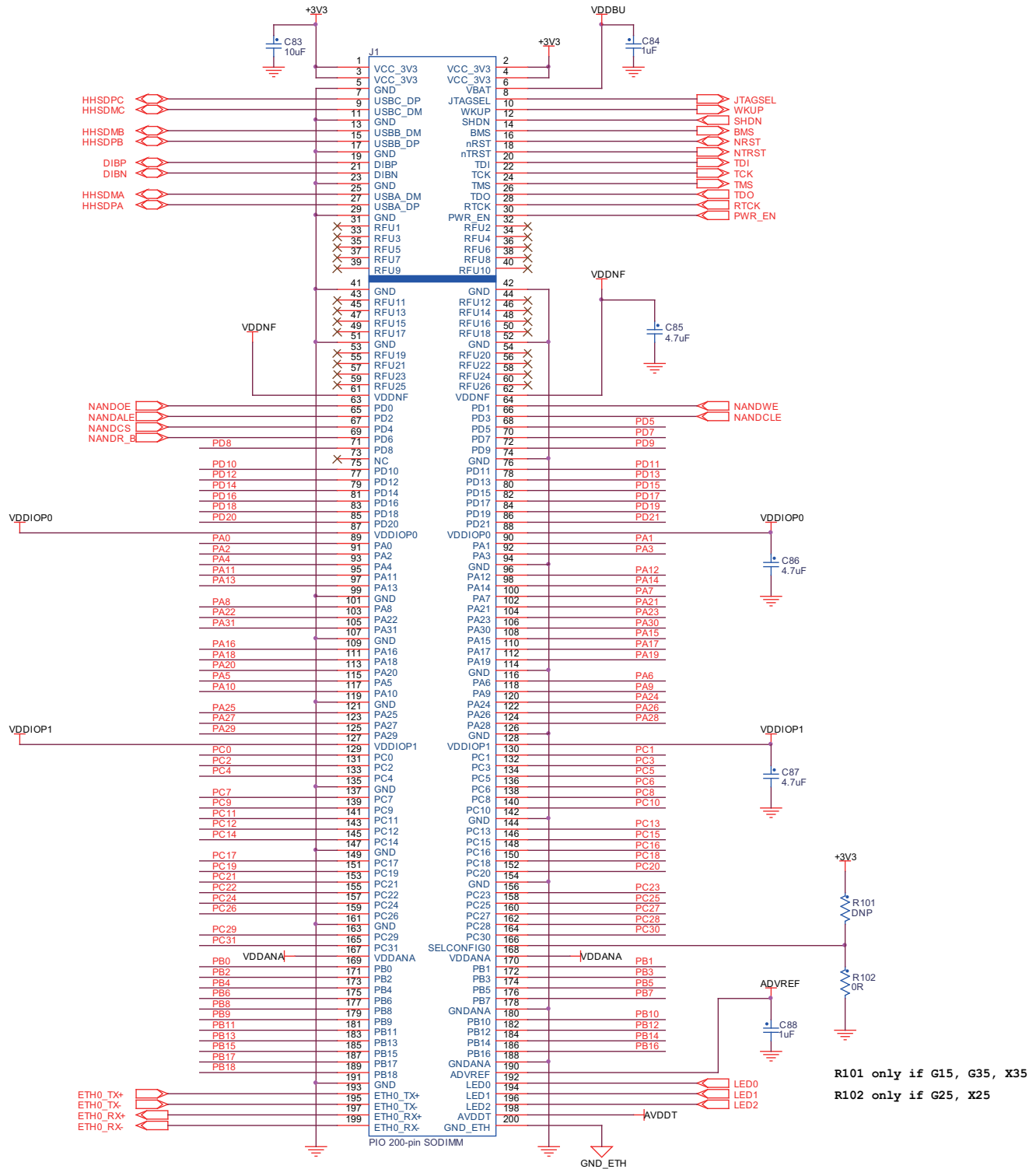
Figure 4-8. Ethernet



4.2.3.10 SODIMM200 Interface

The CM board uses SODIMM200 card edge connector to interface with the EK board.

Figure 4-9. SODIMM200 Interface on CM Board



4.2.4 Configuration

4.2.4.1 Chip Identification

The CM board may be equipped with any of the five processors, all sharing an identical BGA217 footprint. There are two resistors on the CM board for the purpose of identifying which of the five is the one actually mounted.

The tables below show in detail how the CM board, relative to different processors, determines the dedicated “SELCONFIG” logic.

Table 4-5. Resistor Identification

Resistor	SAM9G15	SAM9G25	SAM9G35	SAM9X25	SAM9X35
R49				Populated	Not Populated
R50				Not Populated	Populated
R87	Populated		Populated		
R88	Not Populated		Not Populated		
R101	Populated	Not Populated	Populated	Not Populated	Populated
R102	Not Populated	Populated	Not Populated	Populated	Not Populated

Table 4-6. Module Configuration Identification

		SAM9G15 module	SAM9G35 module	SAM9X35 module	SAM9G25 module	SAM9X25 module
CM Setting	R101	Populated	Populated	Populated	Not Populated	Not Populated
	R102	Not Populated	Not Populated	Not Populated	Populated	Populated
	SELCONFIG (SODIMM200 pin 166)	High	High	High	Low	Low
EK Setting	USART3	Not Selected	Not Selected	Not Selected	Selected	Selected
	ETH1	Not Selected	Not Selected	Not Selected	Not Selected	Selected
DM Setting	LCD	Selected	Selected	Selected	Not Selected	Not Selected

4.2.4.2 Boot Configuration

In order to use SAM-BA boot, the NAND Flash and SPI DataFlash must be deselected. SW1 is dedicated to this purpose.

Table 4-7. Boot Configuration

Designation	Default Setting	Feature
SW1 (1, 4)	ON	Set to OFF disables the NAND flash
SW1 (2, 3)	ON	Set to OFF disables the SPI DataFlash

4.2.5 Connectors

Figure 4-10. CM Board Dimensions

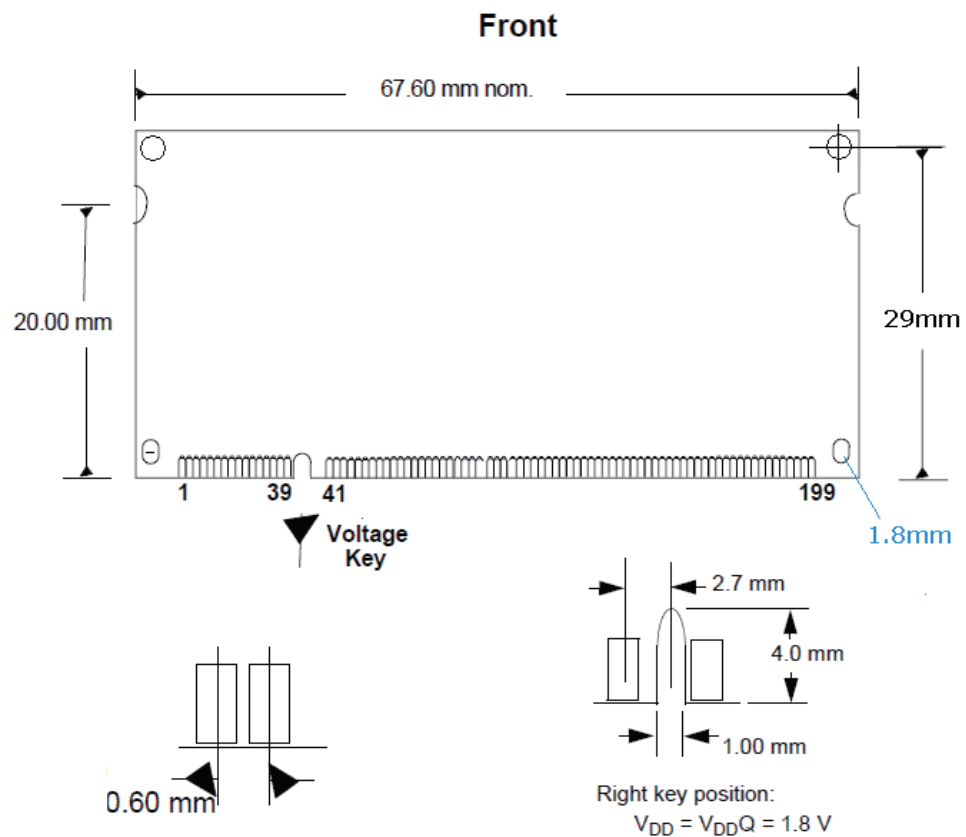
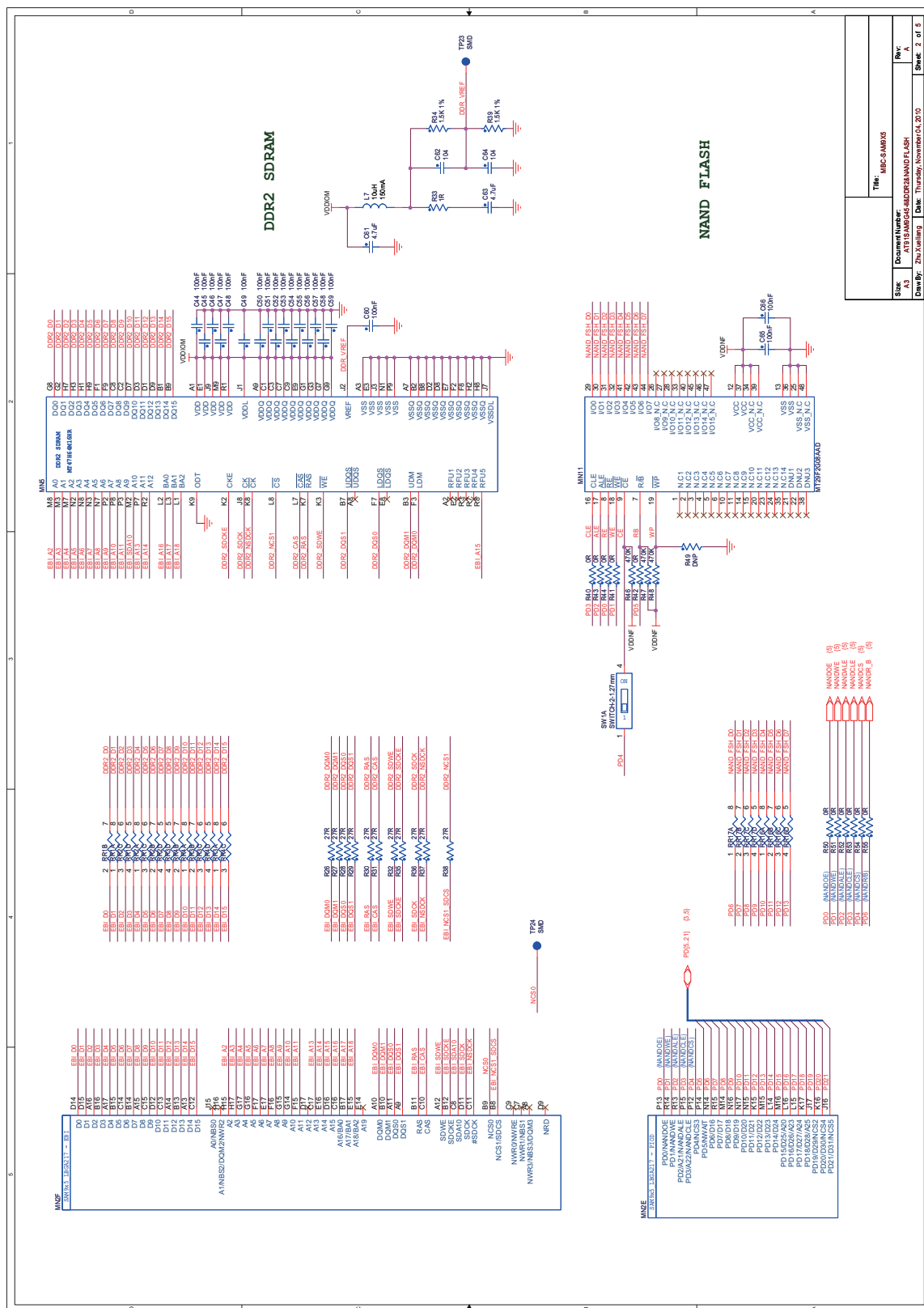


Figure 4-12. CM Board Schematics – 2 of 5



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Figure 4-14. CM Board Schematics – 4 of 5

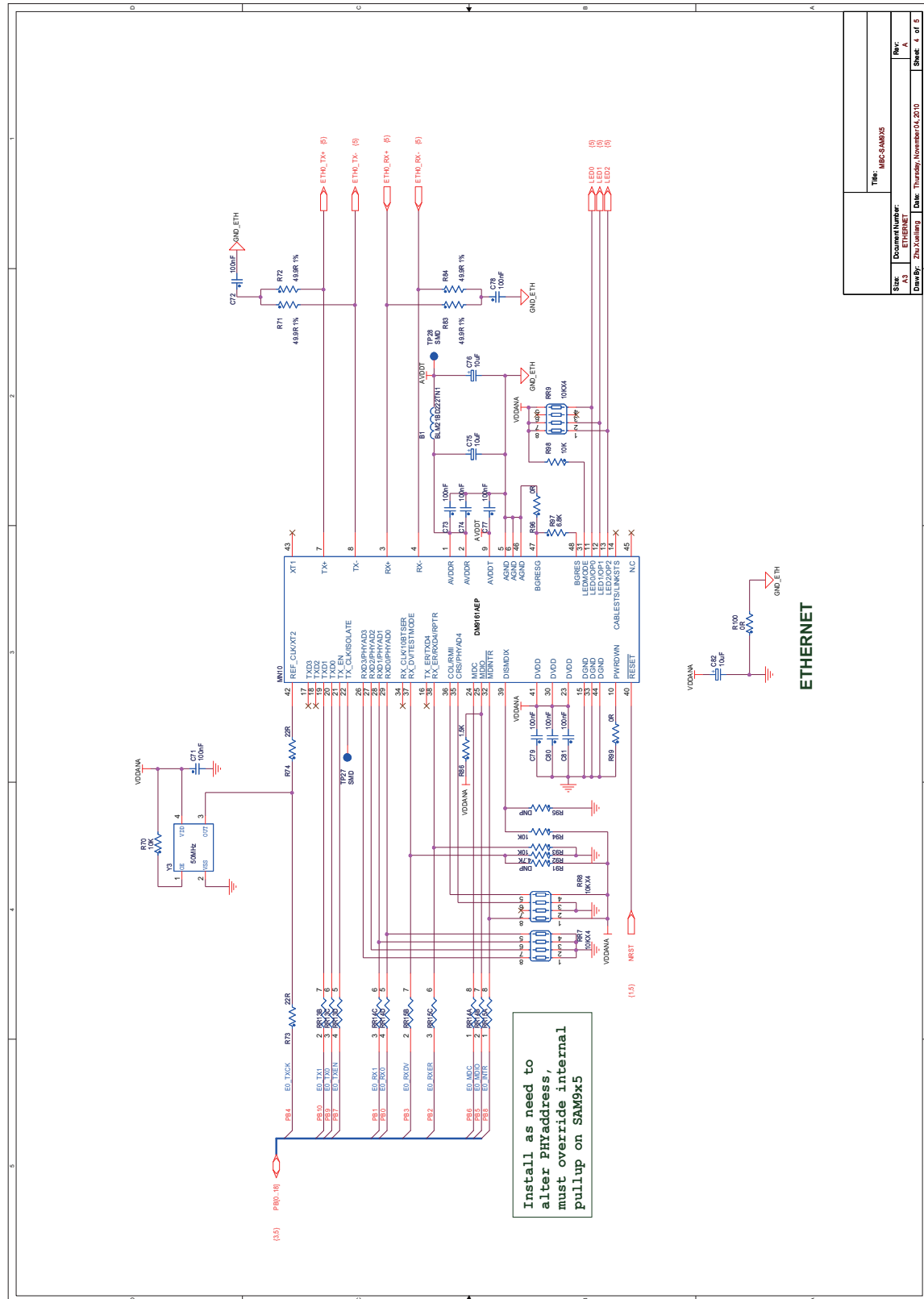
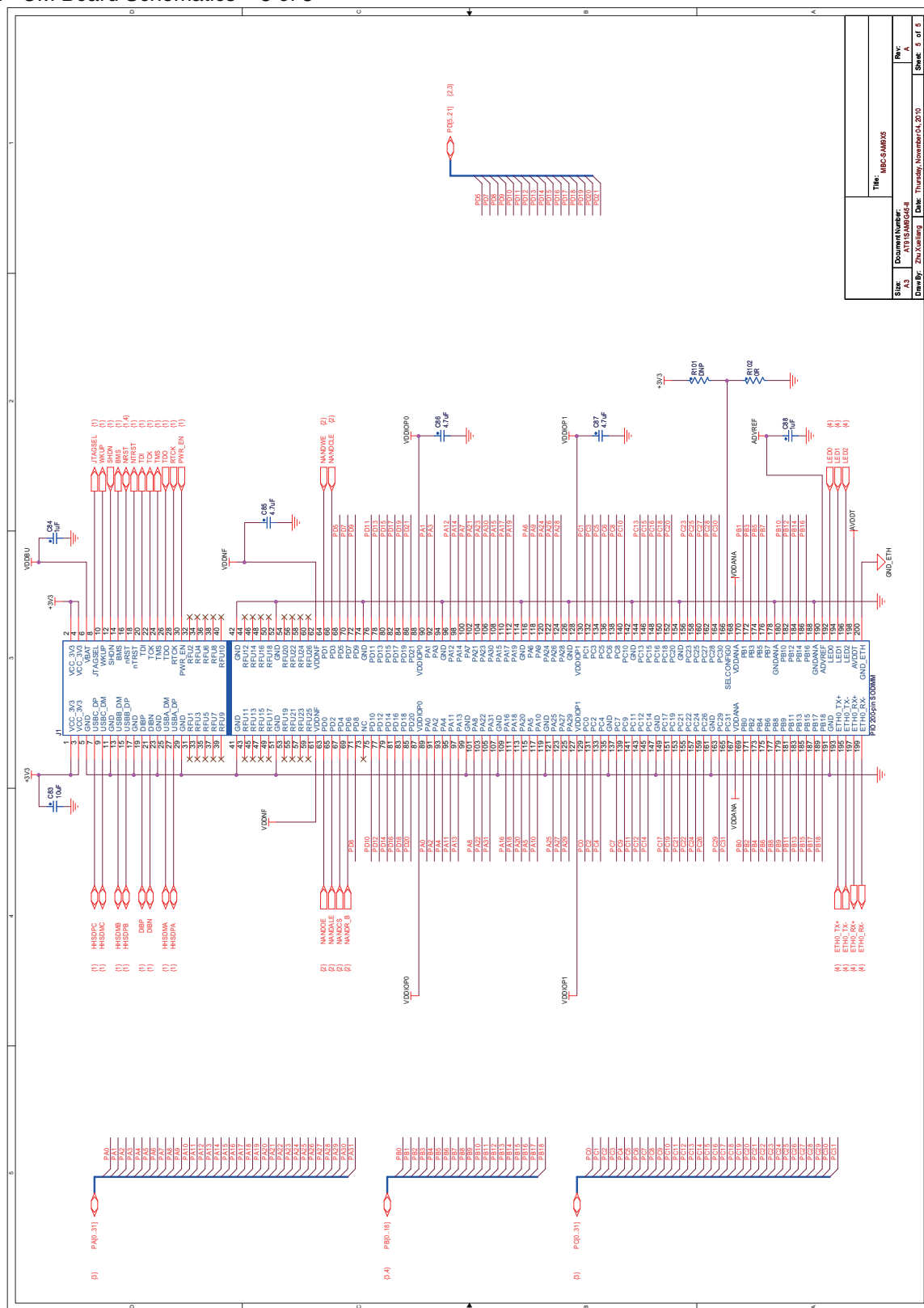


Figure 4-15. CM Board Schematics – 5 of 5

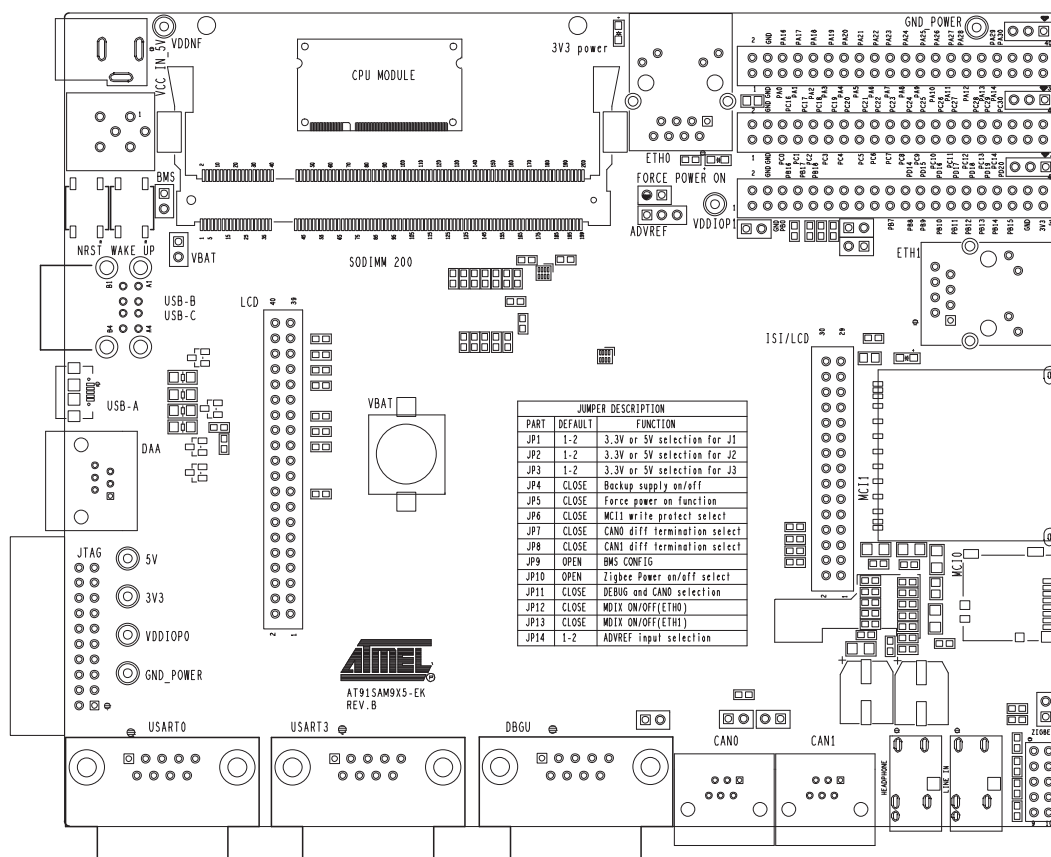


4.3 EK Board Description

4.3.1 EK Board Overview

The EK board serves as the main board that carries the CPU module. It features all necessary peripherals and interfaces for processor evaluation.

Figure 4-16. Commented EK Board Layout



4.3.2 Equipment List

Based on the processor installed on the CM board, the EK board is equipped with the following interfaces or peripherals:

4.3.2.1 Devices

List of the EK board peripherals:

- Two EMAC PHY
- One Audio codec
- Two high speed MCI Card interfaces
- Two CAN transceivers
- Three RS232 ports with level translator features: DBGU, USART0 and USART3
- One Smart DAA port
- Two USB host ports
- One USB host/device port
- On-board power regulation
- LCD/ISI extension interface
- ZigBee® interface
- One-wire device

4.3.2.2 Board Interface Connection

- Main power supply (J4)
- One SODIMM200 socket (CON1)
- USB A Host/Device, support USB host/device using a type micro AB connector (J20)
- USB B Host, support USB host using a type A connector (J19, upper)
- USB C Host, support USB host using a type A connector (J19, lower)
- DBGU (RX and TX only) connected to a 9-way male RS232 connector (J11)
- USART1 (RX, TX, RTS, CTS) connected to a 9-way male RS232 connector (J8)
- USART3 (RX, TX, RTS, CTS) connected to a 9-way male RS232 connector (J12)
- JTAG, 20 pin IDC connector (J9)
- MicroSD connector (J6)
- SD/MMC connector (J7)
- Headphone (J15), line-in (J13)
- Image sensor connector (J21)
- DM board connection for QTouch and TFT LCD display with Touch Screen and backlight (J21, J22)
- DAA connector RJ11 6P4C type (J16)
- CAN bus connectors RJ12 6P6C type (CON2, CON3)
- ZigBee connector (J10)
- Three IO expansion ports (J1, J2, J3)
- Test points, various test points are located throughout the board

4.3.2.3 Push Button Switches

- Reset, board reset (BP1)
- Wake up, push button to bring the processor out of low power mode (BP2)



4.3.3 Function Blocks

4.3.3.1 Processor

The Evaluation Kit board may be used with any of the Core Modules:

- SAM9G15
- SAM9G25
- SAM9G35
- SAM9X25
- SAM9X35

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4.3.3.2 Power Supplies

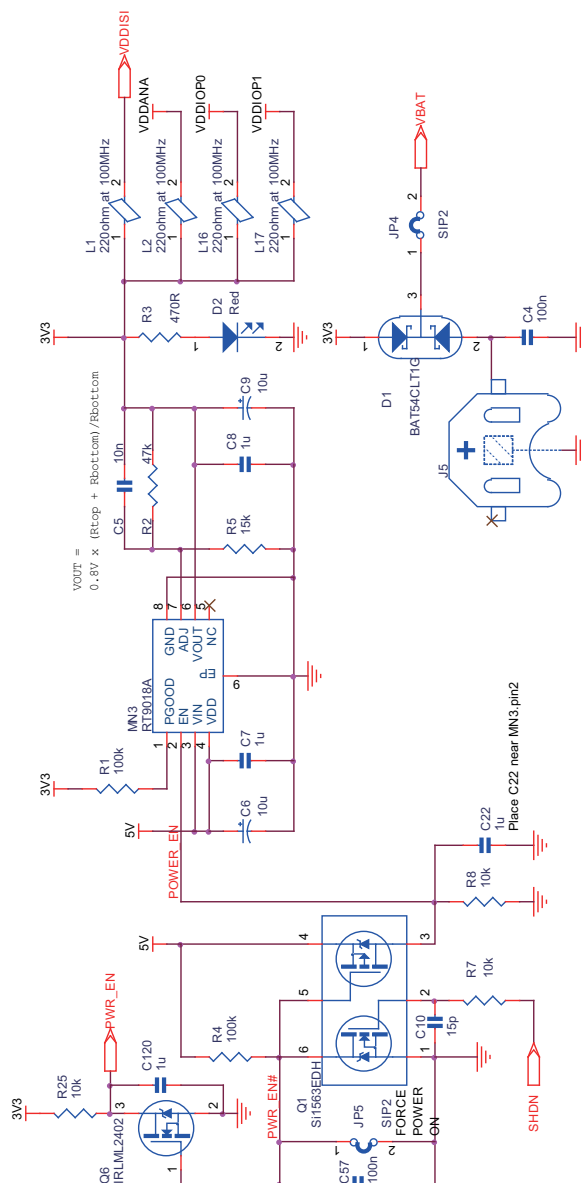
The EK Board features one adjustable LDO. It accepts DC 5V power input and outputs a regulated +3.3V to most other circuits on the board through four 3.3V rails.

- VDDPIO0
- VDDPIO1
- VDDANA
- VDDISI

This LDO is enabled through a dual FET scheme. The processor can assert SHDN (which is a VDDBU-powered I/O) to shut down the LDO to enter the so-called backup mode. The regulator on CM board is also shut down by the action of the SHDN signal.

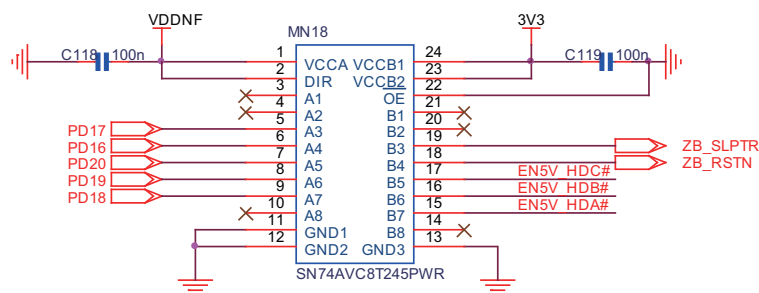
If the 3V battery is mounted on J5, both CM and EK boards can be woken up by action on the BP2 button, which drives the WKUP signal (also a VDDBU-powered I/O).

Figure 4-18. EK Board Power Management



There is another 3.3V rail, VDDNF, supplied from the CM board. VDDNF is set to 3.3V in the current CM design. The processors also support a 1.8V NAND Flash device, in which case VDDNF is set to 1.8V. In order to avoid potential voltage conflict on user-defined applications, a level shifter is inserted between the PIO lines on VDDNF rail and the 3.3V application.

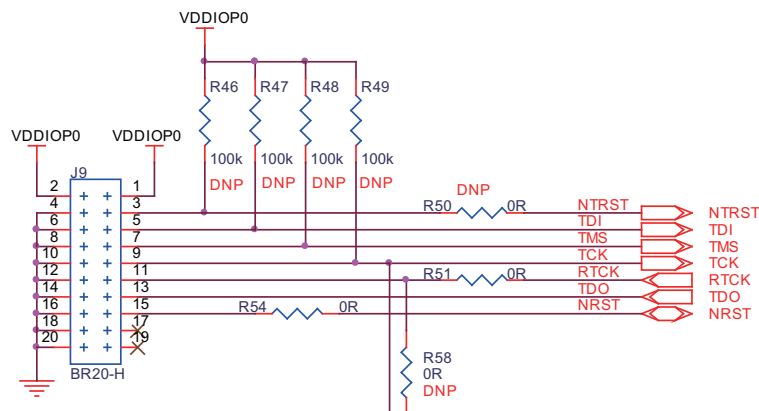
Figure 4-19. Level Shifter For VDDNF Rail



4.3.3.3 JTAG/ICE

Software debug is accessed by a standard 20-pin JTAG connection. This allows connection to a standard USB-to-JTAG in-circuit emulator such as SAM-ICE™.

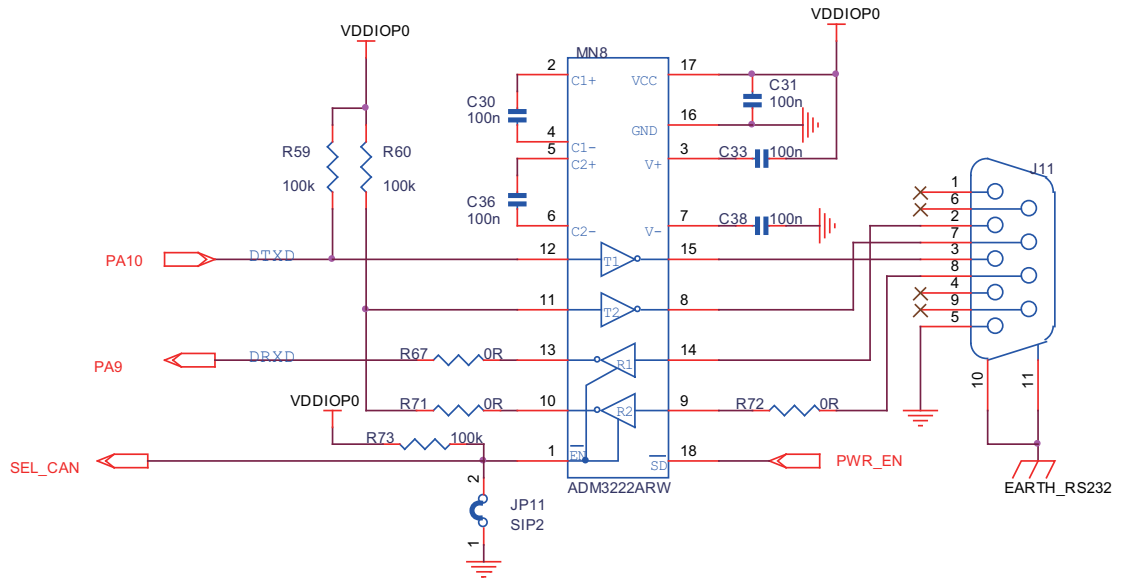
Figure 4-20. JTAG Interface



4.3.3.4 DBGU

The UART is connected to the DB-9 male socket through an RS-232 Transceiver (TXD and RXD only). A jumper, JP11, is used to select DBGU or CAN0 between IO (PA9, PA10) sharing scheme. Close JP11 to select DBGU.

Figure 4-21. DBGU Com Port

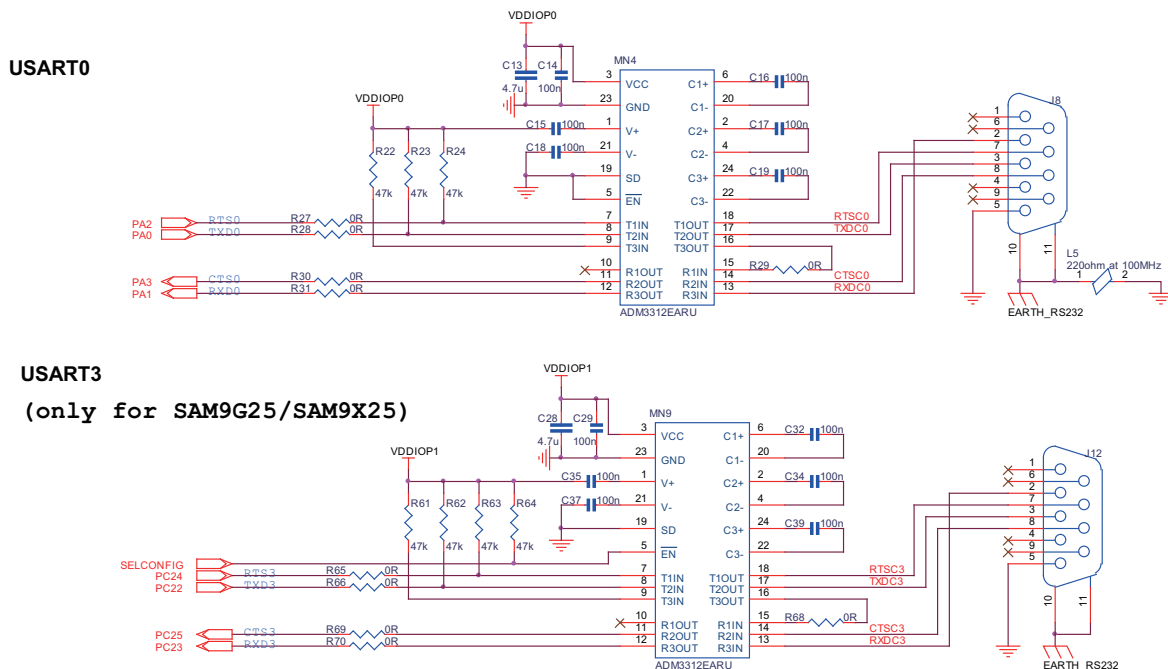


4.3.3.5 USART

The USART0 and USART3 are used as serial communication ports. Both USARTs are buffered with an RS-232 Transceiver (TXD, RXD and handshake CTS/RTS control) and connected to the DB-9 male socket. The software must assign the appropriate PIO pins to enable the USART function.

The USART3 is only supported by SAM9G25 and SAM9X25 processors. The RS-232 Transceiver for USART3 is enabled by the signal SELCONFIG comprised of a pull down resistor on CM board. Ref to [Section 4.4.1 "DM Board Overview"](#) for details.

Figure 4-22. USART Com Port



4.3.3.6 USB Ports

The Evaluation Kit features three USB communication ports:

- Port A Host High Speed (EHCI) and Full Speed (OHCI) multiplexed with USB Device High speed Micro AB connector, J20
- Port B Host High Speed (EHCI) and Full Speed (OHCI) standard type A connector, J19 upper port
- Port C Host Full speed OHCI only standard type A connector, J19 lower port

All three USB Host ports are equipped with 500 mA high-side power switch for self-powered and bus-powered applications. The USB device port features VBUS insert detection function through the resistor ladder R138 and R139.

Refer to the embedded MPU product datasheet for detailed programming information. For datasheet reference numbers and titles, see [Section 1.2 "Applicable Documents"](#).

Figure 4-23. USB Port (A)

USB A HOST/DEVICE INTERFACE

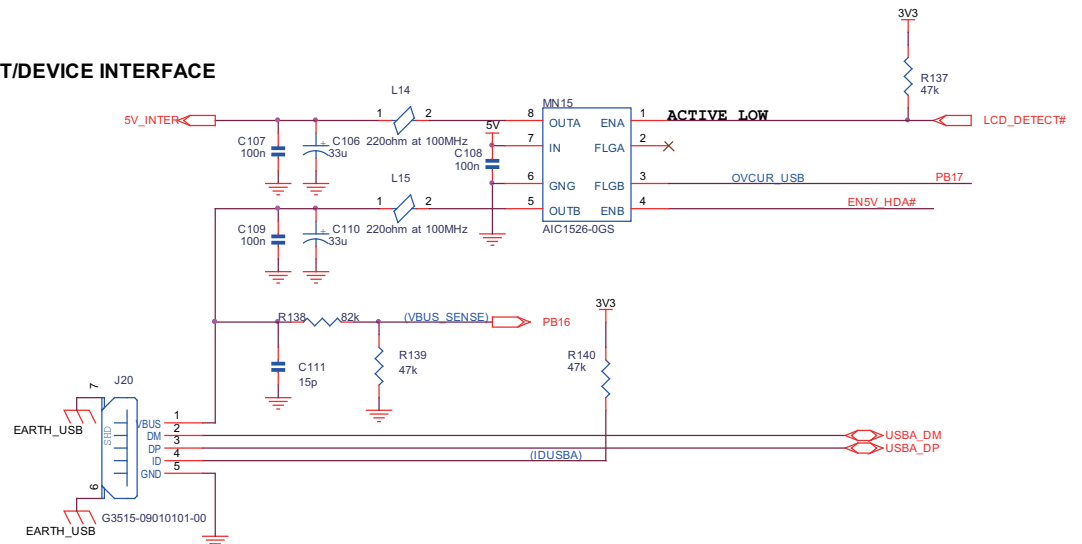
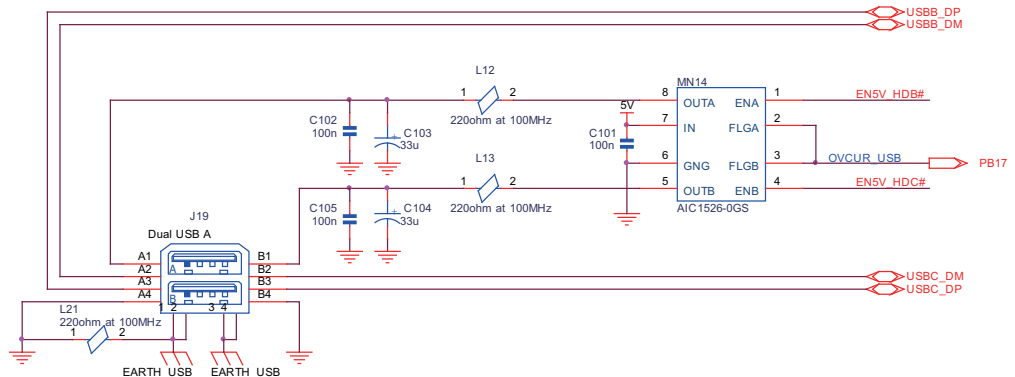


Figure 4-24. USB Port (B & C)

USB HOST B&C INTERFACE



4.3.3.7 Ethernet 10/100 (EMAC) Port

Except for SAM9G15, the processor has two 10/100 Mbps Ethernet Mac Controllers.

	SAM9G15	SAM9G35	SAM9X35	SAM9G25	SAM9X25
EMAC	–	RMII	RMII	MII	MII + RMII

The EK board is equipped with two Davicom DM9161AEP PHYs for each Ethernet port. Both PHY Transceivers are configured as RMII mode. Both PHY transceivers have an RJ45 port with embedded transformer and three-status LEDs.

By default, the ETH0 interface is implemented on the EK board. Additionally, for monitoring and control purposes, an LED functionality is carried on the RJ45 connectors to indicate activity, link, and speed status information for the respective ports.

11115A-ATARM-27-Jul-11

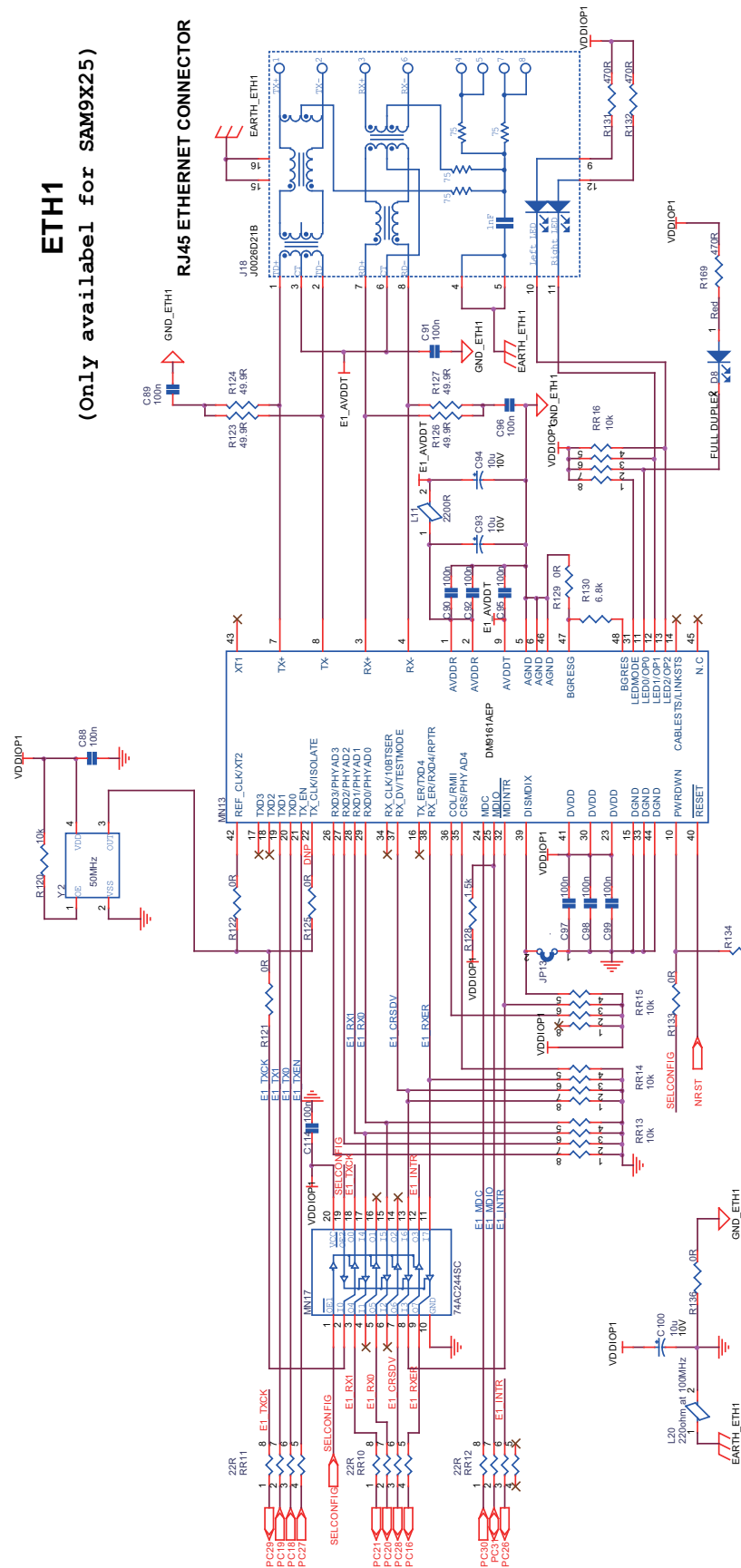


Ethernet 1 is only available for SAM9X25. The PHY on Ethernet 1 is enabled by the SELCONFIG signal from a pull-down resistor on the CM board. Refer to [Section 4.4.1 "DM Board Overview"](#) for detail.

Some pins (PC16, PC20, PC21, PC28, PC26 and PC29) are configured as Ethernet 1 input from PHY for SAM9X25, whereas they are configured as LCD data pins on other processors. An IO buffer MN17 is inserted in series with these signals to prevent the LCD from being disturbed by unknown status of the PHY device.



4-33

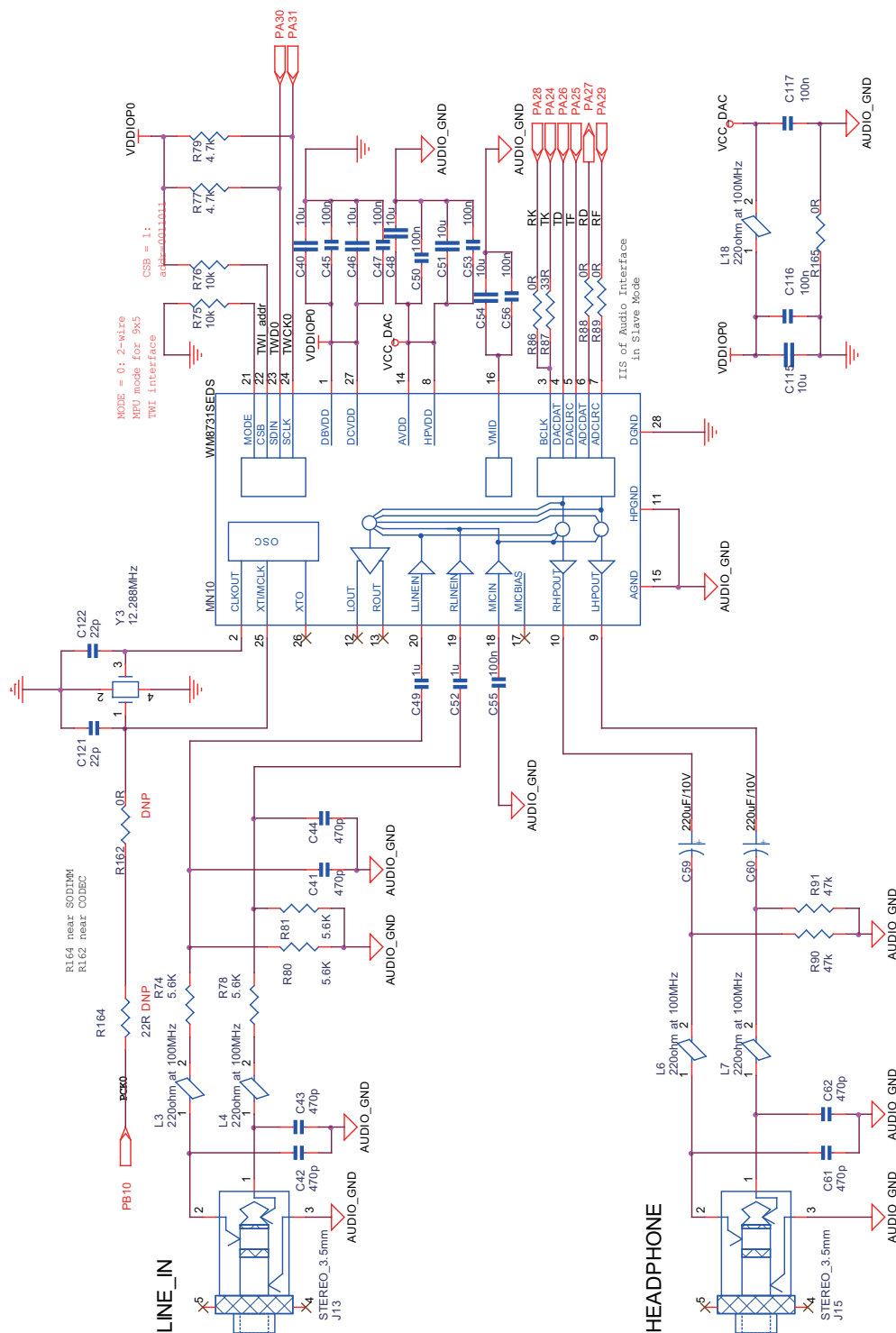


4.3.3.8 Audio

The Evaluation Kit includes a WM8731 CODEC for digital sound input and output. This interface includes audio jacks for line audio input (J13) and headphone line output (J15).

The SAM9 processor is configured in IIS slave mode to interface with the WM8731 Codec. The IIS master mode is also possible for evaluation by populating R162/R164 and removing the crystal Y3.

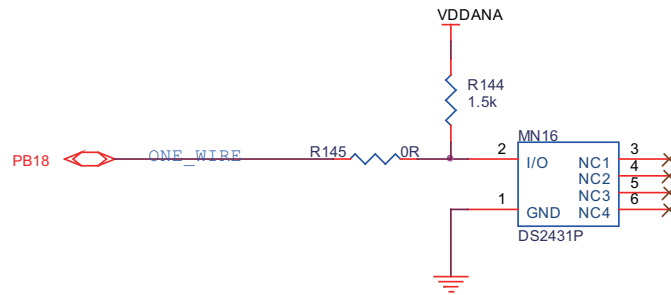
Figure 4-27. Audio Interface



4.3.3.9 1-Wire EEPROM

The EK board also features a 1-Wire device acting as a “firmware label” to store information like chip type, manufacturer’s name, production date etc.

Figure 4-28. 1-Wire on EK



4.3.3.10 CAN Bus

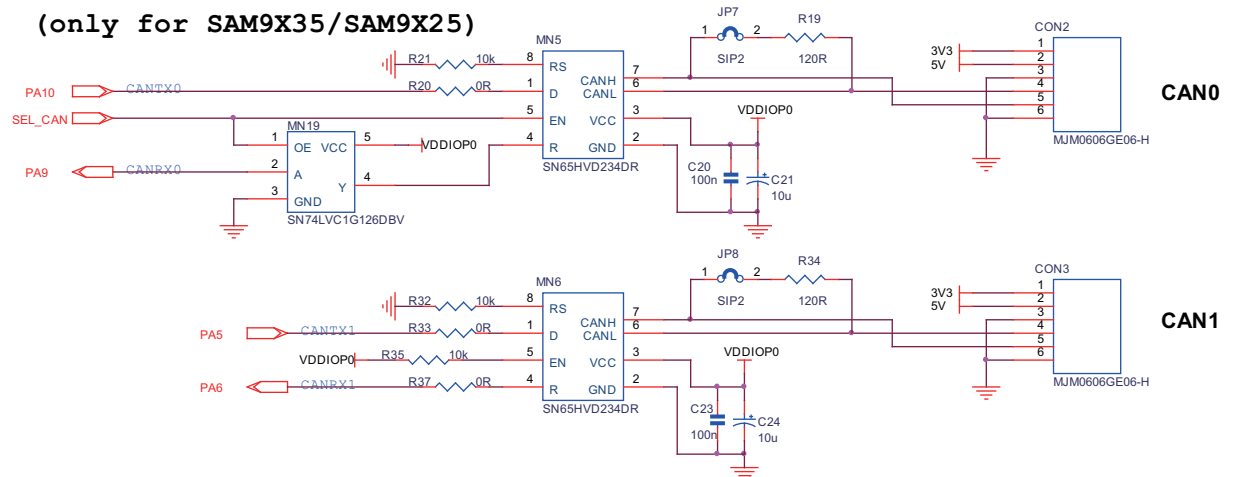
Two boards, the SAM9X35-EK and SAM9X25-EK, feature two Controller Area Network (CAN) ports with transceiver.

CAN0 uses the same IOs (PA9, PA10) as the DBGU. A jumper, JP11, is used to select either of the interfaces.

- Open JP11 to select the CAN function.
- Close JP11 to select the DBGU function.

A 3-state output buffer, MN19 is inserted in series with the output channels of the CAN transceiver, which share IOs with the DBGU. This is necessary because the CAN transceiver does not feature 3-state outputs.

Figure 4-29. CAN on EK



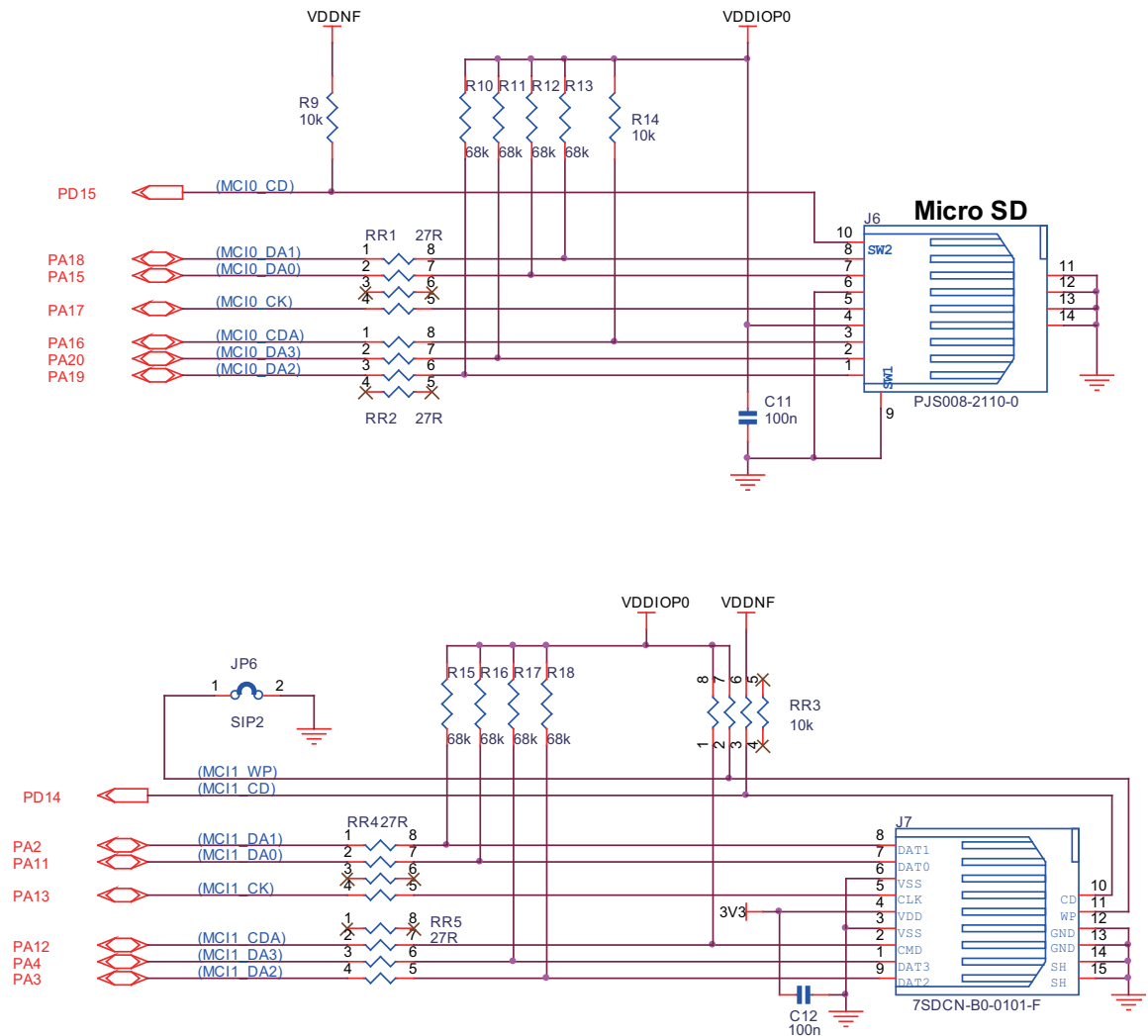
4.3.3.12 SD/MMC Interface

The Evaluation Kit has two high-speed MultiMedia Card Interfaces (MCI). The first interface is used as a 4-bit interface (MCI0), connected to a MicroSD card slot. The second interface is used as a 4-bit interface (MCI1), connected to an SD/MMC card slot.

The memory card is not included in the Evaluation Kit.

Please note that the power is connected to VCC, which is 3.3 volts.

Figure 4-31. SD/MMC Interface

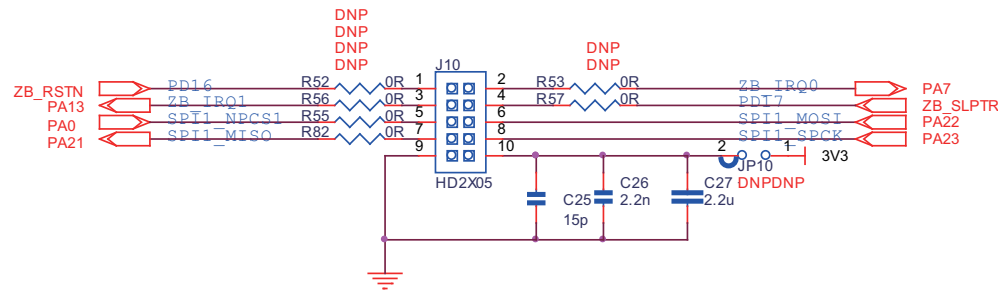


4.3.3.13 ZigBee

The EK board has a 10-pin male connector for the Atmel RZ600 ZigBee module.

DNP 0 Ohm resistors have been implemented in series with the PIO lines that are used elsewhere in the design. Thereby, enable their individual disconnections, should a conflict occur in user application.

Figure 4-32. ZigBee Interface



4.3.3.14 LED Indicators

The EK board has three LED indicators for purposes shown below:

Table 4-8. LED Indicators

Reference	Color	Function
D2	Red	3v3 Power indicator
D7	Red	ETH0 Full Duplex
D8	Red	ETH1 Full Duplex

Refer to [Section 4.3.3.2 "Power Supplies"](#) and [Section 4.3.3.7 "Ethernet 10/100 \(EMAC\) Port"](#) for details.

4.3.3.15 Expansion Ports

Most GPIOs are routed to expansion ports J1, J2, J3.

All I/Os of the MPU Image Sensor Interface (ISI) are routed to connectors J21, J22.

The LCD and touch screen interfaces are routed to connectors J21, J22.

Figure 4-33. I/O Expansion

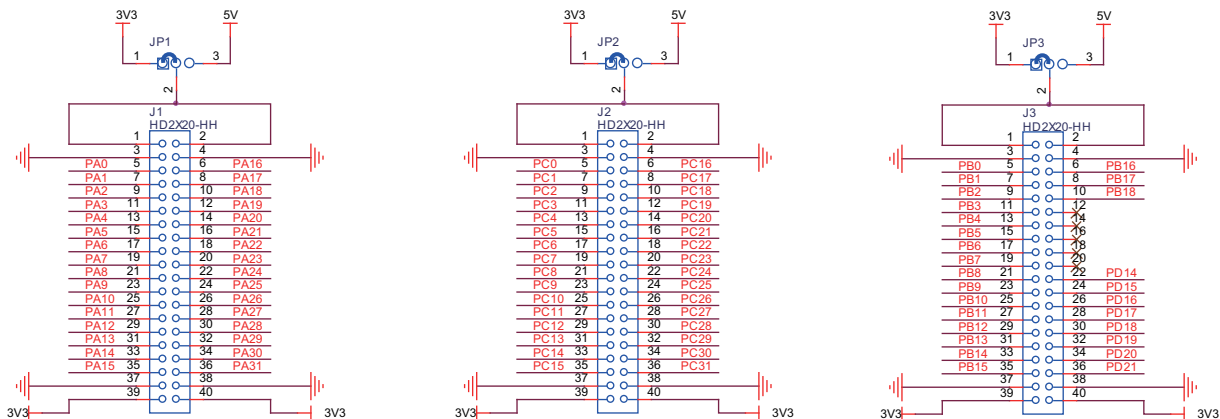
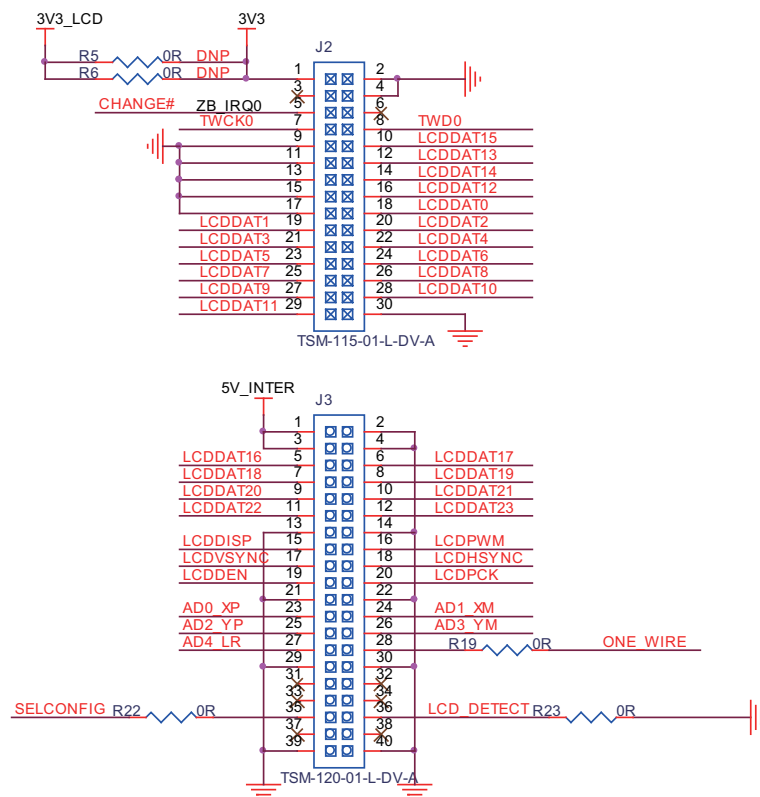


Figure 4-34. LCD and ISI Expansion

4.3.4 Configuration

This section describes the PIO usage, the jumpers, the test points and the solder drops of the EK board.

4.3.4.1 JTAG/ICE Configuration

Table 4-9. JTAG/ICE

Designation	Default Setting	Feature
R50	Not Populated	Disables the ICE NTRST input
R51	Populated	Enables the ICE RTCK return. R94 must be opened
R54	Populated	Enables the ICE NRST input
R58	Not Populated	Disables TCK <-> RTCK local loop

4.3.4.2 Boot Mode Select Configuration

Table 4-10. BMS

Designation	Default Setting	Feature
JP9	Open	Default to boot on embedded ROM Close to boot on external memory

4.3.4.3 Force Power ON Configuration

Table 4-11. Force Power ON

Designation	Default Setting	Feature
JP5	Close	Keep on-board regulator always on Open to feature SHDN function

4.3.4.4 White Protection Configuration on MCI1

Table 4-12. Write Protection on MCI1

Designation	Default Setting	Feature
JP6	Close	MCI1 write protect selected Open to disable protection

4.3.4.5 Selection between DBGU and CAN

Table 4-13. Select DBGU or CAN

Designation	Default Setting	Feature
JP11	Close	Default to select DBGU Open to select CAN0

4.3.4.6 Codec IIS Configuration

Table 4-14. Codec IIS

Designation	Default Setting	Feature
R162, R164	Not Populated	IIS master mode, clock the codec by PCK0
C121, C122, Y3	Populated	IIS slaver mode, clock the codec by external crystal

4.3.4.7 ETH0 Configuration

Table 4-15. ETH0

Designation	Default Setting	Feature
R180, R183, R184, R185, R185, R186, R174, R175, R176	Not Populated	Populated to select the PHY channel on CM. Channel on EK must be cut if select the PHY on CM.
R177, R178, R179, R181, R182, R218, R171, R172, R173	Populated	Selection of PHY channel on EK

4.3.4.8 PIO Usage

Table 4-16. PIO A Pin Assignment and Signal Description

Signal	Alternate	Periph A	Periph B	Periph C		
PA0		TXD0	SPI1_NPCS1		TXD0/ ZigBee	USART0 shared with ZigBee
PA1		RXD0	SPI0_NPCS2		RXD0	USART0
PA2		RTS0	MCI1_DA1	E0_ETX0	RTS0/ MCI1	USART0 shared with MCI1
PA3		CTS0	MCI1_DA2	E0_ETX1	CTS0/ MCI1	USART0 shared with MCI1
PA4		SCK0	MCI1_DA3	E0_ETXER	MCI1	
PA5		TXD1	CANTX1		CAN1	
PA6		RXD1	CANRX1		CAN1	
PA7		TXD2	SPI0_NPCS1			ZB_IRQ0
PA8		RXD2	SPI1_NPCS0			DataFlash
PA9			CANRX0		DBGU+CAN0	DBGU shared with CAN0
PA10			CANTX0		DBGU+CAN0	DBGU shared with CAN0
PA11		SPI0_MISO	MCI1_DA0		MCI1	
PA12		SPI0_MOSI	MCI1_CDA		MCI1	
PA13		SPI0_SPCK	MCI1_CK		MCI1	ZB_IRQ1
PA14		SPI0_NPCS0				MCI0
PA15		MCI0_DA0				MCI0
PA16		MCI0_CDA				MCI0
PA17		MCI0_CK				MCI0
PA18		MCI0_DA1				MCI0
PA19		MCI0_DA2				MCI0
PA20		MCI0_DA3				DataFlash
PA21		TIOA0	SPI1_MISO		ZigBee	DataFlash
PA22		TIOA1	SPI1_MOSI		ZigBee	DataFlash
PA23		TIOA2	SPI1_SPCK		ZigBee	SSC
PA24		TCLK0	TK			SSC
PA25		TCLK1	TF			SSC
PA26		TCLK2	TD			SSC
PA27		TIOB0	RD			SSC
PA28		TIOB1	RK			SSC
PA29		TIOB2	RF			SSC
PA30		TWD0	SPI1_NPCS3	E0_EMDC		TWD0
PA31		TWCK0	SPI1_NPCS2	E0_ETXEN		TWCK0



Table 4-17. PIO B Pin Assignment and Signal Description

Signal	Alternate	Periph A	Periph B	Periph C	Module CM	EK
PB0		E0_RX0	RTS2		ETH0	
PB1		E0_RX1	CTS2		ETH0	
PB2		E0_RXER	SCK2		ETH0	
PB3		E0_RXDV	SPI0_NPCS3		ETH0	
PB4		E0_TXCK	TWD2		ETH0	
PB5		E0_MDIO	TWCK2		ETH0	
PB6	AD7	E0_MDC			ETH0	
PB7	AD8	E0_TXEN			ETH0	
PB8	AD9	E0_TXER			ETH0_INTR	
PB9	AD10	E0_TX0	PCK1		ETH0	
PB10	AD11	E0_TX1	PCK0		ETH0	
PB11	AD0xp	E0_TX2	PWM0			TSC
PB12	AD1xm	E0_TX3	PWM1			TSC
PB13	AD2yp	E0_RX2	PWM2			TSC
PB14	AD3ym	E0_RX3	PWM3			TSC
PB15	AD4lr	E0_RXCK				TSC
PB16	AD5	E0_CRS				VBUS_SENSE (USB)
PB17	AD6	E0_COL				OVCUR_USB (Open drain)
PB18		IRQ	ADTRG		USER_LED1#	ONE_WIRE

Table 4-18. PIO C Pin Assignment and Signal Description

Signal	Alternate	Periph A	Periph B	Periph C		EK f (LCD)	EK f (ISI+IO)
PC0		LCDDAT0	ISI_D0	TWD1		LCDDAT0	ISI_D0
PC1		LCDDAT1	ISI_D1	TWCK1		LCDDAT1	ISI_D1
PC2		LCDDAT2	ISI_D2	TIOA3		LCDDAT2	ISI_D2
PC3		LCDDAT3	ISI_D3	TIOB3		LCDDAT3	ISI_D3
PC4		LCDDAT4	ISI_D4	TCLK3		LCDDAT4	ISI_D4
PC5		LCDDAT5	ISI_D5	TIOA4		LCDDAT5	ISI_D5
PC6		LCDDAT6	ISI_D6	TIOB4		LCDDAT6	ISI_D6
PC7		LCDDAT7	ISI_D7	TCLK4		LCDDAT7	ISI_D7
PC8		LCDDAT8	ISI_D8	UTXD0		LCDDAT8	ISI_D8
PC9		LCDDAT9	ISI_D9	URXD0		LCDDAT9	ISI_D9
PC10		LCDDAT10	ISI_D10	PWM0		LCDDAT10	ISI_D10
PC11		LCDDAT11	ISI_D11	PWM1		LCDDAT11	ISI_D11
PC12		LCDDAT12	ISI_PCK	TIOA5		LCDDAT12	ISI_PCK
PC13		LCDDAT13	ISI_VSYNC	TIOB5		LCDDAT13	ISI_VSYNC
PC14		LCDDAT14	ISI_HSYNC	TCLK5		LCDDAT14	ISI_HSYNC
PC15		LCDDAT15	ISI_MCK	PCK0	SSC	LCDDAT15	ISI_MCK/PCK0
PC16		LCDDAT16	E1_RXER	UTXD1		LCDDAT16	E1_RXER
PC17		LCDDAT17		URXD1		LCDDAT17	
PC18		LCDDAT18	E1_TX0	PWM0		LCDDAT18	E1_TX0
PC19		LCDDAT19	E1_TX1	PWM1		LCDDAT19	E1_TX1
PC20		LCDDAT20	E1_RX0	PWM2		LCDDAT20	E1_RX0
PC21		LCDDAT21	E1_RX1	PWM3		LCDDAT21	E1_RX1
PC22		LCDDAT22	TXD3			LCDDAT22	TXD3
PC23		LCDDAT23	RXD3			LCDDAT23	RXD3
PC24		LCDDISP	RTS3			LCDDISP	RTS3
PC25			CTS3				CTS3
PC26		LCDPWM	SCK3		Eth1_Intr1	LCDPWM	
PC27		LCDVSYNC	E1_TXEN	RTS1		LCDVSYNC	E1_TXEN
PC28		LCDHSYNC	E1_CRSDV	CTS1		LCDHSYNC	E1_CRSDV
PC29		LCDDEN	E1_TXCK	SCK1		LCDDEN	E1_TXCK
PC30		LCDPCK	E1_MDC			LCDPCK	E1_MDC
PC31		FIQ	E1_MDIO	PCK1			E1_MDIO



Table 4-19. PIO D Pin Assignment and Signal Description

Signal	Alternate	Periph A	Periph B	Periph C	Module CM	EK
PD0		NANDOE			Nand Flash	
PD1		NANDWE			Nand Flash	
PD2		A21/NANDALE			Nand Flash	
PD3		A22/NANDCLE			Nand Flash	
PD4		NCS3			CS NAND Flash	
PD5		NWAIT			NAND_RD/BY	
PD6		D16			Nand Flash	
PD7		D17			Nand Flash	
PD8		D18			Nand Flash	
PD9		D19			Nand Flash	
PD10		D20			Nand Flash	MCI0_CD (switch)
PD11		D21			Nand Flash	MCI1_CD (switch)
PD12		D22			Nand Flash	ZB_RSTN
PD13		D23			Nand Flash	ZB_SLPTR
PD14		D24				EN5V_HDA#
PD15		D25	A20			EN5V_HDB#
PD16		D26	A23			EN5V_HDC#
PD17		D27	A24			
PD18		D28	A25			
PD19		D29	NCS2			
PD20		D30	NCS4			
PD21		D31	NCS5		POWR_LED	

4.3.5 Connectors

4.3.5.1 Power Supply

Figure 4-35. Power Supply Connector J4

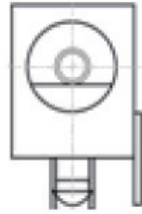


Table 4-20. Power Supply Connector J2 Signal Description

Pin	Mnemonic	Signal description
1	Center	+5V
2		GND
3		Floating

4.3.5.2 SODIMM Card Edge Socket

The Evaluation Kit uses a SODIMM200 standard connector for CM board interfacing.

Please note that this is not an industry standard pin-out and that it is unlikely to be compatible with off-the-shelf SODIMM cards.

Figure 4-36. SODIMM200 Socket CON1



Table 4-21. SODIMM200 CON1 Signal Descriptions

Function	Type	x5 pad name	SODIMM 200			x5 pad name	Type	Function
		Front Side	A		B	Back Side		
VCC 3V3		POWER OUTPUT	1		2	POWER OUTPUT		VCC 3V3
VCC 3V3		POWER OUTPUT	3		4	POWER OUTPUT		VCC 3V3
GND			5		6	POWER OUTPUT	VBAT	
USBC_DP	I/O	USB Data Positive	7		8		SYSC	JTAGSEL
USBC_DM	I/O	USB Data Negative	9		10		SYSC	WKUP
GND			11		12		SYSC	SHDN



Table 4-21. SODIMM200 CON1 Signal Descriptions (Continued)

Function	Type	x5 pad name	SODIMM 200			x5 pad name	Type	Function
USBB_DM	I/O	USB Data Negative	13		14		SYSC	BMS
USBB_DP	I/O	USB Data Positive	15		16		SYSC	nRST
GND			17		18		SYSC	nTRST
DIBP	I/O		19		20		RSTJTAG	TDI
DIBN	I/O		21		22		RSTJTAG	TCK
GND			23		24		RSTJTAG	TMS
USBA_DM	I/O	USB Data Negative	25		26		RSTJTAG	TDO
USBA_DP	I/O	USB Data Positive	27		28		RSTJTAG	RTCK
GND			29		30			PWR_EN
RFU		RFU	31		32	RFU		RFU
RFU		RFU	33		34	RFU		RFU
RFU		RFU	35		36	RFU		RFU
RFU		RFU	37		38	RFU		RFU
RFU		RFU	39		40	RFU		RFU
KEY								
GND			41		42			GND
RFU		RFU	43		44	RFU		RFU
RFU		RFU	45		46	RFU		RFU
RFU		RFU	47		48	RFU		RFU
RFU		RFU	49		50	RFU		RFU
GND			51		52			GND
RFU		RFU	53		54	RFU		RFU
RFU		RFU	55		56	RFU		RFU
RFU		RFU	57		58	RFU		RFU
RFU		RFU	59		60	RFU		RFU
VDDNF	POWER DOMAIN FROM CM		61		62	POWER DOMAIN FROM CM		VDDNF
PD0	GPIO D	NANDOE	63		64	NANDWE	GPIO D	PD1
PD2	GPIO D	A21/NANDALE	65		66	A22/NANDCLE	GPIO D	PD3
PD4	GPIO D	NCS3	67		68	NWAIT	GPIO D	PD5
PD6	GPIO D	D16	69		70	D17	GPIO D	PD7
PD8	GPIO D	D18	71		72	D19	GPIO D	PD9
GND			73		74			GND
PD10	GPIO D	D20	75		76	D21	GPIO D	PD11
PD12	GPIO D	D22	77		78	D23	GPIO D	PD13
PD14	GPIO D	D24	79		80	D25/A20	GPIO D	PD15
PD16	GPIO D	D26/A23	81		82	D27/A24	GPIO D	PD17
PD18	GPIO D	D28/A25	83		84	D29/NCS2	GPIO D	PD19



Table 4-21. SODIMM200 CON1 Signal Descriptions (Continued)

Function	Type	x5 pad name	SODIMM 200			x5 pad name	Type	Function
USBB_DM	I/O	USB Data Negative	13		14		SYSC	BMS
USBB_DP	I/O	USB Data Positive	15		16		SYSC	nRST
GND			17		18		SYSC	nTRST
DIBP	I/O		19		20		RSTJTAG	TDI
DIBN	I/O		21		22		RSTJTAG	TCK
GND			23		24		RSTJTAG	TMS
USBA_DM	I/O	USB Data Negative	25		26		RSTJTAG	TDO
USBA_DP	I/O	USB Data Positive	27		28		RSTJTAG	RTCK
GND			29		30			PWR_EN
RFU		RFU	31		32	RFU		RFU
RFU		RFU	33		34	RFU		RFU
RFU		RFU	35		36	RFU		RFU
RFU		RFU	37		38	RFU		RFU
RFU		RFU	39		40	RFU		RFU
KEY								
GND			41		42			GND
RFU		RFU	43		44	RFU		RFU
RFU		RFU	45		46	RFU		RFU
RFU		RFU	47		48	RFU		RFU
RFU		RFU	49		50	RFU		RFU
GND			51		52			GND
RFU		RFU	53		54	RFU		RFU
RFU		RFU	55		56	RFU		RFU
RFU		RFU	57		58	RFU		RFU
RFU		RFU	59		60	RFU		RFU
VDDNF	POWER DOMAIN FROM CM		61		62	POWER DOMAIN FROM CM		VDDNF
PD0	GPIO D	NANDOE	63		64	NANDWE	GPIO D	PD1
PD2	GPIO D	A21/NANDALE	65		66	A22/NANDCLE	GPIO D	PD3
PD4	GPIO D	NCS3	67		68	NWAIT	GPIO D	PD5
PD6	GPIO D	D16	69		70	D17	GPIO D	PD7
PD8	GPIO D	D18	71		72	D19	GPIO D	PD9
GND			73		74			GND
PD10	GPIO D	D20	75		76	D21	GPIO D	PD11
PD12	GPIO D	D22	77		78	D23	GPIO D	PD13
PD14	GPIO D	D24	79		80	D25/A20	GPIO D	PD15
PD16	GPIO D	D26/A23	81		82	D27/A24	GPIO D	PD17
PD18	GPIO D	D28/A25	83		84	D29/NCS2	GPIO D	PD19



Table 4-21. SODIMM200 CON1 Signal Descriptions (Continued)

Function	Type	x5 pad name	SODIMM 200			x5 pad name	Type	Function
USBB_DM	I/O	USB Data Negative	13		14		SYSC	BMS
USBB_DP	I/O	USB Data Positive	15		16		SYSC	nRST
GND			17		18		SYSC	nTRST
DIBP	I/O		19		20		RSTJTAG	TDI
DIBN	I/O		21		22		RSTJTAG	TCK
GND			23		24		RSTJTAG	TMS
USBA_DM	I/O	USB Data Negative	25		26		RSTJTAG	TDO
USBA_DP	I/O	USB Data Positive	27		28		RSTJTAG	RTCK
GND			29		30			PWR_EN
RFU		RFU	31		32	RFU		RFU
RFU		RFU	33		34	RFU		RFU
RFU		RFU	35		36	RFU		RFU
RFU		RFU	37		38	RFU		RFU
RFU		RFU	39		40	RFU		RFU
KEY								
GND			41		42			GND
RFU		RFU	43		44	RFU		RFU
RFU		RFU	45		46	RFU		RFU
RFU		RFU	47		48	RFU		RFU
RFU		RFU	49		50	RFU		RFU
GND			51		52			GND
RFU		RFU	53		54	RFU		RFU
RFU		RFU	55		56	RFU		RFU
RFU		RFU	57		58	RFU		RFU
RFU		RFU	59		60	RFU		RFU
VDDNF	POWER DOMAIN FROM CM		61		62	POWER DOMAIN FROM CM		VDDNF
PD0	GPIO D	NANDOE	63		64	NANDWE	GPIO D	PD1
PD2	GPIO D	A21/NANDALE	65		66	A22/NANDCLE	GPIO D	PD3
PD4	GPIO D	NCS3	67		68	NWAIT	GPIO D	PD5
PD6	GPIO D	D16	69		70	D17	GPIO D	PD7
PD8	GPIO D	D18	71		72	D19	GPIO D	PD9
GND			73		74			GND
PD10	GPIO D	D20	75		76	D21	GPIO D	PD11
PD12	GPIO D	D22	77		78	D23	GPIO D	PD13
PD14	GPIO D	D24	79		80	D25/A20	GPIO D	PD15
PD16	GPIO D	D26/A23	81		82	D27/A24	GPIO D	PD17
PD18	GPIO D	D28/A25	83		84	D29/NCS2	GPIO D	PD19



Table 4-21. SODIMM200 CON1 Signal Descriptions (Continued)

Function	Type	x5 pad name	SODIMM 200			x5 pad name	Type	Function
PD20	GPIO D	D30/NCS4	85		86	D31/NCS5	GPIO D	PD21
VDDIOP0	POWER OUTPUT		87		88	POWER OUTPUT		VDDIOP0
PA0	GPIO A	TXD0/SPI1-NPCS1	89		90	RXD0/SPI0-NPCS2	GPIO A	PA1
PA2	GPIO A	MCI1_DA1/E0_ETX0	91		92	CTS0/ MCI1_DA2/ E0_ETX1	GPIO A	PA3
PA4	GPIO A	SCK0/MCI1_DA3/ E0_ETXER	93		94			GND
PA11	GPIO A	SPI0_MISO/ MCI1_DA0	95		96	SPI0_MOSI/ MCI1_CDA	GPIO A	PA12
PA13	GPIO A	SPI0_SPCK/ MCI1_CK	97		98	SPI0_NPCS0	GPIO A	PA14
GND			99		100	TXD2/ SPI0_NPCS1	GPIO A	PA7
PA8	GPIO A	RXD2/SPI1_NPCS0	101		102	TIOA0 /SPI1_MISO	GPIO A	PA21
PA22	GPIO A	TIOA1/SPI1_MOS1	103		104	TIOA2/ SPI1_SPCK	GPIO A	PA23
PA31	GPIO A	TWCK0/SPI1_NPCS2/ E0_ETXEN	105		106	TWD0/ SPI1_NPCS3/ E0_EMDC	GPIO A	PA30
GND			107		108	MCI0_DA0	GPIO A	PA15
PA16	GPIO A	MCI0_CDA	109		110	MCI0_CK	GPIO A	PA17
PA18	GPIO A	MCI0_DA1	111		112	MCI0_DA2	GPIO A	PA19
PA20	GPIO A	MCI0_DA3	113		114			GND
PA5	GPIO A	TXD1/CANTX1	115		116	RXD1/CANRX1	GPIO A	PA6
PA10	GPIO A	DTXD/CANTX0	117		118	DRXD/CANRX0	GPIO A	PA9
GND			119		120	TCLK0/TK	GPIO A	PA24
PA25	GPIO A	TCLK1/TF	121		122	TCLK2/TD	GPIO A	PA26
PA27	GPIO A	TIOB0/RD	123		124	TIOB1/RK	GPIO A	PA28
PA29	GPIO A	TIOB2/RF	125		126			GND
VDDIOP1	POWER OUTPUT		127		128	POWER OUTPUT		VDDIOP1
PC0	GPIO C	LCDDAT0/ISI_D0	129		130	LCDDAT1	GPIO C	PC1
PC2	GPIO C	LCDDAT2/ISI_D2	131		132	LCDDAT3	GPIO C	PC3
PC4	GPIO C	LCDDAT4	133		134	LCDDAT5	GPIO C	PC5
GND			135		136	LCDDAT6	GPIO C	PC6
PC7	GPIO C	LCDDAT7	137		138	LCDDAT8	GPIO C	PC8
PC9	GPIO C	LCDDAT9	139		140	LCDDAT10	GPIO C	PC10
PC11	GPIO C	LCDDAT11	141		142			GND
PC12	GPIO C	LCDDAT12	143		144	LCDDAT13	GPIO C	PC13
PC14	GPIO C	LCDDAT14	145		146	LCDDAT15	GPIO C	PC15
GND			147		148	LCDDAT16	GPIO C	PC16



Table 4-21. SODIMM200 CON1 Signal Descriptions (Continued)

Function	Type	x5 pad name	SODIMM 200			x5 pad name	Type	Function
PC17	GPIO C	LCDDAT17	149		150	LCDDAT18	GPIO C	PC18
PC19	GPIO C	LCDDAT19	151		152	LCDDAT20	GPIO C	PC20
PC21	GPIO C	LCDDAT21	153		154			GND
PC22	GPIO C	LCDDAT22	155		156	LCDDAT23	GPIO C	PC23
PC24	GPIO C	LCDDISP	157		158		GPIO C	PC25
PC26	GPIO C	LCDPWM	159		160	LCDVSYNC	GPIO C	PC27
GND			161		162	LCDHSYNC	GPIO C	PC28
PC29	GPIO C	LCDDEN	163		164	E1_MDC	GPIO C	PC30
PC31	GPIO C	E1_MDIO	165		166			SELCONFIG
VDDANA	POWER OUTPUT		167		168	POWER OUTPUT		VDDANA
PB0	GPIO B	E0_RX0	169		170	E0_RX1	GPIO B	PB1
PB2	GPIO B	E0_RXER	171		172	E0_RXDV	GPIO B	PB3
PB4	GPIO B	E0_TXCK	173		174	E0_MDIO	GPIO B	PB5
PB6	GPIO B	E0_MDC	175		176	E0_TXEN	GPIO B	PB7
PB8	GPIO B	E0_TXER	177		178	GNDANA		
PB9	GPIO B	E0_TX0	179		180	E0_TX1	GPIO B	PB10
PB11	GPIO B	E0_TX2	181		182	E0_TX3	GPIO B	PB12
PB13	GPIO B	E0_RX2	183		184	E0_RX3	GPIO B	PB14
PB15	GPIO B	E0_RXCK	185		186	E0_CRS	GPIO B	PB16
PB17	GPIO B	E0_COL	187		188	GNDANA		
PB18	GPIO B	IRQ	189		190	POWER OUTPUT		POWR_REF
GND			191		192		ETH	LED0
ETH0_TX+	ETH		193		194		ETH	LED1
ETH0_TX-	ETH		195		196		ETH	LED2
ETH0_RX+	ETH		197		198		ETH	AVDDT
ETH0_RX-	ETH		199		200			GND_ETH

4.3.5.3 JTAG/ICE Connector

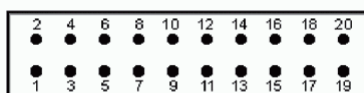
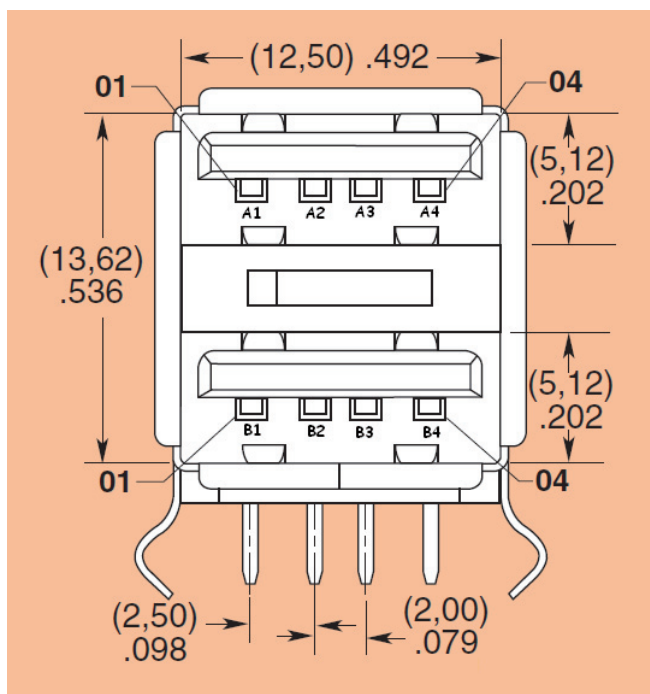
Figure 4-37. JTAG J9

Table 4-22. JTAG/ICE Connector J13 Signal Descriptions

Pin	Mnemonic	Description
1	VTref. 3.3V power	This is the target reference voltage. It is used to check if the target has power, to create the logic-level reference for the input comparators, and to control the output logic levels to the target. It is normally fed from VDD on the target board and must not have a series resistor.
2	Vsupply. 3.3V power	This pin is not connected in SAM-ICE. It is reserved for compatibility with other equipment. Connect to VDD or leave open in target system.
3	nTRST TARGET RESET - Active-low output signal that resets the target	JTAG Reset. Output from SAM-ICE to the Reset signal on the target JTAG port. Typically connected to nTRST on the target CPU. This pin is normally pulled HIGH on the target to avoid unintentional resets when there is no connection.
4	GND	Common ground
5	TDI TEST DATA INPUT - Serial data output line, sampled on the rising edge of the TCK signal.	JTAG data input of target CPU. It is recommended that this pin is pulled to a defined state on the target board. Typically connected to TDI on target CPU.
6	GND	Common ground
7	TMS TEST MODE SELECT	JTAG mode set input of target CPU. This pin should be pulled up on the target. Typically connected to TMS on target CPU. Output signal that sequences the target's JTAG state machine, sampled on the rising edge of the TCK signal.
8	GND	Common ground
9	TCK TEST CLOCK - Output timing signal, for synchronizing test logic and control register access.	JTAG clock signal to target CPU. It is recommended that this pin is pulled to a defined state on the target board. Typically connected to TCK on target CPU.
10	GND	Common ground
11	RTCK - Input Return test clock signal from the target.	Some targets must synchronize the JTAG inputs to internal clocks. To assist in meeting this requirement, a returned and retimed TCK can be used to dynamically control the TCK rate. SAM-ICE supports adaptive clocking which waits for TCK changes to be echoed correctly before making further changes. Connect to RTCK if available, otherwise to GND.
12	GND	Common ground
13	TDO JTAG TEST DATA OUTPUT - Serial data input from the target.	JTAG data output from target CPU. Typically connected to TDO on target CPU.
14	GND	Common ground
15	nSRST RESET	Active-low reset signal. Target CPU reset signal.
16	GND	Common ground
17	RFU	This pin is not connected in SAM-ICE.
18	GND	Common ground
19	RFU	This pin is not connected in SAM-ICE.
20	GND	Common ground

4.3.5.4 USB Type A Dual Port

Figure 4-38. USB Type A Dual Port J19**Table 4-23.** USB Type A Dual Port J19 Signal Descriptions

Pin	Mnemonic	Description
A1	Vbus – USB_A	5V power
A2	DM – USB_A	Data minus
A3	DP – USB_A	Data plus
A4	GND	Common ground
B1	Vbus – USB_A	5V power
B2	DM – USB_A	Data minus
B3	DP – USB_A	Data plus
B4	GND	Common ground
Mechanical pins		Shield

4.3.5.5 USB Micro AB

Figure 4-39. USB Host/Device Micro AB Connector J20

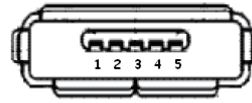


Table 4-24. USB Host/Device Micro AB Connector J20 Signal Descriptions

Pin	Mnemonic	Description
1	Vbus	5v power
2	DM	Data minus
3	DP	Data plus
4	ID	On the Go Identification
5	GND	Common ground

4.3.5.6 DBGU

Figure 4-40. DBGU Connector J11

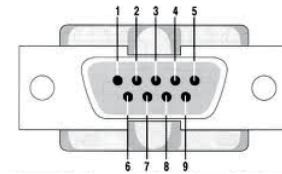
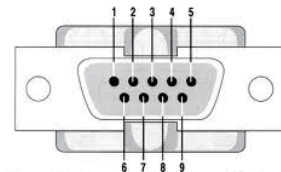


Table 4-25. DBGU Connector J11 Signal Descriptions

Pin	Mnemonic	Description
1, 4, 6, 9		NO CONNECTION
2	RXD (RECEIVED DATA)	RS232 serial data output signal
3	TXD (TRANSMITTED Data)	RS232 serial data input signal
5	GND	Common ground
7	RTS (REQUEST TO SEND)	NO USED
8	CTS (CLEAR TO SEND)	NO USED
Mechanical pins		Shield

4.3.5.7 RS232 Connector with RTS/CTS Handshake Support

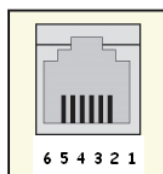
Figure 4-41. USART Connector J12, J13**Table 4-26.** USART Connector J12 Signal Descriptions

Pin	Mnemonic		Description
1, 4, 6, 9			NO CONNECTION
2	RXD (RECEIVED DATA)	PA1	RS232 serial data output signal
3	TXD (TRANSMITTED Data)	PA0	RS232 serial data input signal
5	GND		Common ground
7	RTS (REQUEST TO SEND)	PA2	Active-positive RS232 input signal
8	CTS (CLEAR TO SEND)	PA3	Active-positive RS232 output signal
Mechanical pins			Shield

Table 4-27. USART Connector J13 Signal Descriptions

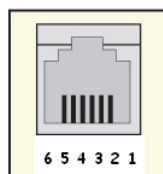
Pin	Mnemonic		Description
1, 4, 6, 9			NO CONNECTION
2	RXD (RECEIVED DATA)	PC23	RS232 serial data output signal
3	TXD (TRANSMITTED Data)	PC22	RS232 serial data input signal
5	GND		Common ground
7	RTS (REQUEST TO SEND)	PC24	Active-positive RS232 input signal
8	CTS (CLEAR TO SEND)	PC25	Active-positive RS232 output signal
Mechanical pins			Shield

4.3.5.8 DAA RJ11 Socket (6P4C)

Figure 4-42. DAA RJ11 Socket J16**Table 4-28.** DAA RJ11 Socket J16 Signal Descriptions

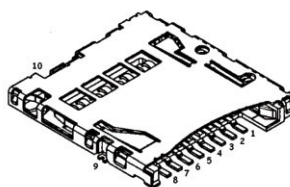
Pin	Mnemonic	Description
1, 2, 5, 6		NO CONNECTION
3	RAC	RING side of ordinary telephone line
4	TAC	TIP side of ordinary telephone line

4.3.5.9 CAN RJ12 Socket (6P6C)

Figure 4-43. CAN RJ12 Socket CON2, CON3**Table 4-29.** DAA RJ11 Socket J16 Signal Descriptions

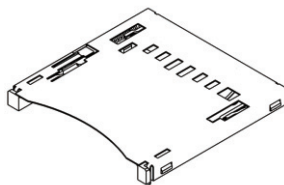
Pin	Mnemonic	Description
1	3V3	POWER PIN
2	5V	POWER PIN
4	CANL	CAN bus differential pair
5	CANH	CAN bus differential pair
4, 6	GND	Common ground

4.3.5.10 MicroSD MCI0

Figure 4-44. MicroSD Socket J6**Table 4-30.** MicroSD Socket J6 Signal Descriptions

Pin	Mnemonic	Description
1	DAT2	Data Bit 2
2	CD/DAT3	Card Detect/Data Bit 3
3	CMD	Command Line
4	VCC	Supply Voltage 3.3V
5	CLK	Command Line
6	VSS	Common ground
7	DAT0	Data Bit 0
8	DAT1	Data Bit 1
9	SW1	No use, grounded
10	CARD DETECT	CARD DETECT

4.3.5.11 SD/MMC MCI1

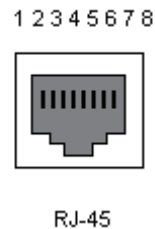
Figure 4-45. SD/MMC Socket J7**Table 4-31.** SD Socket J7 Signal Descriptions

Pin	Mnemonic	PIO	Signal		
			MMC Card	SD Card	
				1-Bit Mode	4-Bit Mode
1	MCI1_DA2	PA3	Not Used	Read Wait (RW)	Data Line DAT2 or Read Wait (RW)
2	MCI1_DA3	PA4	Reserved	Not Used	Data Line DAT3
3	MCI1_CDA	PA12	Command/Response		
4	VDDIOP0		Supply Voltage (3.3-volts) VDDIOP0		

Table 4-31. SD Socket J7 Signal Descriptions (Continued)

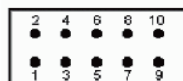
Pin	Mnemonic	PIO	Signal		
			MMC Card	SD Card	
				1-Bit Mode	4-Bit Mode
5	MCI1_CK	PA13	Clock		
6	GND		Ground		
7	MCI1_DA0	PA11	Data Line DAT0		
8	MC1_DA1	PA2	Not Used	Interrupt (IRQ)	Data Line DAT1 or Interrupt (IRQ)
9	GND		Ground		
10	MCI1_CD	PD14	Card Detect, configured as GPIO, Power domain VDDNF		
11	WP		Write Protect Detect, connects to jumper JP6		
12	GND		Ground		
13	GND		Ground		
14	GND		Ground		
15	GND		Ground		

4.3.5.12 Ethernet RJ45 Socket J17, J18

Figure 4-46. Ethernet RJ45 Socket J17, J18**Table 4-32.** DAA RJ11 Socket J16 Signal Descriptions

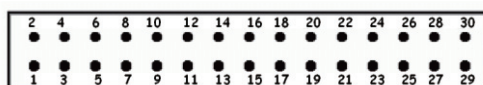
Pin	Mnemonic	Description
1	TX+	DIFFERENTIAL OUTPUT PLUS
2	TX-	DIFFERENTIAL OUTPUT MINUS
3	RX+	DIFFERENTIAL INPUT PLUS
4	Reserved	
5	Reserved	DIFFERENTIAL INPUT MINUS
6	RX-	
7	Reserved	
8	Reserved	

4.3.5.13 ZigBee Socket J10

Figure 4-47. ZigBee Socket J10**Table 4-33.** ZigBee Socket J10 Signal Descriptions

Function	Signal Name	Port	Pin	Pin	Port	Signal Name	Function	Option on misc. port set by OR or solder shunts
Reset	/RST		1	2		Misc.		■ EEprom for MAC address, cap array settings and serial number ■ TST: test mode activation ■ CLKM: RF chip clock output
Interrupt Request	IRQ		3	4		SLP_TR	SLP_TR	
SPI chip select	/SEL		5	6		MOSI	SPI MOSI	
SPI MISO	MISO		7	8		SCLK	SPI CLK	
Power Supply	GND	GND	9	10	VCC	VCC	VCC	Voltage range: 1.8v to 5.5v, regulated to 3.3v

4.3.5.14 LCD/ISI Socket J21

Figure 4-48. LCD/ISI Socket J21**Table 4-34.** LCD/ISI Socket J21 Signal Descriptions

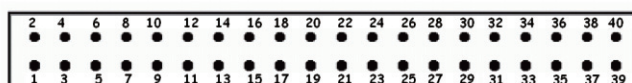
LCD	ISI	Pin Num	Pin Num	ISI	LCD
3V3	3V3	1	2	GND	GND
VDDISI	VDDISI	3	4	GND	GND
ZB_IRQ0	ZB_IRQ0	5	6		ZB_IRQ1
TWCK0	TWCK0	7	8		TWD0
GND	GND	9	10	ISI_MCK	LCDDAT15
GND	GND	11	12	ISI_VSYNC	LCDDAT13
GND	GND	13	14	ISI_HSYNC	LCDDAT14



Table 4-34. LCD/ISI Socket J21 Signal Descriptions (Continued)

LCD	ISI	Pin Num	Pin Num	ISI	LCD
GND	GND	15	16	ISI_PCK	LCDDAT12
GND	GND	17	18	ISI_D0	LCDDAT0
LCDDAT1	ISI_D1	19	20	ISI_D2	LCDDAT2
LCDDAT3	ISI_D3	21	22	ISI_D4	LCDDAT4
LCDDAT5	ISI_D5	23	24	ISI_D6	LCDDAT6
LCDDAT7	ISI_D7	25	26	ISI_D8	LCDDAT8
LCDDAT9	ISI_D9	27	28	ISI_D10	LCDDAT10
LCDDAT11	ISI_D11	29	30	GND	GND

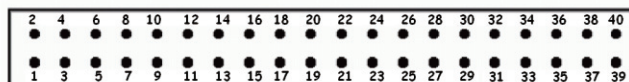
4.3.5.15 LCD/TSC Socket J22

Figure 4-49. LCD/TSC Socket J22**Table 4-35.** LCD/TSC Socket J22 Signal Descriptions

LCD		Pin Num	Pin Num		LCD
5V	5V_INTER	1	2	GND	GND
5V	5V_INTER	3	4	GND	GND
LCDDAT16		5	6		LCDDAT17
LCDDAT18		7	8		LCDDAT19
LCDDAT20		9	10		LCDDAT21
LCDDAT22		11	12		LCDDAT23
GND	GND	13	14	GND	GND
LCDDISP		15	16		LCDPWM
LCDCSYNC		17	18		LCDHSYNC
LCDDEN		19	20		LCDPCK
GND	GND	21	22	GND	GND
AD0_XP	TSC	23	24	TSC	AD1_XM
AD2_YP	TSC	25	26	TSC	AD3_YM
AD4_LR	TSC	27	28		ONE_WIRE
GND	GND	29	30	GND	GND
SPI1_MISO		31	32		SPI1_MOSI
SPI1_SPCK		33	34		SPI1_NPCS1
EN_PWRLCD	SELCONFIG	35	36	LCD_DETECT	LCD_DETECT#
PD16		37	38		PD17
GND	GND	39	40	GND	GND

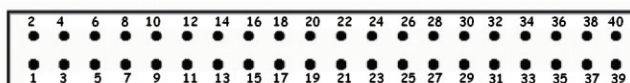


4.3.5.16 IO Expansion Port J1

Figure 4-50. IO Expansion Socket J1**Table 4-36.** Expansion Socket J1 Signal Descriptions

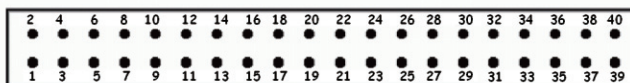
PIO	Power	Pin Num	Pin Num	Power	PIO
	3V3, or 5V	1	2	3V3, or 5V	
	GND	3	4	GND	
PA0		5	6		PA16
PA1		7	8		PA17
PA2		9	10		PA18
PA3		11	12		PA19
PA4		13	14		PA20
PA5		15	16		PA21
PA6		17	18		PA22
PA7		19	20		PA23
PA8		21	22		PA24
PA9		23	24		PA25
PA10		25	26		PA26
PA11		27	28		PA27
PA12		29	30		PA28
PA13		31	32		PA29
PA14		33	34		PA30
PA15		35	36		PA31
	GND	37	38	GND	
	3V3	39	40	3V3	

4.3.5.17 IO Expansion Port J2

Figure 4-51. IO Expansion Socket J2**Table 4-37.** Expansion Socket J1 Signal Descriptions

PIO	Power	Pin Num	Pin Num	Power	PIO
	3V3, or 5V	1	2	3V3, or 5V	
	GND	3	4	GND	
PC0		5	6		PC16
PC1		7	8		PC17
PC2		9	10		PC18
PC3		11	12		PC19
PC4		13	14		PC20
PC5		15	16		PC21
PC6		17	18		PC22
PC7		19	20		PC23
PC8		21	22		PC24
PC9		23	24		PC25
PC10		25	26		PC26
PC11		27	28		PC27
PC12		29	30		PC28
PC13		31	32		PC29
PC14		33	34		PC30
PC15		35	36		PC31
	GND	37	38	GND	
	3V3	39	40	3V3	

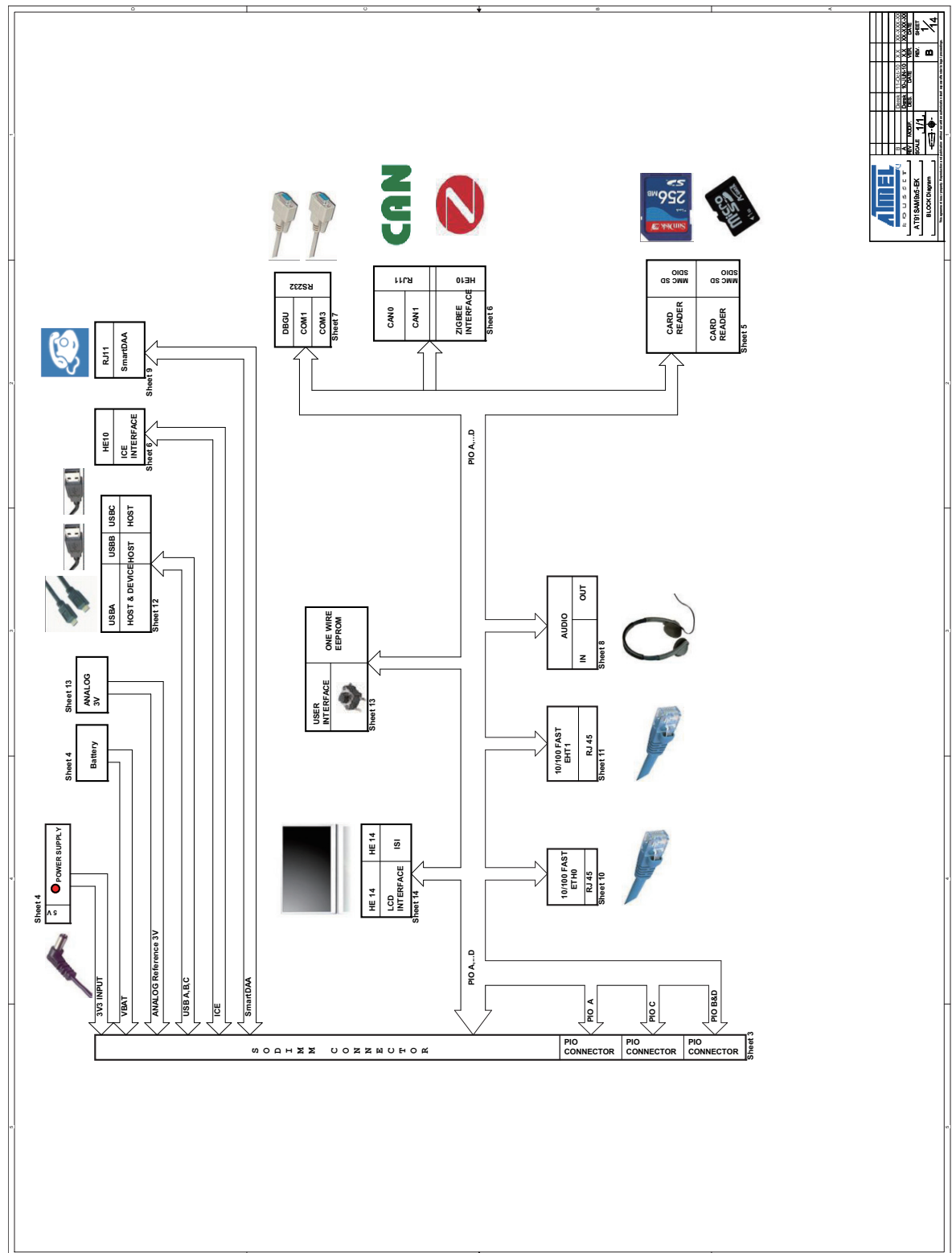
4.3.5.18 IO Expansion Port J3

Figure 4-52. IO Expansion Socket J3**Table 4-38.** Expansion Socket J1 Signal Descriptions

PIO	Power	Pin Num	Pin Num	Power	PIO
	3V3, or 5V	1	2	3V3, or 5V	
	GND	3	4	GND	
PB0		5	6		PB16
PB1		7	8		PB17
PB2		9	10		PB18
PB3		11	12		-
PB4		13	14		-
PB5		15	16		-
PB6		17	18		-
PB7		19	20		-
PB8		21	22		PD14
PB9		23	24		PD15
PB10		25	26		PD16
PB11		27	28		PD17
PB12		29	30		PD18
PB13		31	32		PD19
PB14		33	34		PD20
PB15		35	36		PD21
	GND	37	38	GND	
	3V3	39	40	3V3	

4.3.6 Schematics

Figure 4-53. EK Board Schematics



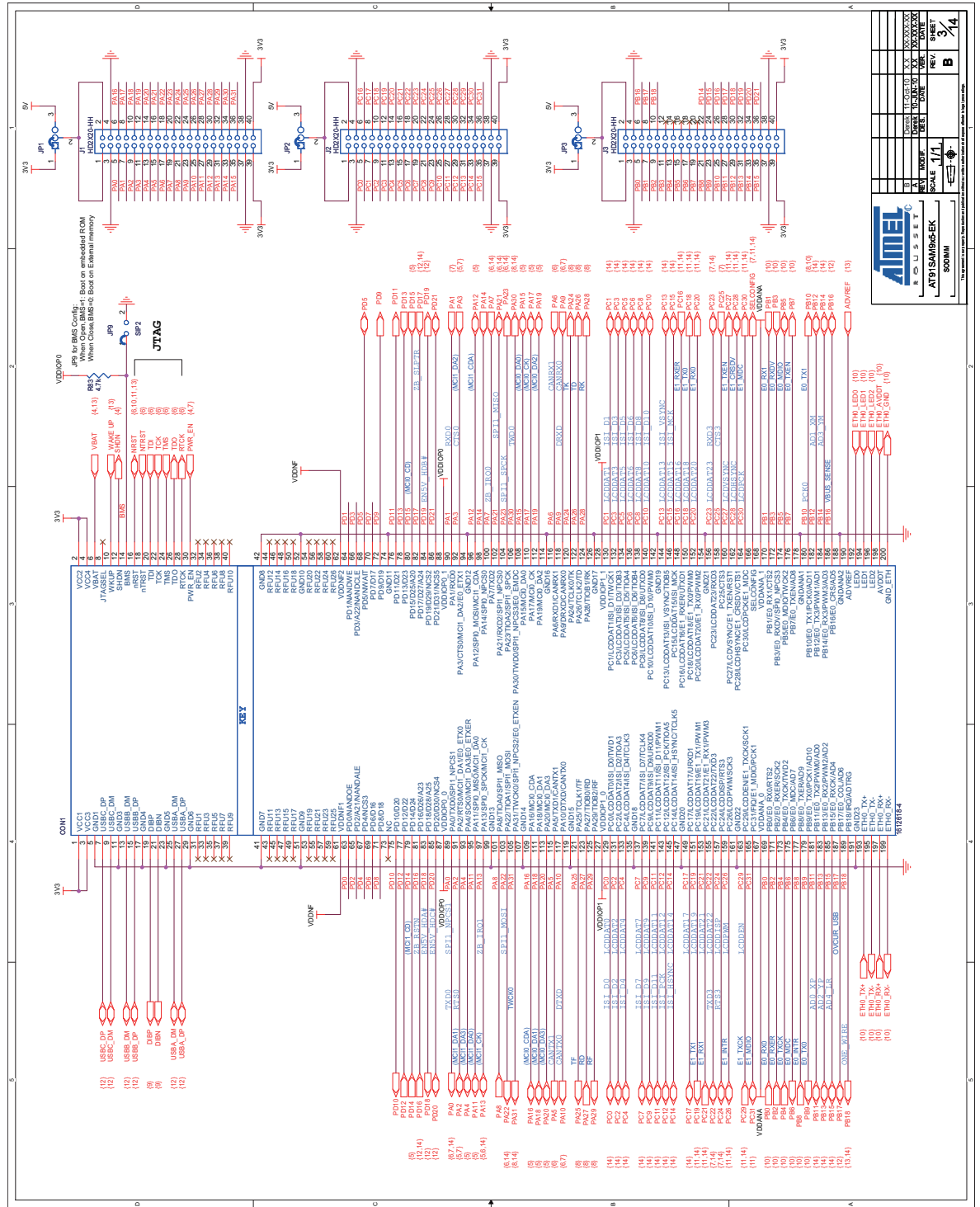
REVISION HISTORY			TEST POINT			SCHEMATICS CONVENTIONS		
REV	DATA	NOTE	PAGE	REFERENCE	FUNCTION	(1) Resistance Unit: "K" is "Kohm", "R" is "Ohm"	(2) "DNP" means the component is not populated by default	
A	2010.07	ORIGINAL RELEASED	4	TP1, TP2	GND			
B	2010.10	SECOND RELEASED	4	TP3	5V			
			4	TP4	5V3			
			4	TP5	VDDIO50			
			4	TP6	VDDIOPT			
			4	TP7	VDDNF			

JUMPER and SOLDERDROP		
PAGE	REFERENCE	DEFAULT FUNCTION
3	JP1	1-2 3.3V or 5V selection for J1
	JP2	1-2 3.3V or 5V selection for J2
	JP3	1-2 3.3V or 5V selection for J3
	JP4	OPEN Default boot or embedded ROM. Close boot or external memory
4	JP5	CLOSE Backup supply on/off
	JP6	CLOSE Force power on function
	JP7	CLOSE MCI1 write protect select
	JP8	CLOSE CAN0 diff termination select
	JP9	CLOSE CAN1 diff termination select
	JP10	OPEN ZigBee Power on/off select
	JP11	CLOSE DEBUG and CAN0 selection
	JP12	CLOSE MDIX ON/OFF (ETH0)
	JP13	CLOSE MDIX ON/OFF (ETH1)
13	JP14	1-2 ADVREF input selection

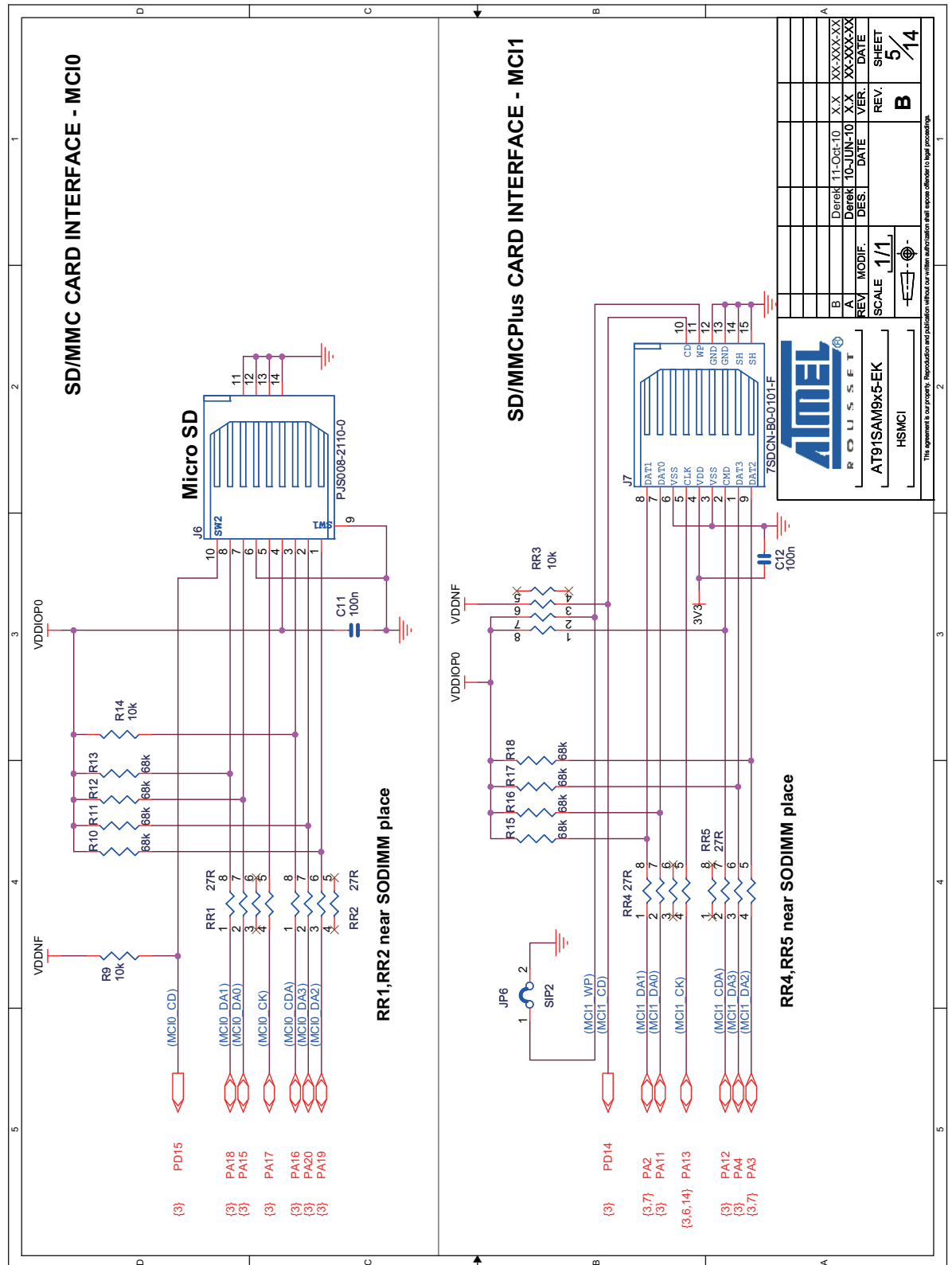
TABLE OF CONTENTS	
PAGE	DESCRIPTION
1	Block Diagram
2	Pin Descriptions
3	SDRAM
4	POWER SUPPLY
5	HSMDI
6	ZigBee Can Interface, JTAG
7	USART0, USART3, Debug port
8	AUDIO
9	SmartDAA
10	ETH0
11	ETH1
12	USB Interface
13	Miscellaneous
14	LCDMIS1

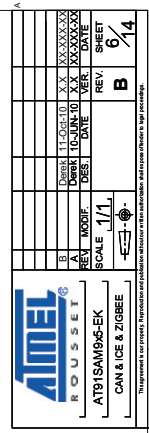
PIO MUXING	
PIOA	USAG
PIOB	USAG
PIOC	USAG
PIOD	USAG
PIOE	USAG
PIOF	USAG
PIOG	USAG
PIOH	USAG
PIOI	USAG
PIOJ	USAG
PIOK	USAG
PIOL	USAG
PIOM	USAG
PIOO	USAG
PIOP	USAG
PIOQ	USAG
PIOR	USAG
PIOS	USAG
PIOT	USAG
PIOU	USAG
PIOV	USAG
PIOW	USAG
PIOX	USAG
PIOY	USAG
PIOZ	USAG

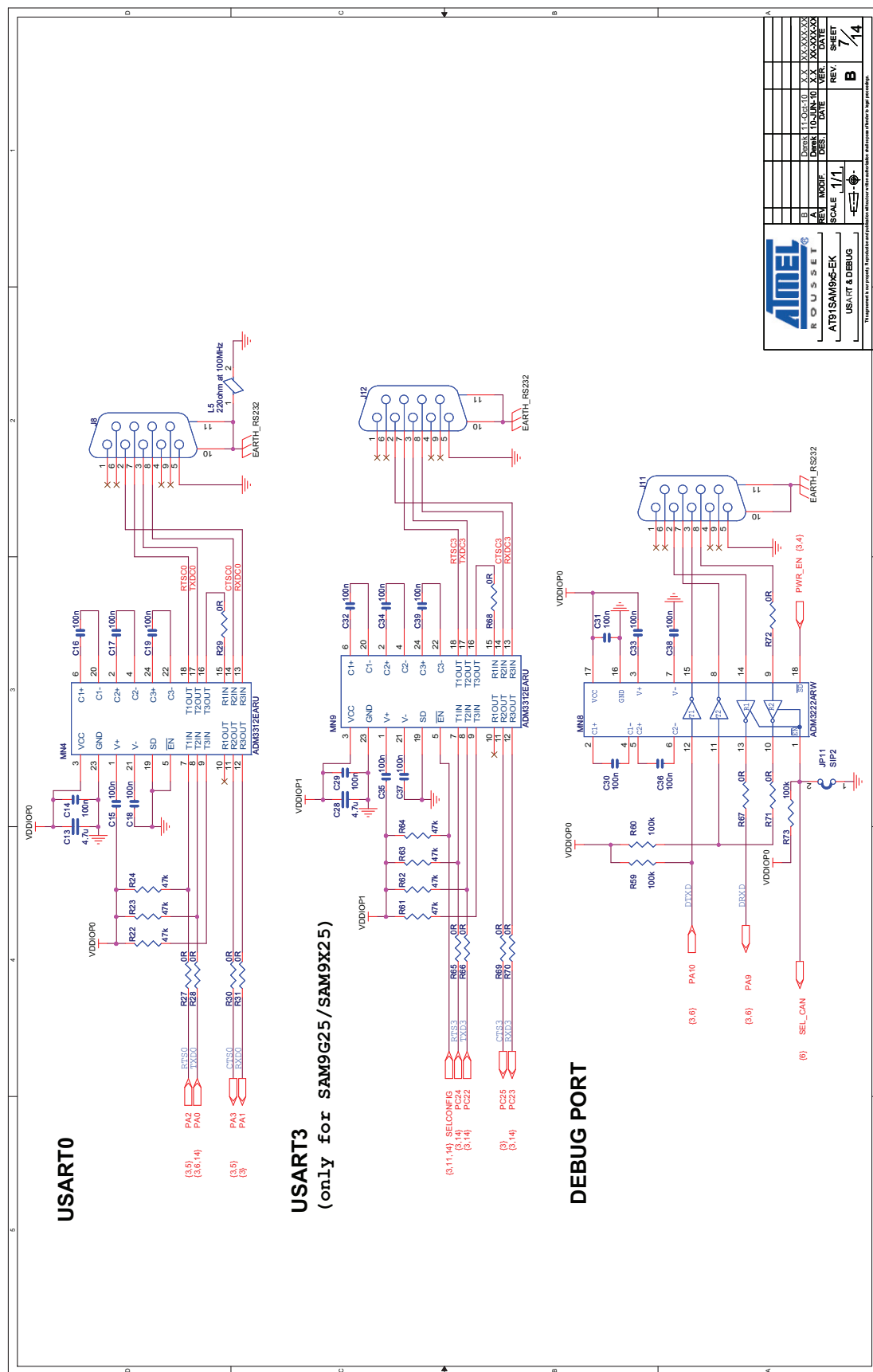
DEFAULT NO POPULATE PARTS	
PAGE	REFERENCE
6	R52 R53 R55 R56 R57 R58 R59 R60 R61 R62 R63 R64 R65 R66 R67 R68 R69 R70 R71 R72 R73 R74 R75 R76 R77 R78 R79 R80 R81 R82 R83 R84 R85 R86 R87 R88 R89 R90 R91 R92 R93 R94 R95 R96 R97 R98 R99 R100 R101 R102 R103 R104 R105 R106 R107 R108 R109 R110 R111 R112 R113 R114 R115 R116 R117 R118 R119 R120 R121 R122 R123 R124 R125 R126 R127 R128 R129 R130 R131 R132 R133 R134 R135 R136 R137 R138 R139 R140 R141 R142 R143 R144 R145 R146 R147 R148 R149 R150 R151 R152 R153 R154 R155 R156 R157 R158 R159 R160 R161 R162 R163 R164 R165 R166 R167 R168 R169 R170 R171 R172 R173 R174 R175 R176 R177 R178 R179 R180 R181 R182 R183 R184 R185 R186 R187 R188 R189 R190 R191 R192 R193 R194 R195 R196 R197 R198 R199 R200 R201 R202 R203 R204 R205 R206 R207 R208 R209 R210 R211 R212 R213 R214 R215 R216 R217 R218 R219 R220 R221 R222 R223 R224 R225 R226 R227 R228 R229 R230 R231 R232 R233 R234 R235 R236 R237 R238 R239 R240 R241 R242 R243 R244 R245 R246 R247 R248 R249 R250 R251 R252 R253 R254 R255 R256 R257 R258 R259 R260 R261 R262 R263 R264 R265 R266 R267 R268 R269 R270 R271 R272 R273 R274 R275 R276 R277 R278 R279 R280 R281 R282 R283 R284 R285 R286 R287 R288 R289 R290 R291 R292 R293 R294 R295 R296 R297 R298 R299 R300 R301 R302 R303 R304 R305 R306 R307 R308 R309 R310 R311 R312 R313 R314 R315 R316 R317 R318 R319 R320 R321 R322 R323 R324 R325 R326 R327 R328 R329 R330 R331 R332 R333 R334 R335 R336 R337 R338 R339 R340 R341 R342 R343 R344 R345 R346 R347 R348 R349 R350 R351 R352 R353 R354 R355 R356 R357 R358 R359 R360 R361 R362 R363 R364 R365 R366 R367 R368 R369 R370 R371 R372 R373 R374 R375 R376 R377 R378 R379 R380 R381 R382 R383 R384 R385 R386 R387 R388 R389 R390 R391 R392 R393 R394 R395 R396 R397 R398 R399 R400 R401 R402 R403 R404 R405 R406 R407 R408 R409 R410 R411 R412 R413 R414 R415 R416 R417 R418 R419 R420 R421 R422 R423 R424 R425 R426 R427 R428 R429 R430 R431 R432 R433 R434 R435 R436 R437 R438 R439 R440 R441 R442 R443 R444 R445 R446 R447 R448 R449 R450 R451 R452 R453 R454 R455 R456 R457 R458 R459 R460 R461 R462 R463 R464 R465 R466 R467 R468 R469 R470 R471 R472 R473 R474 R475 R476 R477 R478 R479 R480 R481 R482 R483 R484 R485 R486 R487 R488 R489 R490 R491 R492 R493 R494 R495 R496 R497 R498 R499 R500 R501 R502 R503 R504 R505 R506 R507 R508 R509 R510 R511 R512 R513 R514 R515 R516 R517 R518 R519 R520 R521 R522 R523 R524 R525 R526 R527 R528 R529 R530 R531 R532 R533 R534 R535 R536 R537 R538 R539 R540 R541 R542 R543 R544 R545 R546 R547 R548 R549 R550 R551 R552 R553 R554 R555 R556 R557 R558 R559 R560 R561 R562 R563 R564 R565 R566 R567 R568 R569 R570 R571 R572 R573 R574 R575 R576 R577 R578 R579 R580 R581 R582 R583 R584 R585 R586 R587 R588 R589 R590 R591 R592 R593 R594 R595 R596 R597 R598 R599 R600 R601 R602 R603 R604 R605 R606 R607 R608 R609 R610 R611 R612 R613 R614 R615 R616 R617 R618 R619 R620 R621 R622 R623 R624 R625 R626 R627 R628 R629 R630 R631 R632 R633 R634 R635 R636 R637 R638 R639 R640 R641 R642 R643 R644 R645 R646 R647 R648 R649 R650 R651 R652 R653 R654 R655 R656 R657 R658 R659 R660 R661 R662 R663 R664 R665 R666 R667 R668 R669 R670 R671 R672 R673 R674 R675 R676 R677 R678 R679 R680 R681 R682 R683 R684 R685 R686 R687 R688 R689 R690 R691 R692 R693 R694 R695 R696 R697 R698 R699 R700 R701 R702 R703 R704 R705 R706 R707 R708 R709 R710 R711 R712 R713 R714 R715 R716 R717 R718 R719 R720 R721 R722 R723 R724 R725 R726 R727 R728 R729 R730 R731 R732 R733 R734 R735 R736 R737 R738 R739 R740 R741 R742 R743 R744 R745 R746 R747 R748 R749 R750 R751 R752 R753 R754 R755 R756 R757 R758 R759 R760 R761 R762 R763 R764 R765 R766 R767 R768 R769 R770 R771 R772 R773 R774 R775 R776 R777 R778 R779 R780 R781 R782 R783 R784 R785 R786 R787 R788 R789 R790 R791 R792 R793 R794 R795 R796 R797 R798 R799 R800 R801 R802 R803 R804 R805 R806 R807 R808 R809 R810 R811 R812 R813 R814 R815 R816 R817 R818 R819 R820 R821 R822 R823 R824 R825 R826 R827 R828 R829 R830 R831 R832 R833 R834 R835 R836 R837 R838 R839 R840 R841 R842 R843 R844 R845 R846 R847 R848 R849 R850 R851 R852 R853 R854 R855 R856 R857 R858 R859 R860 R861 R862 R863 R864 R865 R866 R867 R868 R869 R870 R871 R872 R873 R874 R875 R876 R877 R878 R879 R880 R881 R882 R883 R884 R885 R886 R887 R888 R889 R890 R891 R892 R893 R894 R895 R896 R897 R898 R899 R900 R901 R902 R903 R904 R905 R906 R907 R908 R909 R910 R911 R912 R913 R914 R915 R916 R917 R918 R919 R920 R921 R922 R923 R924 R925 R926 R927 R928 R929 R930 R931 R932 R933 R934 R935 R936 R937 R938 R939 R940 R941 R942 R943 R944 R945 R946 R947 R948 R949 R950 R951 R952 R953 R954 R955 R956 R957 R958 R959 R960 R961 R962 R963 R964 R965 R966 R967 R968 R969 R970 R971 R972 R973 R974 R975 R976 R977 R978 R979 R980 R981 R982 R983 R984 R985 R986 R987 R988 R989 R990 R991 R992 R993 R994 R995 R996 R997 R998 R999 R1000 R1001 R1002 R1003 R1004 R1005 R1006 R1007 R1008 R1009 R1010 R1011 R1012 R1013 R1014 R1015 R1016 R1017 R1018 R1019 R1020 R1021 R1022 R1023 R1024 R1025 R1026 R1027 R1028 R1029 R1030 R1031 R1032 R1033 R1034 R1035 R1036 R1037 R1038 R1039 R1040 R1041 R1042 R1043 R1044 R1045 R1046 R1047 R1048 R1049 R1050 R1051 R1052 R1053 R1054 R1055 R1056 R1057 R1058 R1059 R1060 R1061 R1062 R1063 R1064 R1065 R1066 R1067 R1068 R1069 R1070 R1071 R1072 R1073 R1074 R1075 R1076 R1077 R1078 R1079 R1080 R1081 R1082 R1083 R1084 R1085 R1086 R1087 R1088 R1089 R1090 R1091 R1092 R1093 R1094 R1095 R1096 R1097 R1098 R1099 R1100 R1101 R1102 R1103 R1104 R1105 R1106 R1107 R1108 R1109 R1110 R1111 R1112 R1113 R1114 R1115 R1116 R1117 R1118 R1119 R1120 R1121 R1122 R1123 R1124 R1125 R1126 R1127 R1128 R1129 R1130 R1131 R1132 R1133 R1134 R1135 R1136 R1137 R1138 R1139 R1140 R1141 R1142 R1143 R1144 R1145 R1146 R1147 R1148 R1149 R1150 R1151 R1152 R1153 R1154 R1155 R1156 R1157 R1158 R1159 R1160 R1161 R1162 R1163 R1164 R1165 R1166 R1167 R1168 R1169 R1170 R1171 R1172 R1173 R1174 R1175 R1176 R1177 R1178 R1179 R1180 R1181 R1182 R1183 R1184 R1185 R1186 R1187 R1188 R1189 R1190 R1191 R1192 R1193 R1194 R1195 R1196 R1197 R1198 R1199 R1200 R1201 R1202 R1203 R1204 R1205 R1206 R1207 R1208 R1209 R1210 R1211 R1212 R1213 R1214 R1215 R1216 R1217 R1218 R1219 R1220 R1221 R1222 R1223 R1224 R1225 R1226 R1227 R1228 R1229 R1230 R1231 R1232 R1233 R1234 R1235 R1236 R1237 R1238 R1239 R1240 R1241 R1242 R1243 R1244 R1245 R1246 R1247 R1248 R1249 R1250 R1251 R1252 R1253 R1254 R1255 R1256 R1257 R1258 R1259 R1260 R1261 R1262 R1263 R1264 R1265 R1266 R1267 R1268 R1269 R1270 R1271 R1272 R1273 R1274 R1275 R1276 R1277 R1278 R1279 R1280 R1281 R1282 R1283 R1284 R1285 R1286 R1287 R1288 R1289 R1290 R1291 R1292 R1293 R1294 R1295 R1296 R1297 R1298 R1299 R1300 R1301 R1302 R1303 R1304 R1305 R1306 R1307 R1308 R1309 R1310 R1311 R1312 R1313 R1314 R1315 R1316 R1317 R1318 R1319 R1320 R1321 R1322 R1323 R1324 R1325 R1326 R1327 R1328 R1329 R1330 R1331 R1332 R1333 R1334 R1335 R1336 R1337 R1338 R1339 R1340 R1341 R1342 R1343 R1344 R1345 R1346 R1347 R1348 R1349 R1350 R1351 R1352 R1353 R1354 R1355 R1356 R1357 R1358 R1359 R1360 R1361 R1362 R1363 R1364 R1365 R1366 R1367 R1368 R1369 R1370 R1371 R1372 R1373 R1374 R1375 R1376 R1377 R1378 R1379 R1380 R1381 R1382 R1383 R1384 R1385 R1386 R1387 R1388 R1389 R1390 R1391 R1392 R1393 R1394 R1395 R1396 R1397 R1398 R1399 R1400 R1401 R1402 R1403 R1404 R1405 R1406 R1407 R1408 R1409 R1410 R1411 R1412 R1413 R1414 R1415 R1416 R1417 R1418 R1419 R1420 R1421 R1422 R1423 R1424 R1425 R1426 R1427 R1428 R1429 R1430 R1431 R1432 R1433 R1434 R1435 R1436 R1437 R1438 R1439 R1440 R1441 R1442 R1443 R1444 R1445 R1446 R1447 R1448 R1449 R1450 R1451 R1452 R1453 R1454 R1455 R1456 R1457 R1458 R1459 R1460 R1461 R1462 R1463 R1464 R1465 R1466 R1467 R1468 R1469 R1470 R1471 R1472 R1473 R1474 R1475 R1476 R1477 R1478 R1479 R1480 R1481 R1482 R1483 R1484 R1485 R1486 R1487 R1488 R1489 R1490 R1491 R1492 R1493 R1494 R1495 R1496 R1497 R1498 R1499 R1500 R1501 R1502 R1503 R1504 R1505 R1506 R1507 R1508 R1509 R1510 R1511 R1512 R1513 R1514 R1515 R1516 R1517 R1518 R1519 R1520 R1521 R1522 R1523 R1524 R1525 R1526 R1527 R1528 R1529 R1530 R1531 R1532 R1533 R1534 R1535 R1536 R1537 R1538 R1539 R1540 R1541 R1542 R1543 R1544 R1545 R1546 R1547 R1548 R1549 R1550 R1551 R1552 R1553 R1554 R1555 R1556 R1557 R1558 R1559 R1560 R1561 R1562 R1563 R1564 R1565 R1566 R1567 R1568 R1569 R1570 R1571 R1572 R1573 R1574 R1575 R1576 R1577 R1578 R1579 R1580 R1581 R1582 R1583 R1584 R1585 R1586 R1587 R1588 R1589 R1590 R1591 R1592 R1593 R1594 R1595 R1596 R1597 R1598 R1599 R1600 R1601 R1602 R1603 R1604 R1605 R1606 R1607 R1608 R1609 R1610 R1611 R1612 R1613 R1614 R1615 R1616 R1617 R1618 R1619 R1620 R1621 R1622 R1623 R1624 R1625 R1626 R1627 R1628 R1629 R1630 R1631 R1632 R1633 R1634 R1635 R1636 R1637 R1638 R1639 R1640 R1641 R1642 R1643 R1644 R1645 R1646 R1647 R1648 R1649 R1650 R1651 R1652 R1653 R1654 R1655 R1656 R1657 R1658 R1659 R1660 R1661 R1662 R1663 R1664 R1665 R1666 R1667 R1668 R1669 R1670 R1671 R1672 R1673 R1674 R1675 R1676 R1677 R1678 R1679 R1680 R1681 R1682 R1683 R1684 R1685 R1686 R1687 R1688 R1689 R1690 R1691 R1692 R1693 R1694 R1695 R1696 R1697 R1698 R1699 R1700 R1701 R1702 R1703 R1704 R1705 R1706 R1707 R1708 R1709 R1710 R1711 R1712 R1713 R1714 R1715 R1716 R1717 R1718 R1719 R1720 R1721 R1722 R1723 R1724 R1725 R1726 R1727 R1728 R1729 R1730 R1731 R1732 R1733 R1734 R1735 R1736 R1737 R1738 R1739 R1740 R1741 R1742 R1743 R1744 R1745 R1746 R1747 R1748 R1749 R1750 R1751 R1752 R1753 R1754 R1755 R1756 R1757 R1758 R1759 R1760 R1761 R1762 R1763 R1764 R1765 R1766 R1767 R1768 R1769 R1770 R1771 R1772 R1773 R1774 R1775 R1776 R1777 R1778 R1779 R1780 R1781 R1782 R1783 R1784 R1785 R1786 R1787 R1788 R1789 R1790 R1791 R1792 R1793 R1794 R1795 R1796 R1797 R1798 R1799 R1800 R1801 R1802 R1803 R1804 R1805 R1806 R1807 R1808 R1809 R1810 R1811 R1812 R1813 R1814 R1815 R1816 R1817 R1818 R1819 R1820 R1821 R1822 R1823 R1824 R1825 R1826 R1827 R1828 R1829 R1830 R1831 R1832 R1833 R1834 R1835 R1836 R1837 R1838 R1839 R1840 R1841 R1842 R1843 R1844 R1845 R1846 R1847 R1848 R1849 R1850 R1851 R1852 R1853 R1854 R1855 R1856 R1857 R1858 R1859 R1860 R1861 R1862 R1863 R1864 R1865 R1866 R1867 R1868 R1869 R1870 R1871 R1872 R1873 R1874 R1875 R1876 R1877 R1878 R1879 R1880 R1881 R1882 R1883 R1884 R1885 R1886 R1887 R1888 R1889 R1890 R1891 R1892 R1893 R1894 R1895 R1896 R1897 R1898 R1899 R1900 R1901 R1902 R1903 R1904 R1905 R1906 R1907 R1908 R1909 R1910 R1911 R1912 R1913 R1914 R1915 R1916 R1917 R1918 R1919 R1920 R1921 R1922 R1923 R1924 R1925 R1926 R1927 R1928 R1929 R1930 R1931 R1932 R1933 R1934 R1935 R1936 R1937 R1938 R1939 R1940 R1941 R1942 R1943 R1944 R1945 R1946 R1947 R1948 R1949 R1950 R1951 R1952 R1953 R1954 R1955 R1956 R1957 R1958 R1959 R1960 R1961 R1962 R1963 R1964 R1965 R1966 R1967 R1968 R1969 R1970 R1971 R1972 R1973 R1974 R1975 R1976 R1977 R1978 R1979 R1980 R1981 R1982 R1983 R1984 R1985 R1986 R1987 R1988 R1989 R1990 R1991 R1992 R1993 R1994 R1995 R1996 R1997 R1998 R1999 R2000 R2001 R2002 R2003 R2004 R2005 R2006 R2007 R2008 R2009 R2010 R2011 R2012 R2013 R2014 R2015 R2016 R2017 R2018 R2019 R2020 R2021 R2022 R2023 R2024 R2025 R2026 R2027 R2028 R2029 R2030 R2031 R2032 R2033 R2034 R2035 R2036 R2037 R2038 R2039 R2040 R2041 R2042 R2043 R2044 R2045 R2046 R2047 R2048 R2049 R2050 R2051 R2052 R2053 R2054 R2055 R2056 R2057 R2058 R2059 R2060 R2061 R2062 R2063 R2064 R2065 R2066 R2067 R2068 R2069 R2070 R2071 R2072 R2073 R2074 R2075 R2076 R2077 R2078 R2079 R2080 R2081 R2082 R2083 R2084 R2085 R2086 R2087 R2088 R2089 R2090 R2091 R2092 R2093 R2094 R2095 R2096 R2097 R2098 R2099 R2100 R2101 R2102 R2103 R2104 R2105 R2106 R2107 R2108 R2109 R2110 R2111 R2112 R2113 R2114 R2115 R2116 R2117 R2118 R2119 R2120 R2121 R2122 R2123 R2124 R2125 R2126 R2127 R2128 R2129 R2130 R2131 R2132 R2133 R2134 R2135 R2136 R2137 R2138 R2139 R2140 R2141 R2142 R2143 R2144 R2145 R2146 R2147 R2148 R2149 R2150 R2151 R2152 R2153 R2154 R2155 R2156 R2157 R2158 R2159 R2160 R2161 R2162 R2163 R2164 R2165 R2166 R2167 R2168 R2169 R2170 R2171 R2172 R2173 R2174 R2175 R2176 R2177 R2178 R2179 R2180 R2181 R2182 R2183 R2184 R2185 R2186 R2187 R2188 R2189 R2190 R2191 R2192 R2193 R2194 R2195 R2196 R2197 R2198 R2199 R2200 R2201 R2202 R2203 R2204 R2205 R2206 R2207 R2208 R2209 R2210 R2211 R2212 R2213 R2214 R2215 R2216 R2217 R2218 R2219 R2220 R2221 R2222 R2223 R2224 R2225 R2226 R2227 R2228 R2229 R2230 R2231 R2232 R2233 R2234 R2235 R2236 R2237 R2238 R2239 R2240 R2241 R2242 R2243 R2244 R2245 R2246 R2247 R2248 R2249 R2250 R2251 R2252 R2253 R2254 R2255 R2256 R2257 R2258 R2259 R2260 R2261 R2262 R2263 R2264 R2265 R2266 R2267 R2268 R2269 R2270 R2271 R2272 R2273 R2274 R2275 R2276 R2277 R2278 R2279 R2280 R2281 R2282 R2283 R2284 R2285 R2286 R2287 R2288 R2289 R2290 R2291 R2292 R2293 R2294 R2295 R2296 R2297 R2298 R2299 R2300 R2301 R2302 R2303 R2304 R2305 R2306 R230



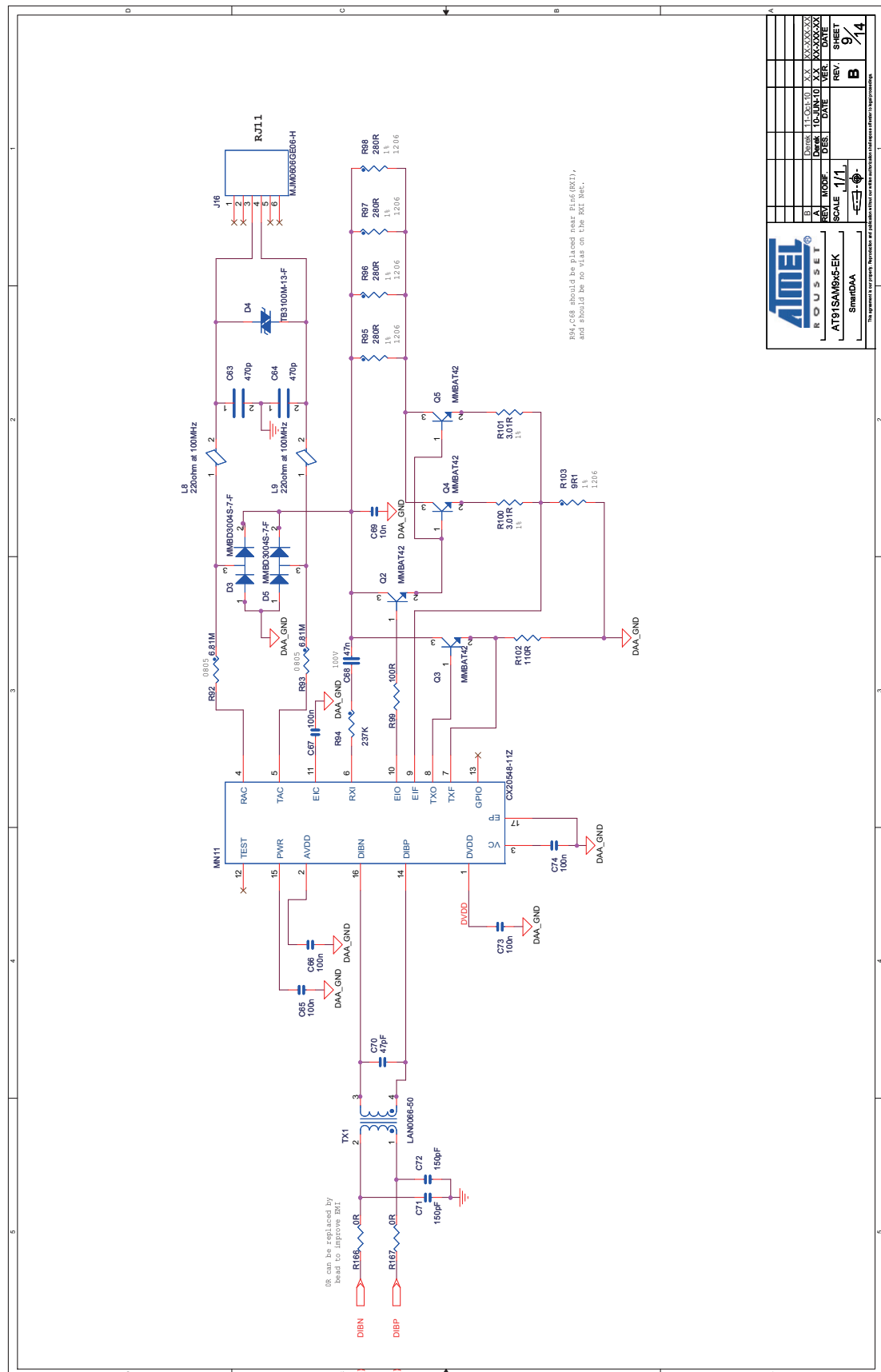
		AT91SAM95EK		SCALE 1/1	SHEET 3/4
REV.	DATE	REV.	DATE	REV.	DATE
1	11/05/10	1	11/05/10	1	11/05/10
2	11/05/10	2	11/05/10	2	11/05/10
3	11/05/10	3	11/05/10	3	11/05/10
4	11/05/10	4	11/05/10	4	11/05/10
5	11/05/10	5	11/05/10	5	11/05/10
6	11/05/10	6	11/05/10	6	11/05/10
7	11/05/10	7	11/05/10	7	11/05/10
8	11/05/10	8	11/05/10	8	11/05/10
9	11/05/10	9	11/05/10	9	11/05/10
10	11/05/10	10	11/05/10	10	11/05/10
11	11/05/10	11	11/05/10	11	11/05/10
12	11/05/10	12	11/05/10	12	11/05/10
13	11/05/10	13	11/05/10	13	11/05/10
14	11/05/10	14	11/05/10	14	11/05/10
15	11/05/10	15	11/05/10	15	11/05/10
16	11/05/10	16	11/05/10	16	11/05/10
17	11/05/10	17	11/05/10	17	11/05/10
18	11/05/10	18	11/05/10	18	11/05/10
19	11/05/10	19	11/05/10	19	11/05/10
20	11/05/10	20	11/05/10	20	11/05/10
21	11/05/10	21	11/05/10	21	11/05/10
22	11/05/10	22	11/05/10	22	11/05/10
23	11/05/10	23	11/05/10	23	11/05/10
24	11/05/10	24	11/05/10	24	11/05/10
25	11/05/10	25	11/05/10	25	11/05/10
26	11/05/10	26	11/05/10	26	11/05/10
27	11/05/10	27	11/05/10	27	11/05/10
28	11/05/10	28	11/05/10	28	11/05/10
29	11/05/10	29	11/05/10	29	11/05/10
30	11/05/10	30	11/05/10	30	11/05/10
31	11/05/10	31	11/05/10	31	11/05/10
32	11/05/10	32	11/05/10	32	11/05/10
33	11/05/10	33	11/05/10	33	11/05/10
34	11/05/10	34	11/05/10	34	11/05/10
35	11/05/10	35	11/05/10	35	11/05/10
36	11/05/10	36	11/05/10	36	11/05/10
37	11/05/10	37	11/05/10	37	11/05/10
38	11/05/10	38	11/05/10	38	11/05/10
39	11/05/10	39	11/05/10	39	11/05/10
40	11/05/10	40	11/05/10	40	11/05/10
41	11/05/10	41	11/05/10	41	11/05/10
42	11/05/10	42	11/05/10	42	11/05/10
43	11/05/10	43	11/05/10	43	11/05/10
44	11/05/10	44	11/05/10	44	11/05/10
45	11/05/10	45	11/05/10	45	11/05/10
46	11/05/10	46	11/05/10	46	11/05/10
47	11/05/10	47	11/05/10	47	11/05/10
48	11/05/10	48	11/05/10	48	11/05/10
49	11/05/10	49	11/05/10	49	11/05/10
50	11/05/10	50	11/05/10	50	11/05/10
51	11/05/10	51	11/05/10	51	11/05/10
52	11/05/10	52	11/05/10	52	11/05/10
53	11/05/10	53	11/05/10	53	11/05/10
54	11/05/10	54	11/05/10	54	11/05/10
55	11/05/10	55	11/05/10	55	11/05/10
56	11/05/10	56	11/05/10	56	11/05/10
57	11/05/10	57	11/05/10	57	11/05/10
58	11/05/10	58	11/05/10	58	11/05/10
59	11/05/10	59	11/05/10	59	11/05/10
60	11/05/10	60	11/05/10	60	11/05/10
61	11/05/10	61	11/05/10	61	11/05/10
62	11/05/10	62	11/05/10	62	11/05/10
63	11/05/10	63	11/05/10	63	11/05/10
64	11/05/10	64	11/05/10	64	11/05/10
65	11/05/10	65	11/05/10	65	11/05/10
66	11/05/10	66	11/05/10	66	11/05/10
67	11/05/10	67	11/05/10	67	11/05/10
68	11/05/10	68	11/05/10	68	11/05/10
69	11/05/10	69	11/05/10	69	11/05/10
70	11/05/10	70	11/05/10	70	11/05/10
71	11/05/10	71	11/05/10	71	11/05/10
72	11/05/10	72	11/05/10	72	11/05/10
73	11/05/10	73	11/05/10	73	11/05/10
74	11/05/10	74	11/05/10	74	11/05/10
75	11/05/10	75	11/05/10	75	11/05/10
76	11/05/10	76	11/05/10	76	11/05/10
77	11/05/10	77	11/05/10	77	11/05/10
78	11/05/10	78	11/05/10	78	11/05/10
79	11/05/10	79	11/05/10	79	11/05/10
80	11/05/10	80	11/05/10	80	11/05/10
81	11/05/10	81	11/05/10	81	11/05/10
82	11/05/10	82	11/05/10	82	11/05/10
83	11/05/10	83	11/05/10	83	11/05/10
84	11/05/10	84	11/05/10	84	11/05/10
85	11/05/10	85	11/05/10	85	11/05/10
86	11/05/10	86	11/05/10	86	11/05/10
87	11/05/10	87	11/05/10	87	11/05/10
88	11/05/10	88	11/05/10	88	11/05/10
89	11/05/10	89	11/05/10	89	11/05/10
90	11/05/10	90	11/05/10	90	11/05/10
91	11/05/10	91	11/05/10	91	11/05/10
92	11/05/10	92	11/05/10	92	11/05/10
93	11/05/10	93	11/05/10	93	11/05/10
94	11/05/10	94	11/05/10	94	11/05/10
95	11/05/10	95	11/05/10	95	11/05/10
96	11/05/10	96	11/05/10	96	11/05/10
97	11/05/10	97	11/05/10	97	11/05/10
98	11/05/10	98	11/05/10	98	11/05/10
99	11/05/10	99	11/05/10	99	11/05/10
100	11/05/10	100	11/05/10	100	11/05/10
101	11/05/10	101	11/05/10	101	11/05/10
102	11/05/10	102	11/05/10	102	11/05/10
103	11/05/10	103	11/05/10	103	11/05/10
104	11/05/10	104	11/05/10	104	11/05/10
105	11/05/10	105	11/05/10	105	11/05/10
106	11/05/10	106	11/05/10	106	11/05/10
107	11/05/10	107	11/05/10	107	11/05/10
108	11/05/10	108	11/05/10	108	11/05/10
109	11/05/10	109	11/05/10	109	11/05/10
110	11/05/10	110	11/05/10	110	11/05/10
111	11/05/10	111	11/05/10	111	11/05/10
112	11/05/10	112	11/05/10	112	11/05/10
113	11/05/10	113	11/05/10	113	11/05/10
114	11/05/10	114	11/05/10	114	11/05/10
115	11/05/10	115	11/05/10	115	11/05/10
116	11/05/10	116	11/05/10	116	11/05/10
117	11/05/10	117	11/05/10	117	11/05/10
118	11/05/10	118	11/05/10	118	11/05/10
119	11/05/10	119	11/05/10	119	11/05/10
120	11/05/10	120	11/05/10	120	11/05/10
121	11/05/10	121	11/05/10	121	11/05/10
122	11/05/10	122	11/05/10	122	11/05/10
123	11/05/10	123	11/05/10	123	11/05/10
124	11/05/10	124	11/05/10	124	11/05/10
125	11/05/10	125	11/05/10	125	11/05/10
126	11/05/10	126	11/05/10	126	11/05/10
127	11/05/10	127	11/05/10	127	11/05/10
128	11/05/10	128	11/05/10	128	11/05/10
129	11/05/10	129	11/05/10	129	11/05/10
130	11/05/10	130	11/05/10	130	11/05/10
131	11/05/10	131	11/05/10	131	11/05/10
132	11/05/10	132	11/05/10	132	11/05/10
133	11/05/10	133	11/05/10	133	11/05/10
134	11/05/10	134	11/05/10	134	11/05/10
135	11/05/10	135	11/05/10	135	11/05/10
136	11/05/10	136	11/05/10	136	11/05/10
137	11/05/10	137	11/05/10	137	11/05/10
138	11/05/10	138	11/05/10	138	11/05/10
139	11/05/10	139	11/05/10	139	11/05/10
140	11/05/10	140	11/05/10	140	11/05/10
141	11/05/10	141	11/05/10	141	11/05/10
142	11/05/10	142	11/05/10	142	11/05/10
143	11/05/10	143	11/05/10	143	11/05/10
144	11/05/10	144	11/05/10	144	11/05/10
145	11/05/10	145	11/05/10	145	11/05/10
146	11/05/10	146	11/05/10	146	11/05/10
147	11/05/10	147	11/05/10	147	11/05/10
148	11/05/10	148	11/05/10	148	11/05/10
149	11/05/10	149	11/05/10	149	11/05/10
150	11/05/10	150	11/05/10	150	11/05/10
151	11/05/10	151	11/05/10	151	11/05/10
152	11/05/10	152	11/05/10	152	11/05/10
153	11/05/10	153	11/05/10	153	11/05/10
154	11/05/10	154	11/05/10	154	11/05/10
155	11/05/10	155	11/05/10	155	11/05/10
156	11/05/10	156	11/05/10	156	11/05/10
157	11/05/10	157	11/05/10	157	11/05/10
158	11/05/10	158	11/05/10	158	11/05/10
159	11/05/10	159	11/05/10	159	11/05/10
160	11/05/10	160	11/05/10	160	11/05/10
161	11/05/10	161	11/05/10	161	11/05/10
162	11/05/10	162	11/05/10	162	11/05/10
163	11/05/10	163	11/05/10	163	11/05/10
164	11/05/10	164	11/05/10	164	11/05/10
165	11/05/10	165	11/05/10	165	11/05/10
166	11/05/10	166	11/05/10	166	11/05/10
167	11/05/10	167	11/05/10	167	11/05/10
168	11/05/10	168	11/05/10	168	11/05/10
169	11/05/10	169	11/05/10	169	11/05/10
170	11/05/10	170	11/05/10	170	11/05/10
171	11/05/10	171	11/05/10	171	11/05/10
172	11/05/10	172	11/05/10	172	11/05/10
173	11/05/10	173	11/05/10	173	11/05/10
174	11/05/10	174	11/05/10	174	11/05/10
175	11/05/10	175	11/05/10	175	11/05/10
176	11/05/10	176	11/05/10	176	11/05/10
177	11/05/10	177	11/05/10	177	11/05/10
178	11/05/10	178	11/05/10	178	11/05/10
179	11/05/10	179	11/05/10	179	11/05/10
180	11/05/10	180	11/05/10	180	11/05/10
181	11/05/10	181	11/05/10	181	11/05/10
182	11/05/10	182	11/05/10	182	11/05/10
183	11/05/10	183	11/05/10	183	11/05/10
184	11/05/10	184	11/05/10	184	11/05/10
185	11/05/10	185	11/05/10	185	11/05/10
186	11/05/10	186	11/05/10	186	11/05/10
187	11/05/10	187	11/05/10	187	11/05/10
188	11/05/10	188			

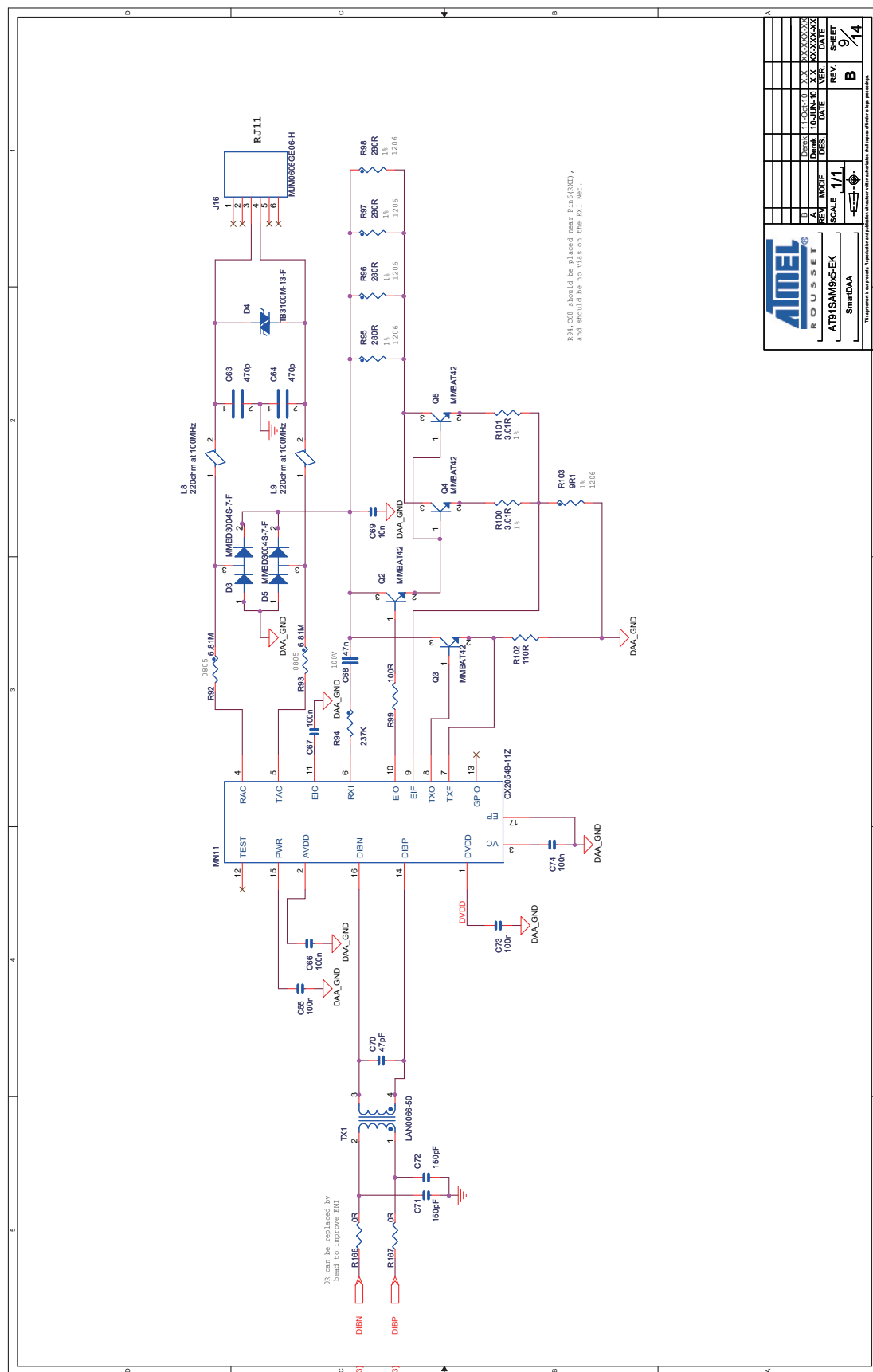


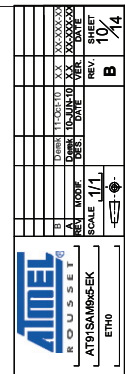
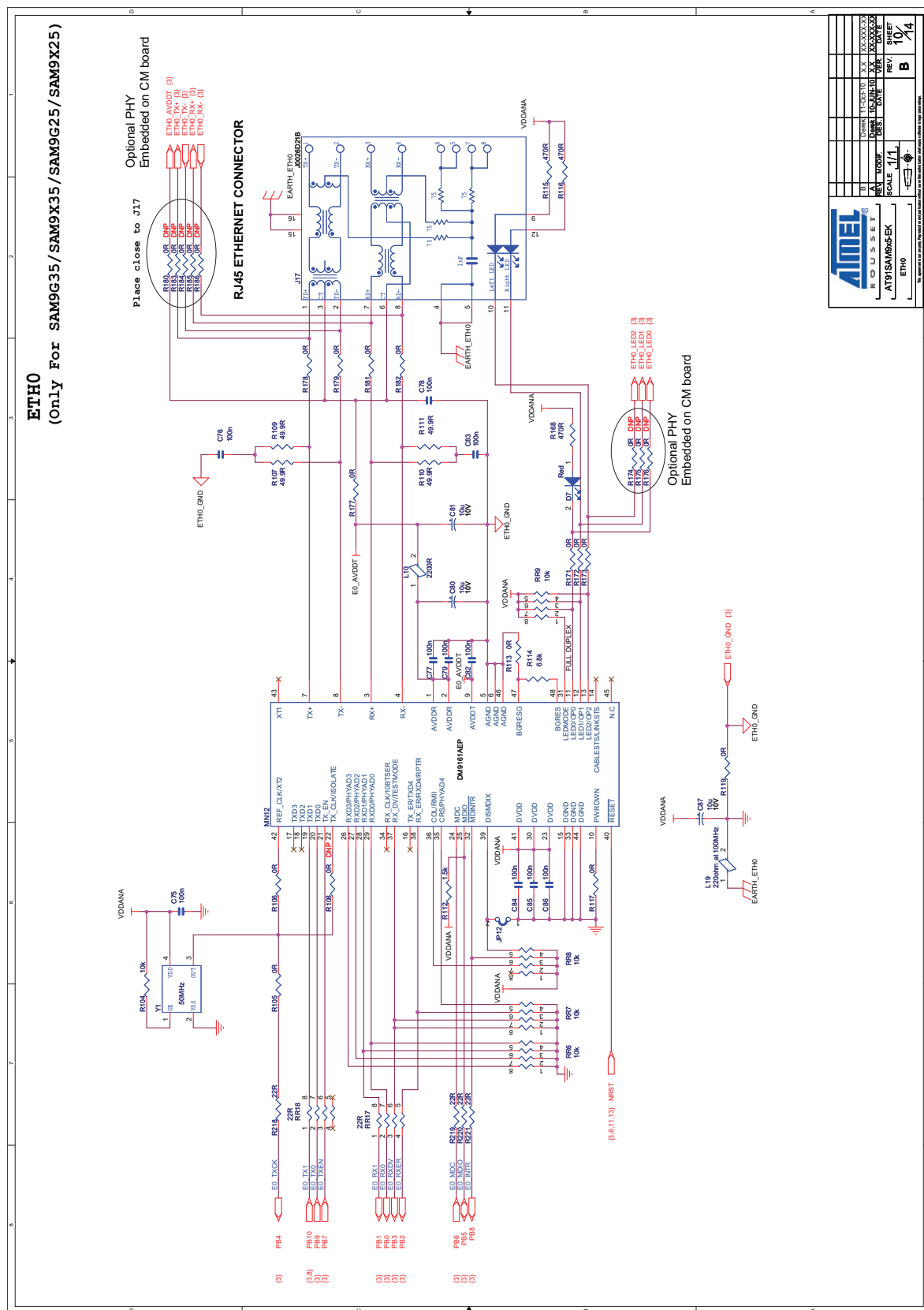


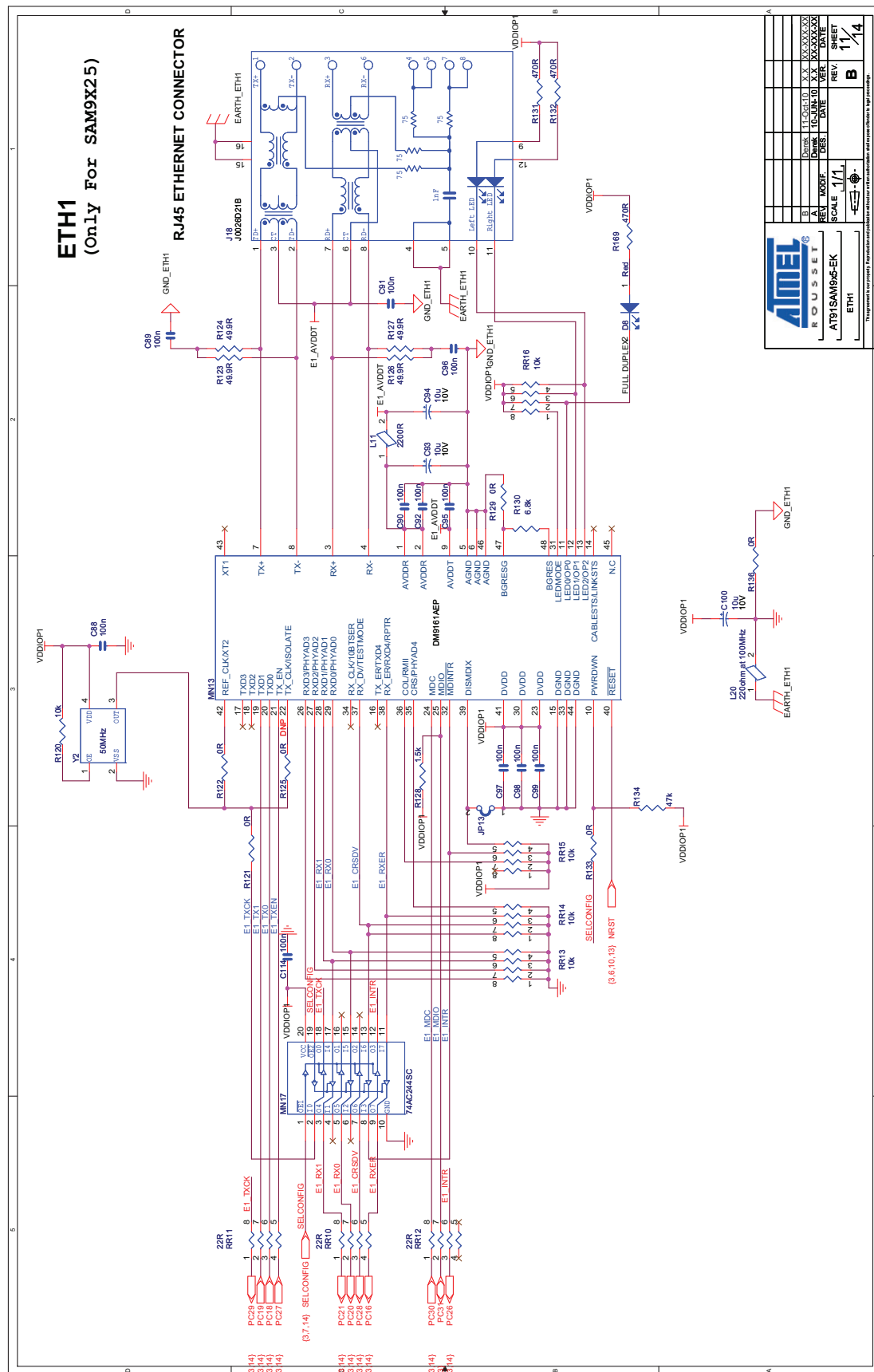








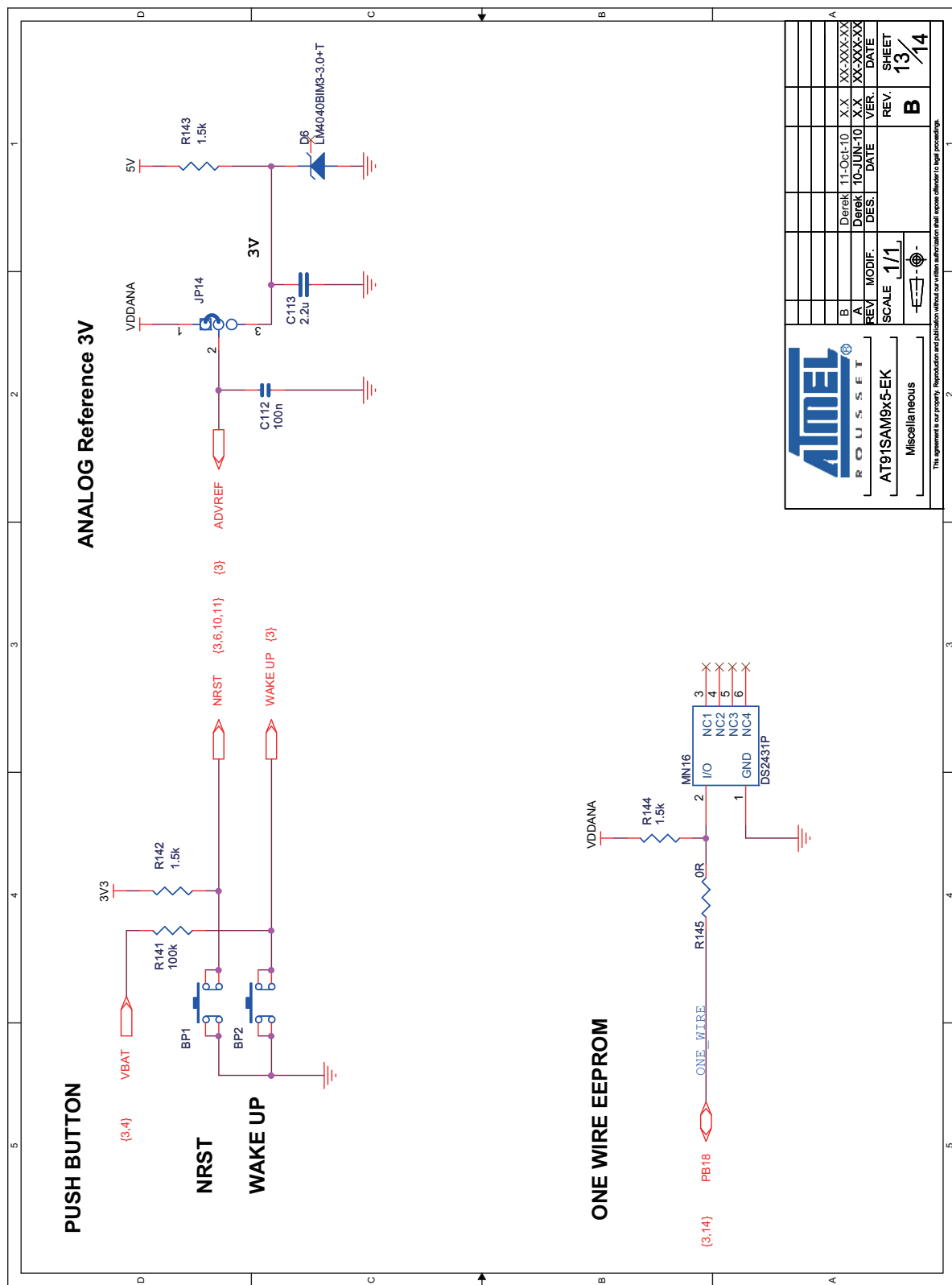




ATMEL		ROUSSET		REV. MODIF.		DATE		REV.		SHEET	
AT91SAM9X25-EK		1/1		1/1		1/1		1/1		1/1	
ETH1		B		1/1		1/1		1/1		1/1	









																			
AT91SAM9X5-EK										LCD & ISI									
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REV. MODIF.										DES. DATE									
A										11-Oct-10									
B										XX									
C										XX									
D										XX									
E										XX									
F										XX									
G										XX									
H										XX									
I										XX									
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K										XX									
L										XX									
M										XX									
N										XX									
O										XX									
P										XX									
Q										XX									
R										XX									
S										XX									
T										XX									
U										XX									
V										XX									
W										XX									
X										XX									
Y										XX									
Z										XX									
AA										XX									
AB										XX									
AC										XX									
AD										XX									
AE										XX									
AF										XX									
AG										XX									
AH										XX									
AI										XX									
AJ										XX									
AK										XX									
AL										XX									
AM										XX									
AN										XX									
AO										XX									
AP										XX									
AQ										XX									
AR										XX									
AS										XX									
AT										XX									
AU										XX									
AV										XX									
AW										XX									
AX										XX									
AY										XX									
AZ										XX									
BA										XX									
BB										XX									
BC										XX									
BD										XX									
BE										XX									
BF										XX									
BG										XX									
BH										XX									
BI										XX									
BJ										XX									
BK										XX									
BL										XX									
BM										XX									
BN										XX									
BO										XX									
BP										XX									
BQ										XX									
BR										XX									
BS										XX									
BT										XX									
BU										XX									
BV										XX									
BW										XX									
BX										XX									
BY										XX									
BZ										XX									
CA										XX									
CB										XX									
CC										XX									
CD										XX									
CE										XX									
CF										XX									
CG										XX									
CH										XX									
CI										XX									
CJ										XX									
CK										XX									
CL										XX									
CM										XX									
CN										XX									
CO										XX									
CP										XX									
CQ										XX									
CR										XX									
CS										XX									
CT										XX									
CU										XX									
CV										XX									
CW										XX									
CX										XX									
CY										XX									
CZ										XX									
DA										XX									
DB										XX									
DC										XX									
DD										XX									
DE										XX									
DF										XX									
DG										XX									
DH										XX									
DI										XX									
DJ										XX									
DK										XX									
DL										XX									
DM										XX									
DN										XX									
DO										XX									
DP										XX									
DQ										XX									
DR										XX									
DS										XX									
DT										XX									
DU										XX									
DV										XX									
DW										XX									
DX										XX									
DY										XX									
DZ										XX									
EA										XX									
EB										XX									
EC										XX									
ED										XX									

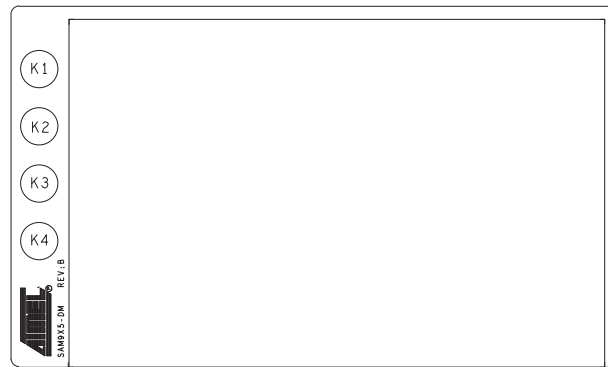
This assessment is not necessary. Recidivism and reoffending without a written authorisation shall amount to a criminal offence in itself.

4.4 Optional Display Module (DM) Board Hardware

4.4.1 DM Board Overview

The optional DM board carries a 5.0" TFT LCD module with touch screen. The DM board also carries four QTouch pads.

Figure 4-54. DM Board Layout



4.4.2 Equipment List

The list of the DM board components follows:

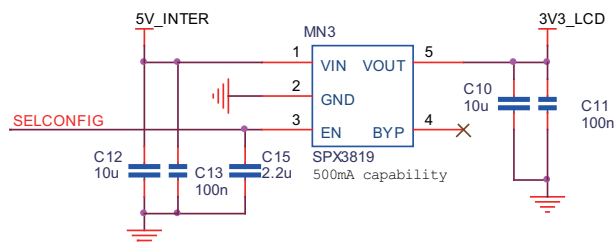
- One 5.0" TFT LCD module
- LCD Back light driver
- 3.3V regulator
- QTouch device
- 1-Wire device

4.4.3 Function Blocks

4.4.3.1 3.3V Regulator

The DM Board features its own LDO for local power regulation. It accepts DC 5V power from a 500 mA high-side power switch on the EK and outputs a regulated +3.3 V to most other circuits on the board.

Figure 4-55. DM Board Power Supply



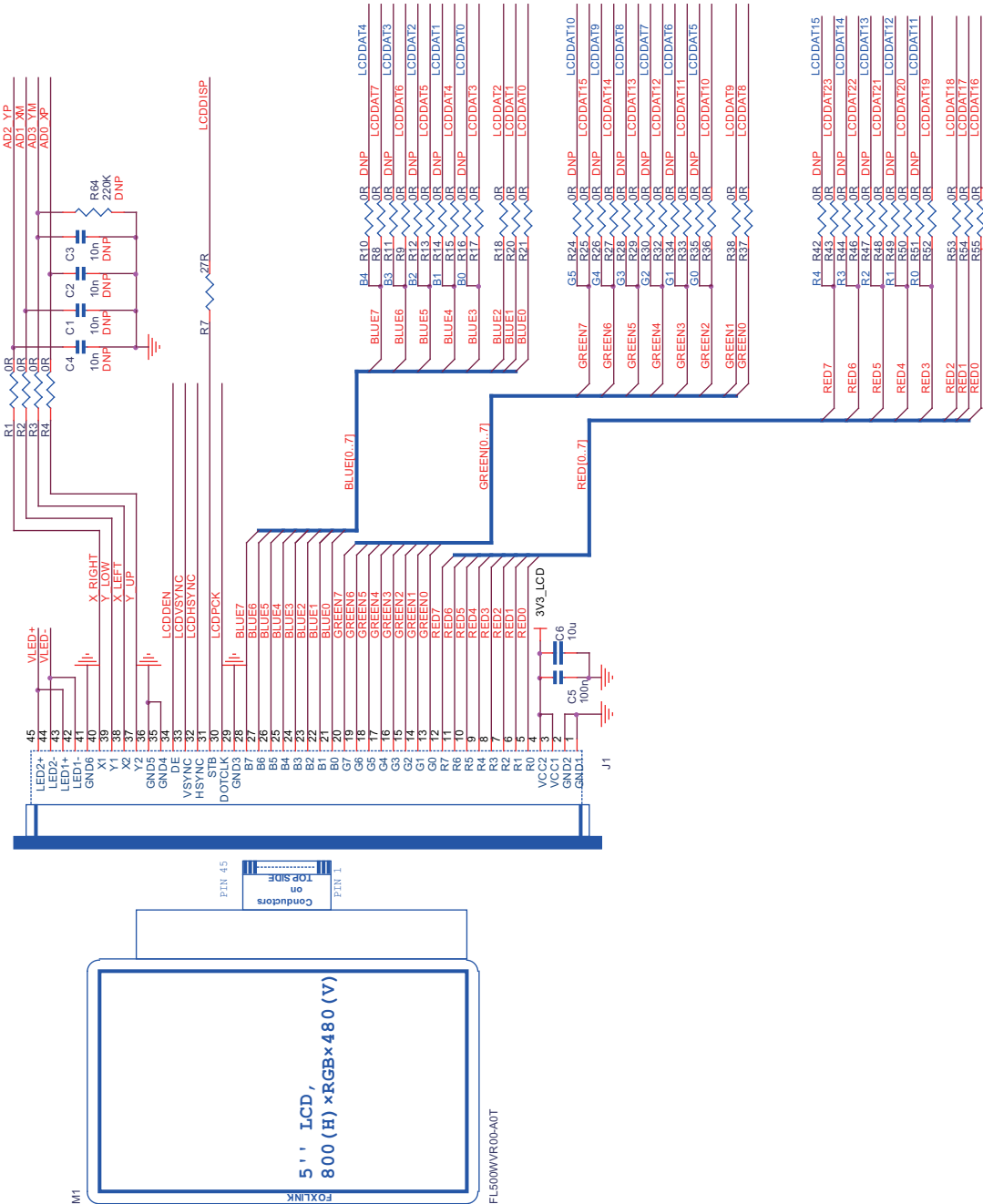
4.4.3.2 TFT LCD with Touch Panel

A 5" 800x480 LCD provides the DM with a low power display feature, back light unit and a touch panel, similar to that used on commercial PDAs.

Graphics and text can be displayed on the dot matrix panel with up to 16 million colors by supplying 24-bit data signals (8bit x RGB by default) or 16-bit data signals (5+6+5bit x RGB in option). This allows the user to develop graphical user interfaces for a wide variety of end applications.

Warning: Never connect/disconnect the LCD display from the board while the power supply is on. Doing so may damage both units.

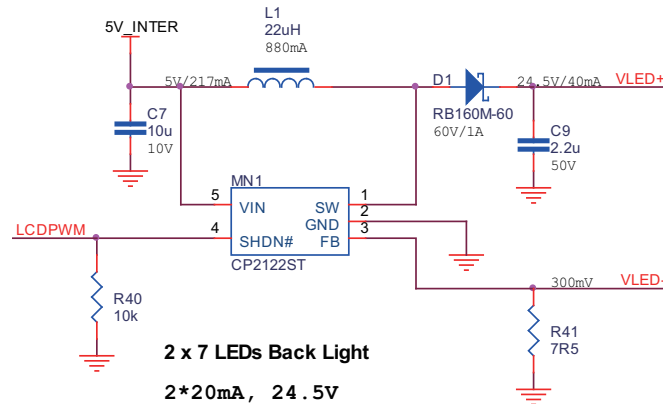
Figure 4-56. LCD with Touch Panel



4.4.3.3 Back Light

The back light voltage is generated from a CP2122ST boost converter. It is powered directly by the DC 5V from the EK board. The back light level is controlled by a PWM signal generated from the MPU Device processor.

Figure 4-57. Back Light Control

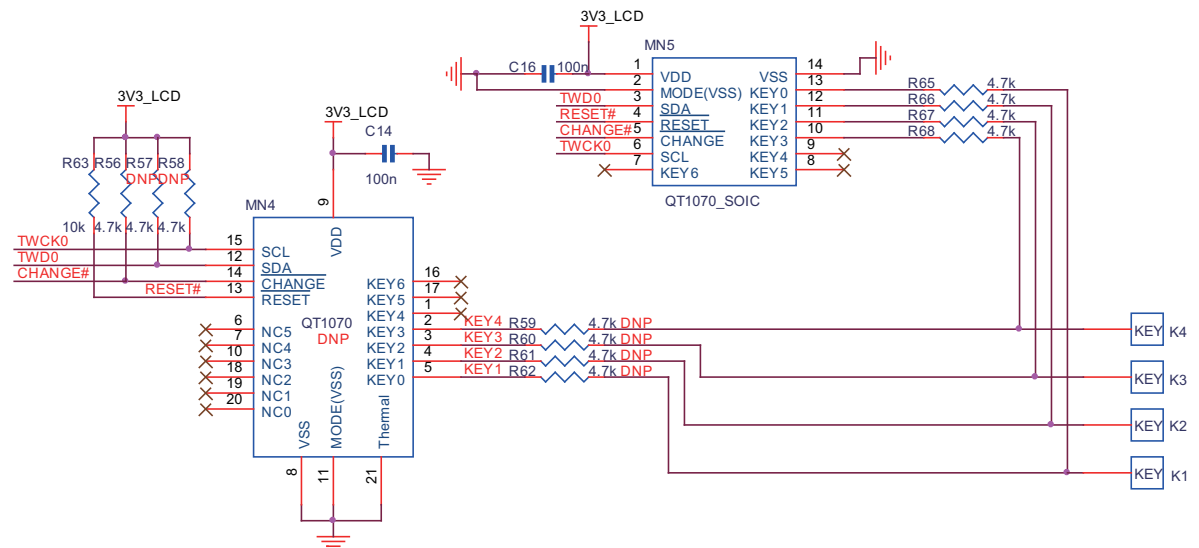


4.4.3.4 QTouch

The DM board carries a QTouch device piloted through a TWI interface. It manages four capacitive touch buttons directly printed on the PCB.

There are dual footprints for the QTouch device, and SOIC is the default mounted one.

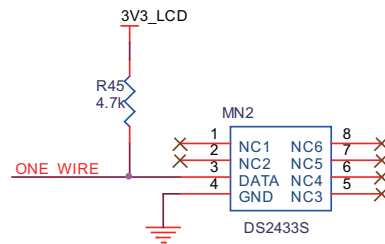
Figure 4-58. QTouch



4.4.3.5 1-Wire

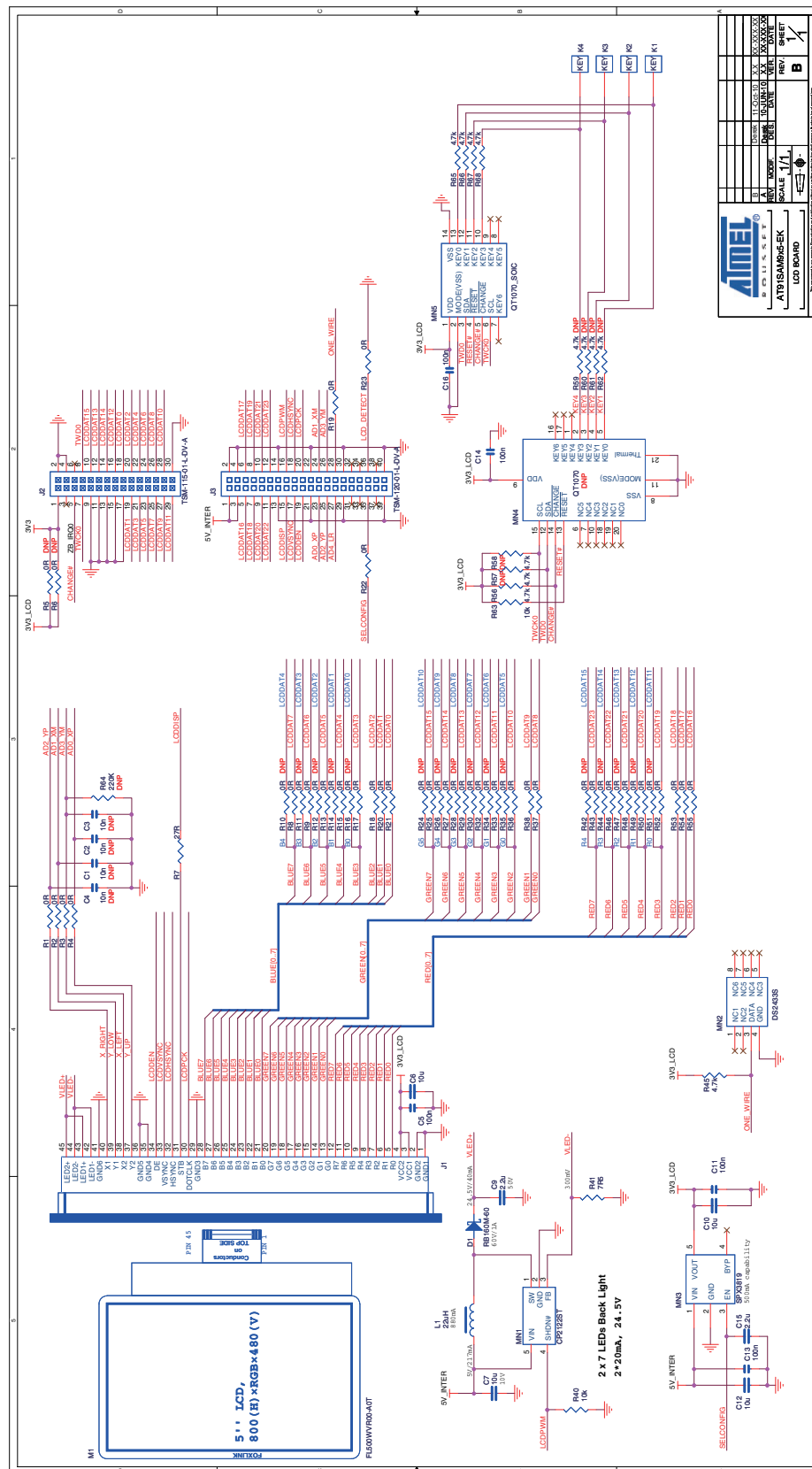
The DM board also uses a 1-Wire device as “firmware label” to store the information such as chip type, manufacturer’s name, production date etc.

Figure 4-59. 1-Wire on DM



4.4.4 Schematics

Figure 4-60. DM Board Schematics





Section 5

Revision History

5.1 Revision History

Table 5-1.

Document	Comments	Change Request Ref.
11115A	First issue.	



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