

FDS6676AS

30V N-Channel PowerTrench® SyncFET™

General Description

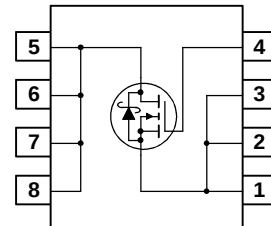
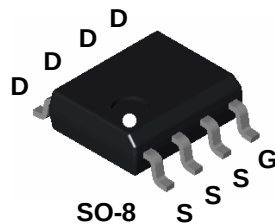
The FDS6676AS is designed to replace a single SO-8 MOSFET and Schottky diode in synchronous DC:DC power supplies. This 30V MOSFET is designed to maximize power conversion efficiency, providing a low $R_{DS(ON)}$ and low gate charge. The FDS6676AS includes an integrated Schottky diode using Fairchild's monolithic SyncFET technology.

Applications

- DC/DC converter
- Low side notebook

Features

- 14.5 A, 30 V. $R_{DS(ON)}$ max= 6.0 m Ω @ $V_{GS} = 10$ V
 $R_{DS(ON)}$ max= 7.25 m Ω @ $V_{GS} = 4.5$ V
- Includes SyncFET Schottky body diode
- Low gate charge (45nC typical)
- High performance trench technology for extremely low $R_{DS(ON)}$ and fast switching
- High power and current handling capability



Absolute Maximum Ratings

$T_A=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous (Note 1a) – Pulsed	14.5	A
		50	
P_D	Power Dissipation for Single Operation (Note 1a) (Note 1b) (Note 1c)	2.5	W
		1.2	
		1	
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to $+150$	$^\circ\text{C}$

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1a)	50	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case (Note 1)	25	

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
FDS6676AS	FDS6676AS	13"	12mm	2500 units
FDS6676AS	FDS6676AS_NL (Note 3)	13"	12mm	2500 units

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

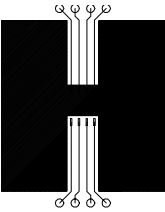
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain–Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 1\text{ mA}$, Referenced to 25°C		28		mV/ $^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}$			500	μA
I_{GSS}	Gate–Body Leakage	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$			± 100	nA
On Characteristics (Note 2)						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 1\text{ mA}$	1	1.5	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 1\text{ mA}$, Referenced to 25°C		–3.3		mV/ $^\circ\text{C}$
$R_{DS(on)}$	Static Drain–Source On–Resistance	$V_{GS} = 10\text{ V}, I_D = 14.5\text{ A}$ $V_{GS} = 4.5\text{ V}, I_D = 13.2\text{ A}$ $V_{GS} = 10\text{ V}, I_D = 14.5\text{ A}, T_J = 125^\circ\text{C}$		4.5 5.9 6.7	6.0 7.25 8.5	m Ω
$I_{D(on)}$	On–State Drain Current	$V_{GS} = 10\text{ V}, V_{DS} = 5\text{ V}$	50			A
g_{FS}	Forward Transconductance	$V_{DS} = 10\text{ V}, I_D = 14.5\text{ A}$		66		S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$		2510		pF
C_{oss}	Output Capacitance			710		pF
C_{rss}	Reverse Transfer Capacitance			270		pF
R_G	Gate Resistance	$V_{GS} = 15\text{ mV}, f = 1.0\text{ MHz}$		1.6	2.8	Ω
Switching Characteristics (Note 2)						
$t_{d(on)}$	Turn–On Delay Time	$V_{DD} = 15\text{ V}, I_D = 1\text{ A},$ $V_{GS} = 10\text{ V}, R_{GEN} = 6\text{ }\Omega$		10	20	ns
t_r	Turn–On Rise Time			12	22	ns
$t_{d(off)}$	Turn–Off Delay Time			43	69	ns
t_f	Turn–Off Fall Time			29	46	ns
$t_{d(on)}$	Turn–On Delay Time	$V_{DD} = 15\text{ V}, I_D = 1\text{ A},$ $V_{GS} = 4.5\text{ V}, R_{GEN} = 6\text{ }\Omega$		17	31	ns
t_r	Turn–On Rise Time			22	35	ns
$t_{d(off)}$	Turn–Off Delay Time			34	54	ns
t_f	Turn–Off Fall Time			29	46	ns
$Q_{g(TOT)}$	Total Gate Charge at $V_{GS}=10\text{V}$	$V_{DD} = 15\text{ V}, I_D = 14.5\text{ A},$		45	63	nC
Q_g	Total Gate Charge at $V_{GS}=5\text{V}$			25	35	nC
Q_{gs}	Gate–Source Charge			7		nC
Q_{gd}	Gate–Drain Charge			8		nC

Electrical Characteristics

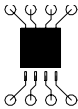
T_A = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Drain–Source Diode Characteristics and Maximum Ratings						
V _{SD}	Drain–Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 3.5 A (Note 2) V _{GS} = 0 V, I _S = 7 A (Note 2)		0.4 0.5	0.7	V
t _{rr}	Diode Reverse Recovery Time	I _F = 14.5A, dI _F /dt = 300 A/μs (Note 3)		27		nS
I _{RM}	Diode Reverse Recovery Current			1.9		A
Q _{rr}	Diode Reverse Recovery Charge			26		nC

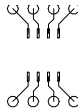
Notes:
1. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{θJC} is guaranteed by design while R_{θCA} is determined by the user's board design.



a) 50°/W when mounted on a 1 in² pad of 2 oz copper



b) 105°/W when mounted on a .04 in² pad of 2 oz copper



c) 125°/W when mounted on a minimum pad.

See "SyncFET Schottky body diode characteristics" below

- Scale 1 : 1 on letter size paper
2. Pulse Test: Pulse Width < 300μs, Duty Cycle < 2.0%
3. FDS6676AS_NL is a lead free product. The FDS6676AS_NL marking will appear on the reel label.

Typical Characteristics

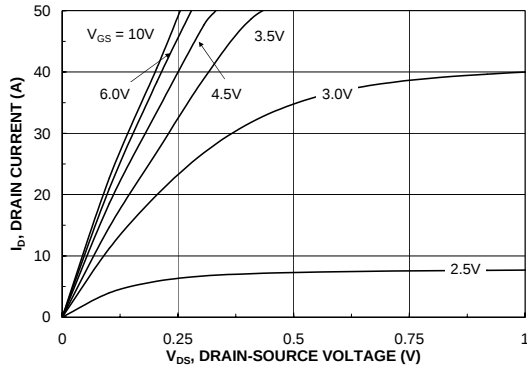


Figure 1. On-Region Characteristics.

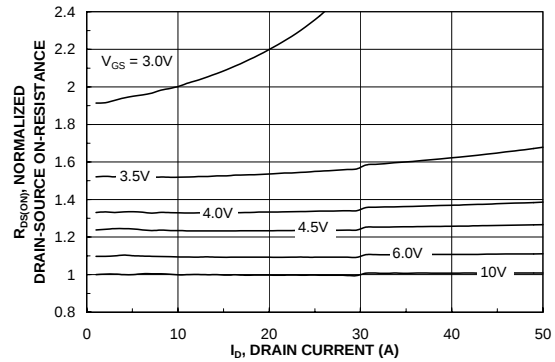


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

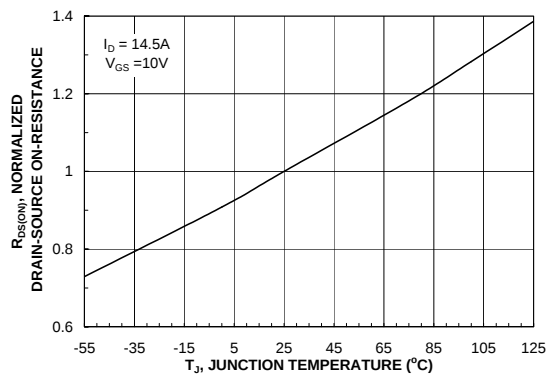


Figure 3. On-Resistance Variation with Temperature.

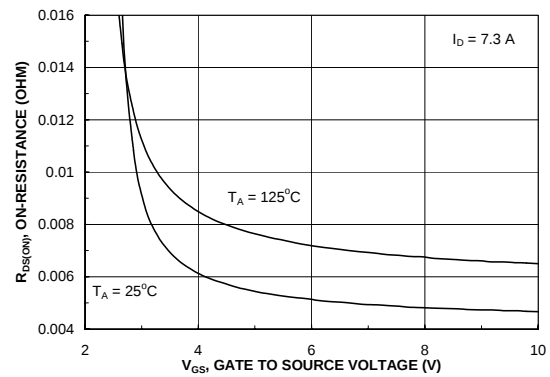


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

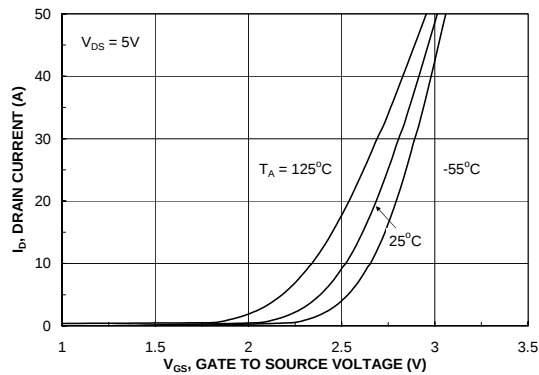


Figure 5. Transfer Characteristics.

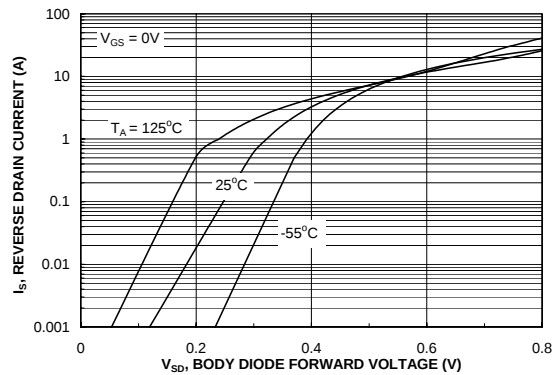


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics (continued)

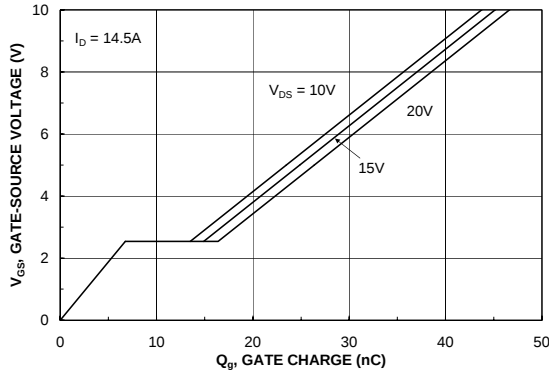


Figure 7. Gate Charge Characteristics.

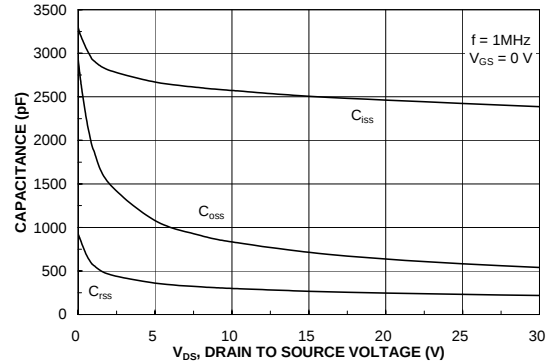


Figure 8. Capacitance Characteristics.

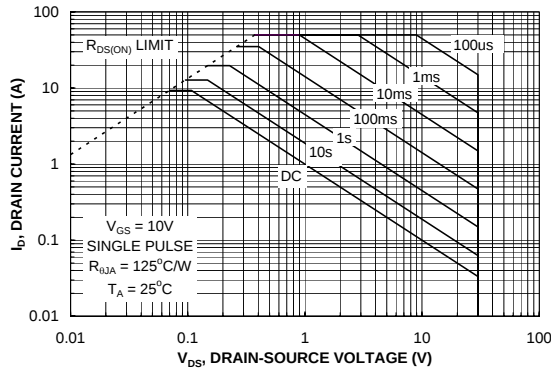


Figure 9. Maximum Safe Operating Area.

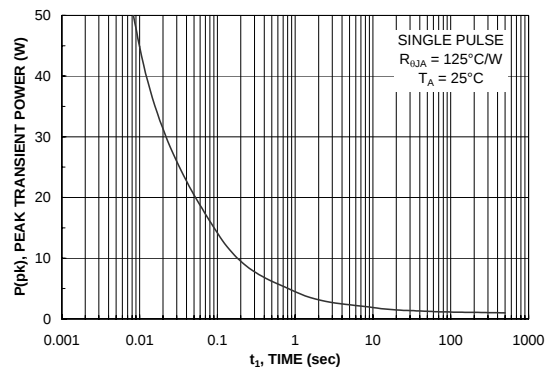


Figure 10. Single Pulse Maximum Power Dissipation.

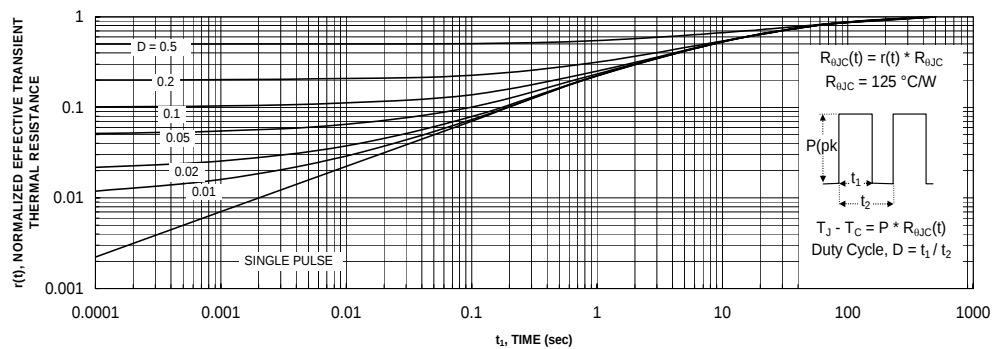


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c.
Transient thermal response will change depending on the circuit board design.

Typical Characteristics (continued)

SyncFET Schottky Body Diode Characteristics

Fairchild's SyncFET process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 12 shows the reverse recovery characteristic of the FDS6676AS.

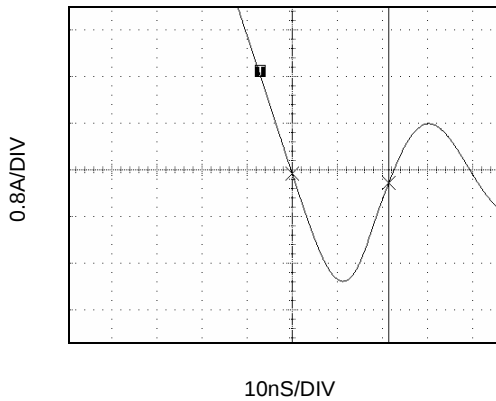


Figure 12. FDS6676AS SyncFET body diode reverse recovery characteristic.

For comparison purposes, Figure 13 shows the reverse recovery characteristics of the body diode of an equivalent size MOSFET produced without SyncFET (FDS6676).

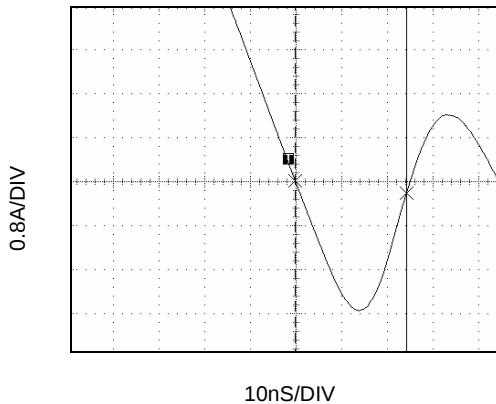


Figure 13. Non-SyncFET (FDS6676) body diode reverse recovery characteristic.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

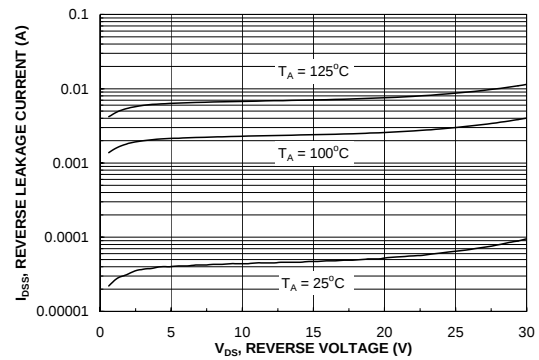


Figure 14. SyncFET body diode reverse leakage versus drain-source voltage and temperature.

Typical Characteristics

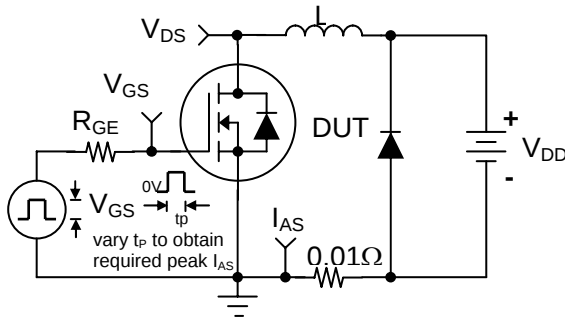


Figure 15. Unclamped Inductive Load Test Circuit

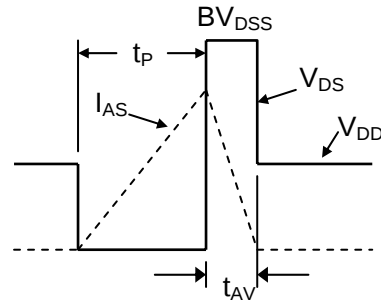


Figure 16. Unclamped Inductive Waveforms

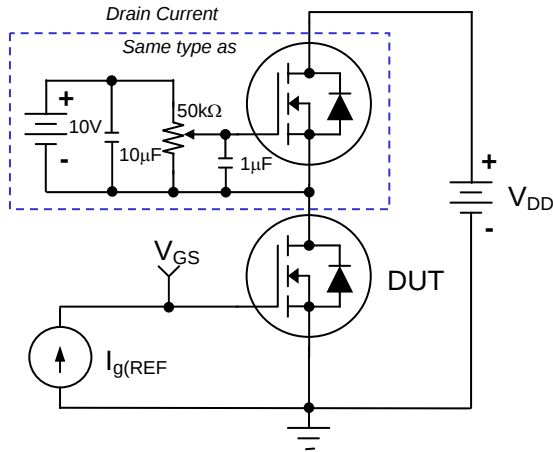


Figure 17. Gate Charge Test Circuit

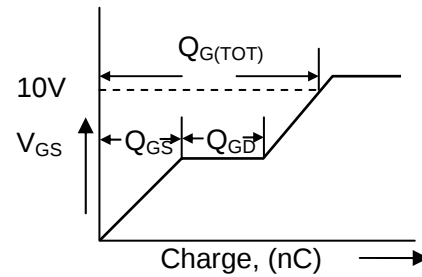


Figure 18. Gate Charge Waveform

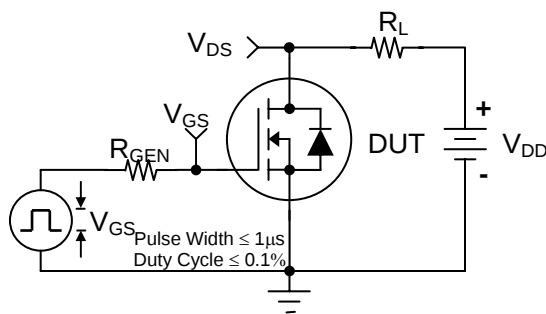


Figure 19. Switching Time Test Circuit

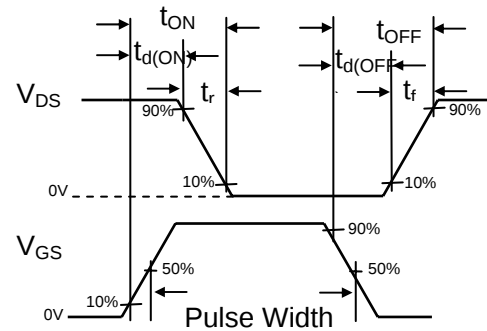


Figure 20. Switching Time Waveforms

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE ^{Ex} TM	FAST [®]	ISOPLANAR TM	Power247 TM	Stealth TM
ActiveArray TM	FAST ^r TM	LittleFET TM	PowerEdge TM	SuperFET TM
Bottomless TM	FPS TM	MICROCOUPLER TM	PowerSaver TM	SuperSOT TM -3
CoolFET TM	FRFET TM	MicroFET TM	PowerTrench [®]	SuperSOT TM -6
CROSSVOLT TM	GlobalOptoisolator TM	MicroPak TM	QFET [®]	SuperSOT TM -8
DO ^{ME} TM	GTO TM	MICROWIRE TM	QS TM	SyncFET TM
EcoSPARK TM	HiSeC TM	MSX TM	QT Optoelectronics TM	TinyLogic [®]
E ² CMOS TM	I ² C TM	MSXPro TM	Quiet Series TM	TINYOPTO TM
EnSigna TM	i-Lo TM	OCX TM	RapidConfigure TM	TruTranslation TM
FACT TM	ImpliedDisconnect TM	OCXPro TM	RapidConnect TM	UHC TM
FACT Quiet Series TM		OPTOLOGIC [®]	μSerDes TM	UltraFET [®]
Across the board. Around the world. TM		OPTOPLANAR TM	SILENT SWITCHER [®]	VCX TM
The Power Franchise [®]		PACMAN TM	SMART START TM	
Programmable Active Droop TM		POP TM	SPM TM	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.



**Стандарт
Электрон
Связь**

Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

Благодаря сотрудничеству с мировыми поставщиками мы осуществляем комплексные и плановые поставки широчайшего спектра электронных компонентов.

Собственная эффективная логистика и склад в обеспечивает надежную поставку продукции в точно указанные сроки по всей России.

Мы осуществляем техническую поддержку нашим клиентам и предпродажную проверку качества продукции. На все поставляемые продукты мы предоставляем гарантию .

Осуществляем поставки продукции под контролем ВП МО РФ на предприятия военно-промышленного комплекса России , а также работаем в рамках 275 ФЗ с открытием отдельных счетов в уполномоченном банке. Система менеджмента качества компании соответствует требованиям ГОСТ ISO 9001.

Минимальные сроки поставки, гибкие цены, неограниченный ассортимент и индивидуальный подход к клиентам являются основой для выстраивания долгосрочного и эффективного сотрудничества с предприятиями радиоэлектронной промышленности, предприятиями ВПК и научно-исследовательскими институтами России.

С нами вы становитесь еще успешнее!

Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург,
Промышленная ул, дом № 19, литера Н,
помещение 100-Н Офис 331