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April 2016

FGH50T65SQD 650 V, 50 A Field Stop Trench IGBT

Features

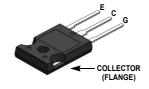
- Maximum Junction Temperature : T_J =175°C
- · Positive Temperaure Co-efficient for Easy Parallel Operating
- · High Current Capability
- Low Saturation Voltage: V_{CE(sat)} =1.6 V(Typ.) @ I_C = 50 A
- 100% of the Parts Tested for I_{LM}(1)
- · High Input Impedance
- Fast Switching
- · Tighten Parameter Distribution
- · RoHS Compliant

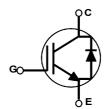
General Description

Using novel field stop IGBT technology, Fairchild's new series of field stop 4th generation IGBTs offer the optimum performance for solar inverter, UPS, welder, telecom, ESS and PFC applications where low conduction and switching losses are essential.

Applications

· Solar Inverter, UPS, Welder, Telecom, ESS, PFC





Absolute Maximum Ratings

Symbol	Description		FGH50T65SQD_F155	Unit	
V _{CES}	Collector to Emitter Voltage		650	V	
V	Gate to Emitter Voltage		± 20	V	
V_{GES}	Transient Gate to Emitter Voltage		± 30	V	
I _C	Collector Current	@ T _C = 25°C	100	Α	
l C	Collector Current	@ T _C = 100°C	50	Α	
I _{LM (1)}	Pulsed Collector Current	@ T _C = 25°C	200	Α	
I _{CM (2)}	Pulsed Collector Current		200	Α	
I _F	Diode Forward Current	@ T _C = 25°C	50	Α	
'F	Diode Forward Current	@ T _C = 100°C	30	Α	
I _{FM}	Pulsed Diode Maximum Forward Currer	t	200	Α	
P _D	Maximum Power Dissipation	@ T _C = 25°C	268	W	
. 0	Maximum Power Dissipation	@ T _C = 100°C	134	W	
T _J	Operating Junction Temperature		-55 to +175	°C	
T _{stg}	Storage Temperature Range		-55 to +175	°C	
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C	

- Notes: 1. V $_{CC}$ = 400 V, V $_{GE}$ = 15 V, I $_{C}$ = 200 A, R $_{G}$ = 3 $\Omega,$ Inductive Load
- 2. Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	FGH50T65SQD_F155	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case, Max.	0.56	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case, Max.	1.25	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Qty per Tube
FGH50T65SQD_F155	FGH50T65SQD	TO-247 G03	Tube	-	-	30

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV _{CES}	Collector to Emitter Breakdown Voltage	V_{GE} = 0V, I_C = 1 mA	650	-	-	V
ΔBV_{CES} / ΔT_{J}	Temperature Coefficient of Breakdown Voltage	I _C = 1 mA, Reference to 25°C	-	0.6	-	V/°C
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0 V$	-	-	250	μΑ
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0 V$	-	-	±400	nA
On Charac	teristics					
V _{GE(th)}	G-E Threshold Voltage	I_C = 50 mA, V_{CE} = V_{GE}	2.6	4.5	6.4	V
		I _C = 50 A, V _{GE} = 15 V, T _C = 25°C	-	1.6	2.1	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 50 A, V _{GE} = 15 V, T _C = 175°C	-	1.92	-	V
Dynamic C	haracteristics				l	
C _{ies}	Input Capacitance		-	3275	-	pF
C _{oes}	Output Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$	-	84	-	pF
C _{res}	Reverse Transfer Capacitance	f = 1MHz	-	12	-	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time		-	22	-	ns
t _r	Rise Time		-	8.7	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{CC} = 400 V, I _C = 12.5 A,	-	105	-	ns
t _f	Fall Time	$R_G = 4.7 \Omega$, $V_{GE} = 15 V$,	-	2.5	-	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 25°C	-	180	-	uJ
E _{off}	Turn-Off Switching Loss		-	45	-	uJ
E _{ts}	Total Switching Loss		-	225	-	uJ
t _{d(on)}	Turn-On Delay Time		-	19	-	ns
t _r	Rise Time] [-	13	-	ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 400 \text{ V}, I_{C} = 25 \text{ A},$ $R_{G} = 4.7 \Omega, V_{GE} = 15 \text{ V},$	-	93	-	ns
t _f	Fall Time		-	6.4	-	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 25°C	-	410	-	uJ
E _{off}	Turn-Off Switching Loss		-	88	-	uJ
E _{ts}	Total Switching Loss] [-	498	-	uJ

Electrical Characteristics of the IGBT (Continued)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Unit
t _{d(on)}	Turn-On Delay Time		-	20	-	ns
t _r	Rise Time		-	9.8	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{CC} = 400 V, I _C = 12.5 A,	-	116	-	ns
t _f	Fall Time	$R_G = 4.7 \Omega, V_{GE} = 15 V,$	-	3.5	-	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 175°C	-	402	-	uJ
E _{off}	Turn-Off Switching Loss		-	110	-	uJ
E _{ts}	Total Switching Loss		-	512	-	uJ
t _{d(on)}	Turn-On Delay Time		-	18	-	ns
t _r	Rise Time		-	15	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{CC} = 400 V, I _C = 25 A,	-	102	-	ns
t _f	Fall Time	$R_G = 4.7 \Omega$, $V_{GE} = 15 V$,	-	8	-	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 175°C	-	641	-	uJ
E _{off}	Turn-Off Switching Loss		-	203	-	uJ
E _{ts}	Total Switching Loss		-	844	-	uJ
Qg	Total Gate Charge	V 400 V 1 50 A	-	99	-	nC
Q _{ge}	Gate to Emitter Charge	V _{CE} = 400 V, I _C = 50 A, V _{GE} = 15 V	-	17	-	nC
Q _{gc}	Gate to Collector Charge	- GE :-:	-	23	-	nC

Electrical Characteristics of the Diode T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Unit
V _{FM}	Diode Forward Voltage	IF = 30 A	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	2.2	2.6	V
FINI			$T_{\rm C}$ = 175°C	-	1.9	-	
E _{rec}	Reverse Recovery Energy		$T_{\rm C}$ = 175°C	-	40	-	uJ
t _{rr}	Diode Reverse Recovery Time	200 A/μs	T _C = 25°C	-	31	-	ns
पा			T _C = 175°C	-	207	-	110
Q _{rr}	Diode Reverse Recovery Charge		T _C = 25°C	-	48	-	nC
≪rr	Diago Novolog Neesovery Charge		T _C = 175°C	-	820	-	

Figure 1. Typical Output Characteristics

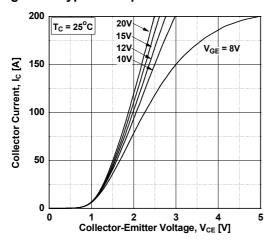


Figure 3. Typical Saturation Voltage Characteristics

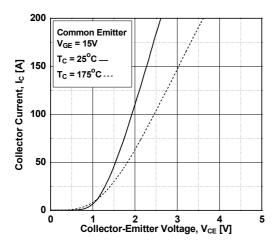


Figure 5. Saturation Voltage vs. V_{GE}

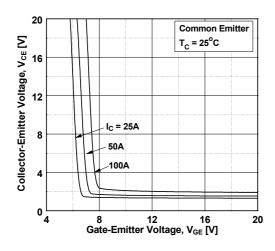


Figure 2. Typical Output Characteristics

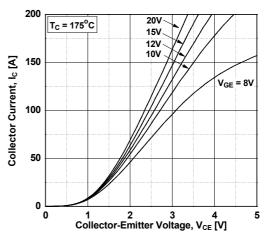


Figure 4. Saturation Voltage vs. Case
Temperature at Variant Current Level

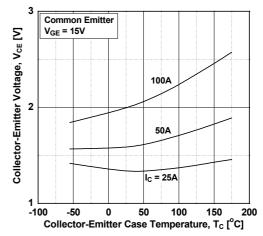


Figure 6. Saturation Voltage vs. V_{GE}

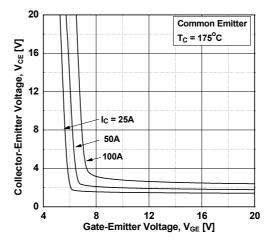


Figure 7. Capacitance Characteristics

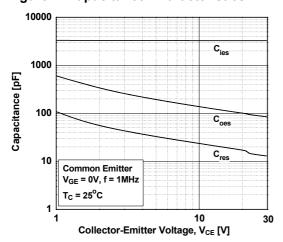


Figure 9. Turn-on Characteristics vs.
Gate Resistance

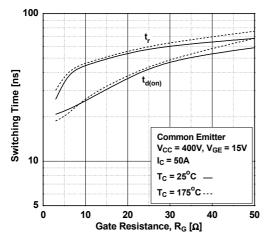


Figure 11. Switching Loss vs.
Gate Resistance

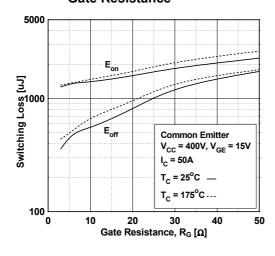


Figure 8. Gate charge Characteristics

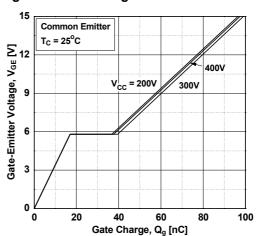


Figure 10. Turn-off Characteristics vs. Gate Resistance

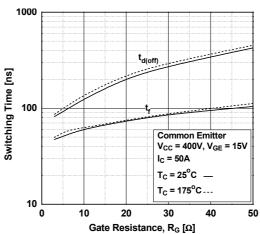


Figure 12. Turn-on Characteristics vs. Collector Current

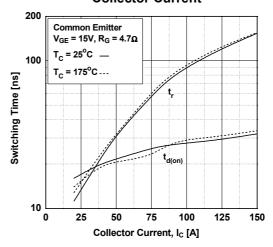
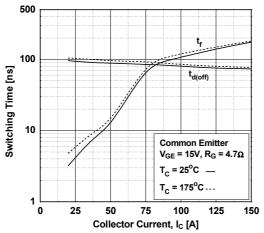


Figure 13. Turn-off Characteristics vs. Collector Current



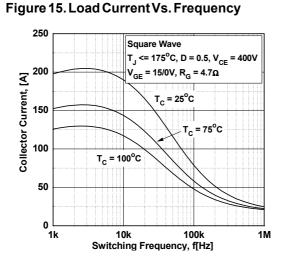


Figure 17. Forward Characteristics

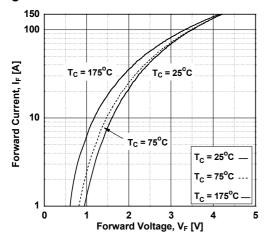


Figure 14. Switching Loss vs. Collector Current

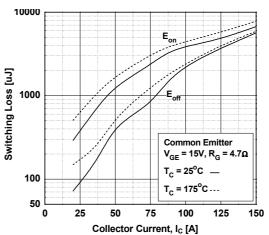


Figure 16. SOA Characteristics

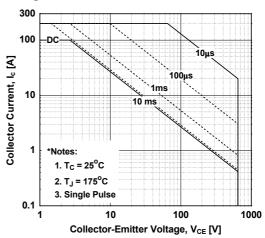


Figure 18. Reverse Recovery Current

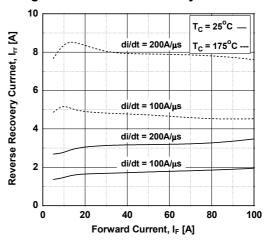


Figure 19. Reverse Recovery Time

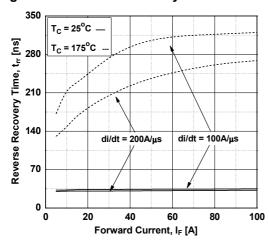


Figure 20. Stored Charge

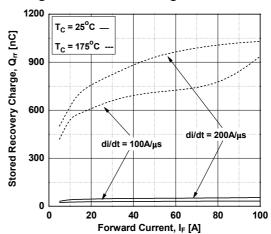


Figure 21.Transient Thermal Impedance of IGBT

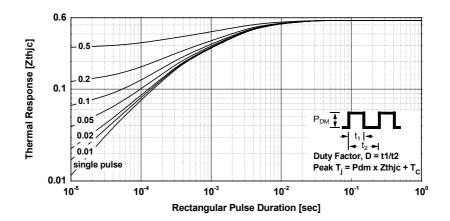
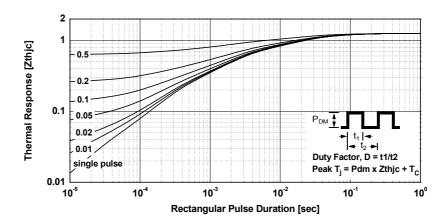
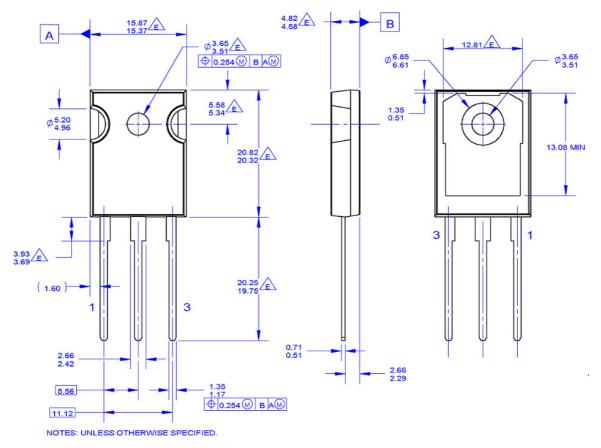


Figure 22. Transient Thermal Impedance of Diode



Mechanical Dimensions



- A. PACKAGE REFERENCE: JEDEC TO-247,
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 B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
 C. ALL DIMENSIONS ARE IN MILLIMETERS.
 D. DRAWING CONFORMS TO ASME Y14.5 1994

- DOES NOT COMPLY JEDEC STANDARD VALUE
 F. DRAWING FILENAME: MKT-T0247G03_REV01

Figure 23. TO-247 3L - TO-247, MOLDED, 3 LEADS, JEDEC AB LONG LEADS

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Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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