To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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PHOTOCOUPLER
PS9821-1,-2

HIGH CMR, 15 Mbps OPEN COLLECTOR OUTPUT TYPE 8-PIN SSOP (SO-8) 3.3 V HIGH-SPEED PHOTOCOUPLER

–NEPOC Series–

DESCRIPTION

The PS9821-1 and PS9821-2 are active-low type high-speed photocouplers that use a GaAlAs light-emitting diode on the input side and a photodetector IC that includes a photodiode and a signal processor on the same chip on the output side.

The PS9821-1, -2 are designed specifically for high common mode transient immunity (CMR) and low pulse width distortion, PS9821-2 is suitable for high density applications.

FEATURES

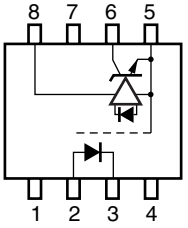
- Low power consumption (V_{CC} = 3.3 V)
- Pulse width distortion ($|t_{PHL}-t_{PLH}|$ = 35 ns MAX.)
- High common mode transient immunity (C_{MH}, C_{ML} = ±15 kV/μs MIN.)
- 40% reduction of mounting area (5-pin SOP × 2)
- High-speed (15 Mbps)
- High isolation voltage (BV = 2 500 Vr.m.s.)
- Open collector output
- Ordering number of tape product : PS9821-1-F3, F4: 1 500 pcs/reel
  : PS9821-2-F3, F4: 1 500 pcs/reel
- Pb-Free product
- Safety standards
  - UL approved: File No. E72422
  - DIN EN60747-5-2 (VDE0884 Part2) approved No.40008347 (option)

APPLICATIONS

- Measurement equipment
- PDP
- FA Network

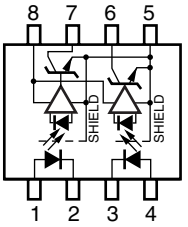
PIN CONNECTION
(Top View)

PS9821-1



1. NC
2. Anode
3. Cathode
4. NC
5. GND
6. V_O
7. NC
8. V_{CC}

PS9821-2



1. Anode1
2. Cathode1
3. Cathode2
4. Anode2
5. GND
6. V_O2
7. V_O1
8. V_{CC}

TRUTH TABLE

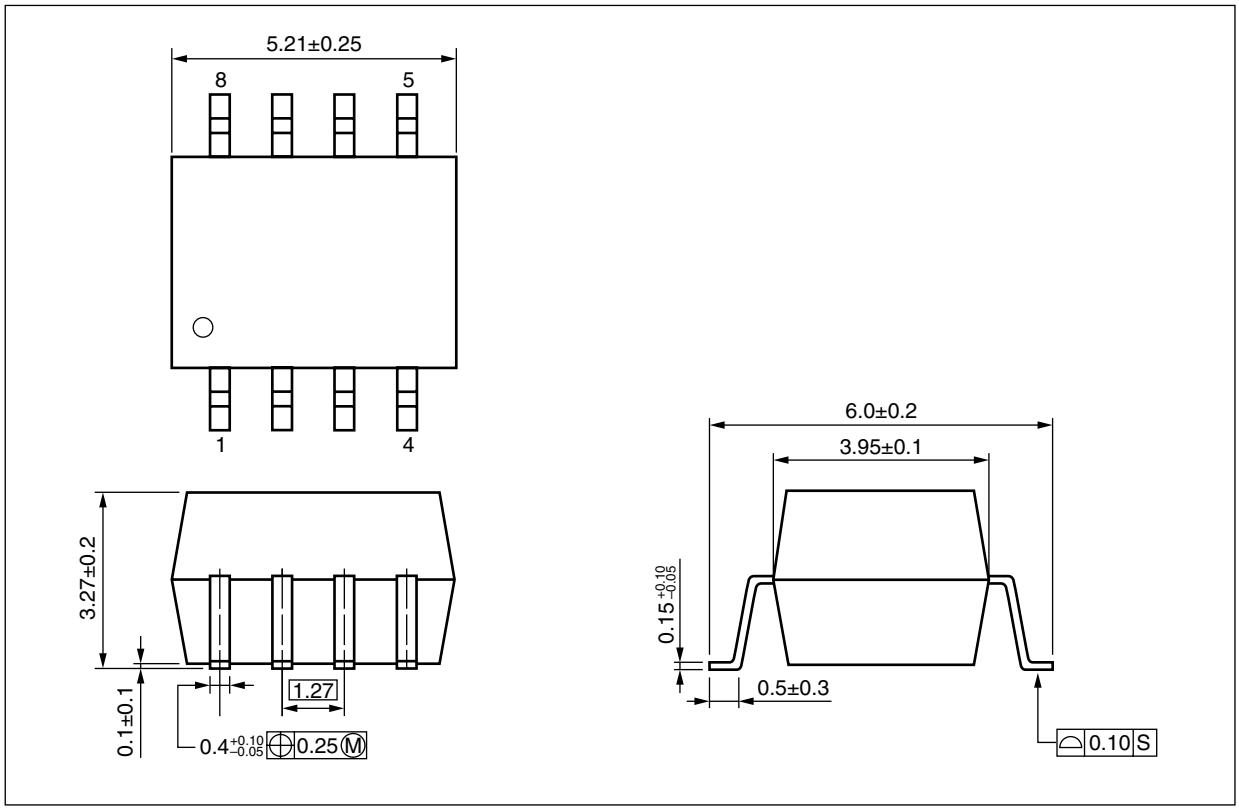
LED	Output
ON	L
OFF	H

Document No. PN10471EJ04V0DS (4th edition)
Date Published March 2008 NS
Printed in Japan

The mark <R> shows major revised points.

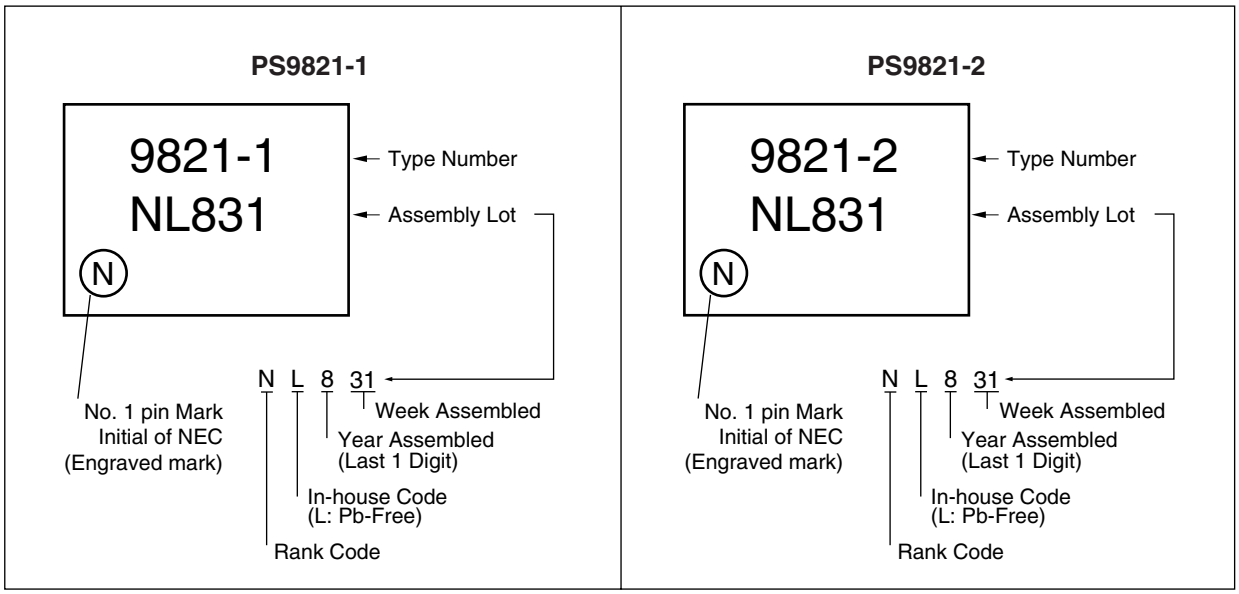
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

<R> **PACKAGE DIMENSIONS (UNIT: mm)**

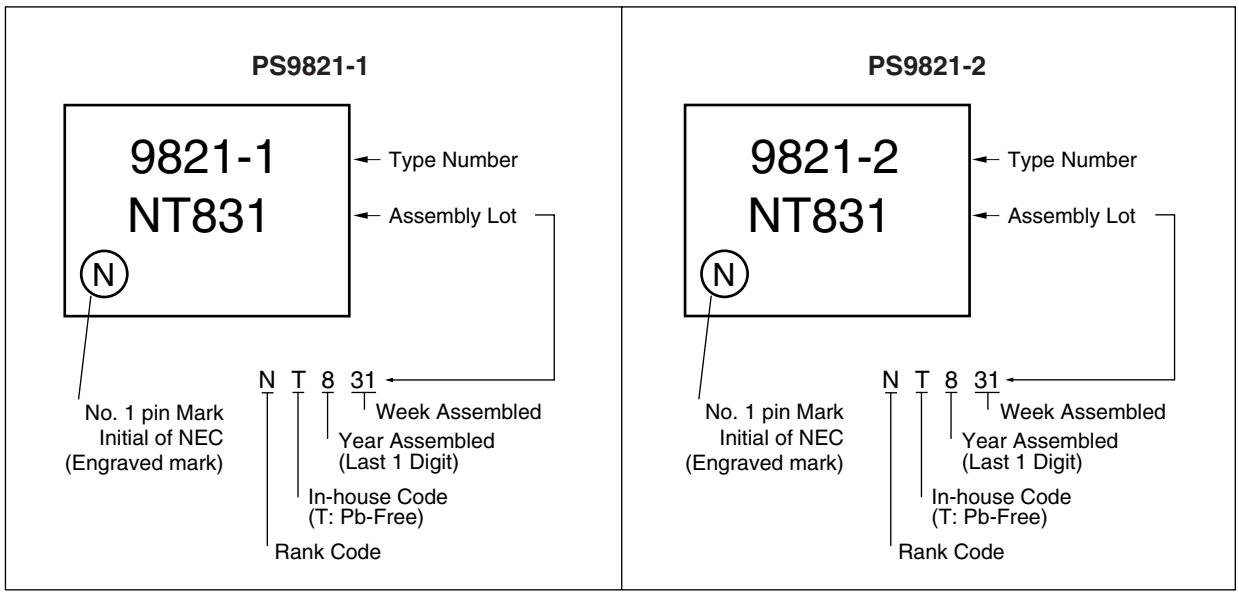


<R> **MARKING EXAMPLE**

SnBi PLATING



Ni/Pd/Au PLATING



<R> **ORDERING INFORMATION**

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standards Approval	Application Part Number*1
PS9821-1	PS9821-1-A	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9821-1
PS9821-1-F3	PS9821-1-F3-A	(SnBi)	Embossed Tape 1 500 pcs/reel	(UL approved)	
PS9821-1-F4	PS9821-1-F4-A				
PS9821-2	PS9821-2-A		20 pcs (Tape 20 pcs cut)		PS9821-2
PS9821-2-F3	PS9821-2-F3-A		Embossed Tape 1 500 pcs/reel		
PS9821-2-F4	PS9821-2-F4-A				
PS9821-1-V	PS9821-1-V-A		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9821-1
PS9821-1-V-F3	PS9821-1-V-F3-A		Embossed Tape 1 500 pcs/reel	(VDE0884 Part2)	
PS9821-1-V-F4	PS9821-1-V-F4-A			approved (Option)	
PS9821-2-V	PS9821-2-V-A		20 pcs (Tape 20 pcs cut)		PS9821-2
PS9821-2-V-F3	PS9821-2-V-F3-A		Embossed Tape 1 500 pcs/reel		
PS9821-2-V-F4	PS9821-2-V-F4-A				
PS9821-1	PS9821-1-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9821-1
PS9821-1-F3	PS9821-1-F3-AX	(Ni/Pd/Au)	Embossed Tape 1 500 pcs/reel	(UL approved)	
PS9821-1-F4	PS9821-1-F4-AX				
PS9821-2	PS9821-2-AX		20 pcs (Tape 20 pcs cut)		PS9821-2
PS9821-2-F3	PS9821-2-F3-AX		Embossed Tape 1 500 pcs/reel		
PS9821-2-F4	PS9821-2-F4-AX				
PS9821-1-V	PS9821-1-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9821-1
PS9821-1-V-F3	PS9821-1-V-F3-AX		Embossed Tape 1 500 pcs/reel	(VDE0884 Part2)	
PS9821-1-V-F4	PS9821-1-V-F4-AX			approved (Option)	
PS9821-2-V	PS9821-2-V-AX		20 pcs (Tape 20 pcs cut)		PS9821-2
PS9821-2-V-F3	PS9821-2-V-F3-AX		Embossed Tape 1 500 pcs/reel		
PS9821-2-V-F4	PS9821-2-V-F4-AX				

***1** For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings		Unit
			PS9821-1	PS9821-2	
Diode	Forward Current	I_F	20 *1	15 *2	mA
	Reverse Voltage	V_R	5		V/ch
Detector	Supply Voltage	V_{CC}	7		V
	Output Voltage	V_O	7		V/ch
	Output Current	I_O	25		mA/ch
	Power Dissipation *3	P_C	40		mW/ch
Isolation Voltage *4		BV	2 500		Vr.m.s.
Operating Ambient Temperature		T_A	−40 to +85		°C
Storage Temperature		T_{stg}	−55 to +125		°C

***1** Reduced to 0.3 mA/°C at T_A = 60°C or more.

***2** Reduced to 0.1 mA/°C at T_A = 60°C or more.

***3** Applies to output pin V_O (collector pin). Reduced to 1.5 mW/°C at T_A = 65°C or more.

***4** AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output.
Pins 1-4 shorted together, 5-8 shorted together.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	V_{FL}	0		0.8	V
High Level Input Current	I_{FH}	6.3	10	12.5	mA
Supply Voltage	V_{CC}	2.7		3.6	V
Pull-up Resistance	R_L	330		4 k	Ω
TLL (R_L = 1.0 kΩ, loads)	N			5	

ELECTRICAL CHARACTERISTICS (1/2) (T_A = –40 to +85°C, unless otherwise specified)

Parameter		Symbol	Conditions	MIN.	TYP. [*1]	MAX.	Unit
Diode	Forward Voltage	V_F	I_F = 10 mA, T_A = 25°C	1.4	1.65	1.8	V
	Reverse Current	I_R	V_R = 3.0 V, T_A = 25°C			10	μA
	Terminal Capacitance	C_t	V_F = 0 V, f = 1 MHz, T_A = 25°C		30		pF
Detector	High Level Output Current	I_{OH}	V_{CC} = V_O = 3.3 V, I_F = 0.8 mA		1	80	μA
			V_{CC} = V_O = 5.5 V, I_F = 0.8 mA		1 [*2]		
	Low Level Output Voltage [*3]	V_{OL}	V_{CC} = 3.3 V, I_F = 5.0 mA, I_{OL} = 13 mA		0.2	0.6	V
			V_{CC} = 5.5 V, I_F = 5.0 mA, I_{OL} = 13 mA		0.2 [*2]		
	High Level Supply Current (PS9821-1)	I_{CCH}	V_{CC} = 3.3 V, I_F = 0 mA, V_O = open		4	7	mA
			V_{CC} = 5.5 V, I_F = 0 mA, V_O = open		5 [*2]		
	High Level Supply Current (PS9821-2)		V_{CC} = 3.3 V, I_F = 0 mA, V_O = open		8	14	
			V_{CC} = 5.5 V, I_F = 0 mA, V_O = open		10 [*2]		
	Low Level Supply Current (PS9821-1)	I_{CCL}	V_{CC} = 3.3 V, I_F = 10 mA, V_O = open		7	10	
			V_{CC} = 5.5 V, I_F = 10 mA, V_O = open		9 [*2]		
	Low Level Supply Current (PS9821-2)		V_{CC} = 3.3 V, I_F = 10 mA, V_O = open		14	20	
			V_{CC} = 5.5 V, I_F = 10 mA, V_O = open		18 [*2]		
Coupled	Threshold Input Current (H → L)	I_{FHL}	V_{CC} = 3.3 V, V_O = 0.8 V, R_L = 350 Ω		2.5	5	mA
			V_{CC} = 5 V, V_O = 0.8 V, R_L = 350 Ω		2.5 [*2]		
	Isolation Resistance	R_{I-O}	V_{I-O} = 1 kV_{DC}, RH = 40 to 60%, T_A = 25°C	10^{11}			Ω
	Insulation Resistance (Input-Input), (PS9821-2)	R_{I-I}	V_{I-I} = 1 kV_{DC}, RH = 40 to 60%, T_A = 25°C	10^{10}			Ω
	Isolation Capacitance	C_{I-O}	V = 0 V, f = 1 MHz, T_A = 25°C		0.6		pF
	Insulation Capacitance (Input-Input), (PS9821-2)	C_{I-I}	V = 0 V, f = 1 MHz, T_A = 25°C		0.3		pF
	Propagation Delay Time (H → L) [*4]	t_{PHL}	T_A = 25°C		45	75	ns
			V_{CC} = 3.3 V, R_L = 350 Ω, I_F = 7.5 mA			100	
			V_{CC} = 5 V, R_L = 350 Ω, I_F = 7.5 mA		38 [*2]		
	Propagation Delay Time (L → H) [*4]	t_{PLH}	T_A = 25°C		50	75	
			V_{CC} = 3.3 V, R_L = 350 Ω, I_F = 7.5 mA			100	
			V_{CC} = 5 V, R_L = 350 Ω, I_F = 7.5 mA		43 [*2]		
	Rise Time	t_r	V_{CC} = 3.3 V, R_L = 350 Ω, I_F = 7.5 mA		20		
			V_{CC} = 5 V, R_L = 350 Ω, I_F = 7.5 mA		20 [*2]		
	Fall Time	t_f	V_{CC} = 3.3 V, R_L = 350 Ω, I_F = 7.5 mA		5		
			V_{CC} = 5 V, R_L = 350 Ω, I_F = 7.5 mA		5 [*2]		
	Pulse Width Distortion (PWD) [*4]	$\lvert t_{PLH}-t_{PHL} \rvert$	V_{CC} = 3.3 V, R_L = 350 Ω, I_F = 7.5 mA		5	35	
			V_{CC} = 5 V, R_L = 350 Ω, I_F = 7.5 mA		5 [*2]		
	Propagation Delay Skew	t_{PSK}	V_{CC} = 3.3 V, R_L = 350 Ω, I_F = 7.5 mA			40	

ELECTRICAL CHARACTERISTICS (2/2) (T_A = –40 to +85°C, unless otherwise specified)

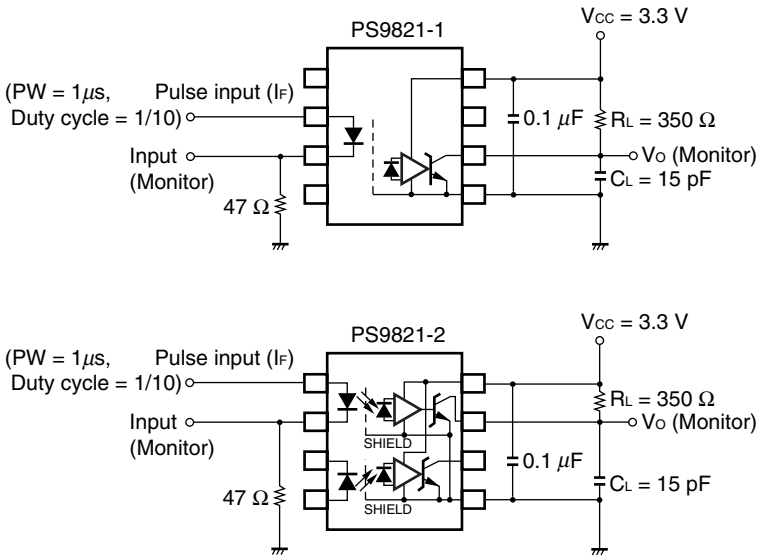
Parameter		Symbol	Conditions	MIN.	TYP. *1	MAX.	Unit
Coupled	Common Mode Transient Immunity at High Level Output*5	CM_H	V_{CC} = 3.3 V, R_L = 350 Ω, T_A = 25°C, I_F = 0 mA, V_O > 2 V, V_{CM} = 1 kV	15	20		kV/μs
			V_{CC} = 5 V, R_L = 350 Ω, T_A = 25°C, I_F = 0 mA, V_O > 2 V, V_{CM} = 1 kV		20*2		
	Common Mode Transient Immunity at Low Level Output*5	CM_L	V_{CC} = 3.3 V, R_L = 350 Ω, T_A = 25°C, I_F = 7.5 mA, V_O < 0.8 V, V_{CM} = 1 kV	15	20		
			V_{CC} = 5 V, R_L = 350 Ω, T_A = 25°C, I_F = 7.5 mA, V_O < 0.8 V, V_{CM} = 1 kV		20*2		

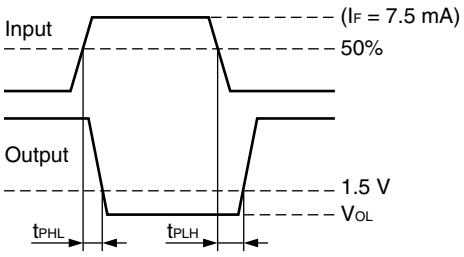
***1** Typical values at T_A = 25°C

***2** These values are reference values.

***3** Because V_{OL} of 2 V or more may be output when LED current input and when output supply of V_{CC} = 2.6 V or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.

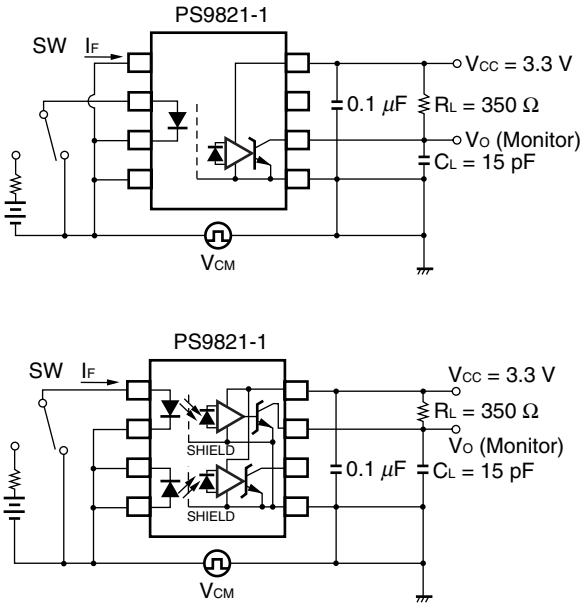
***4** Test circuit for propagation delay time

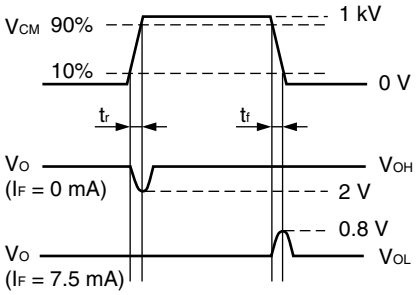




Remark C_L includes probe and stray wiring capacitance.

***5** Test circuit for common mode transient immunity



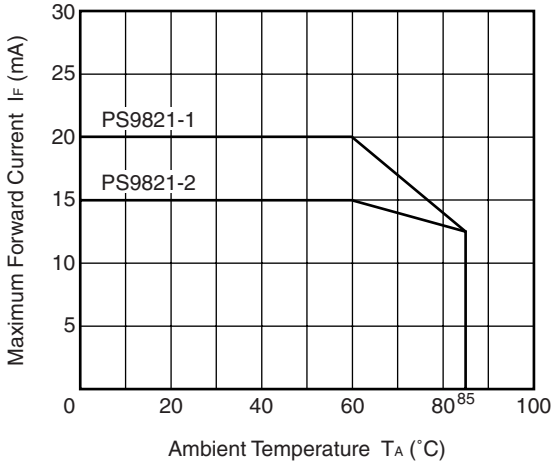


USAGE CAUTIONS

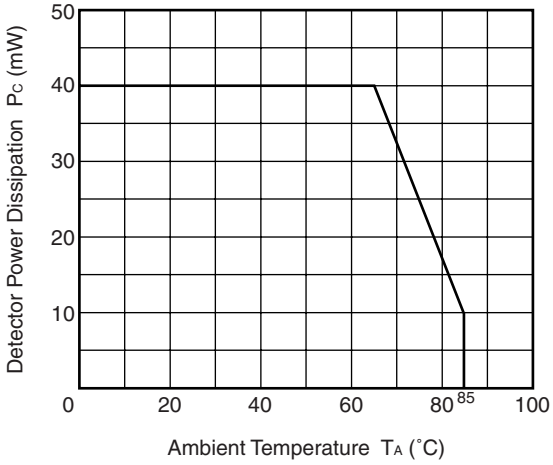
1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
2. By-pass capacitor of 0.1 μF is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
3. Avoid storage at a high temperature and high humidity.

TYPICAL CHARACTERISTICS (T_A = 25°C, unless otherwise specified)

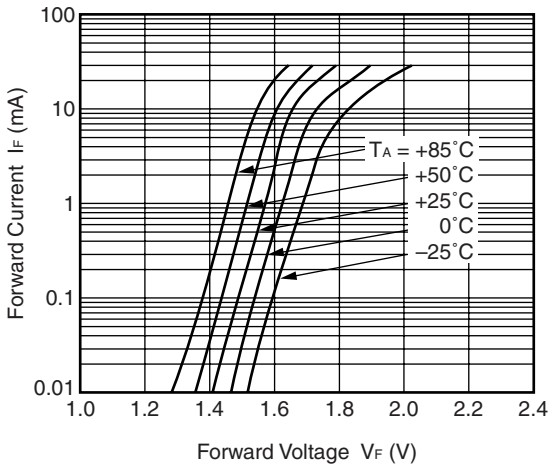
MAXIMUM FORWARD CURRENT vs. AMBIENT TEMPERATURE



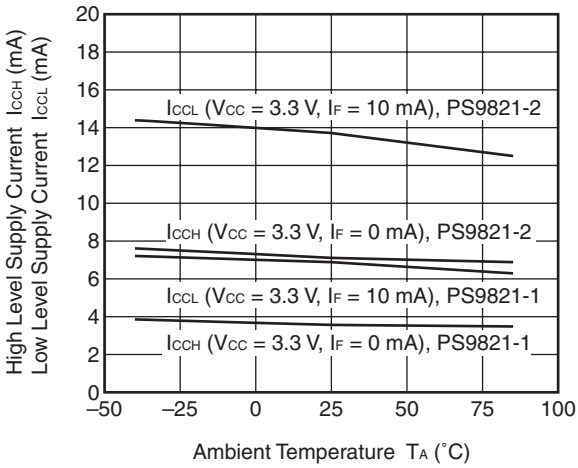
DETECTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



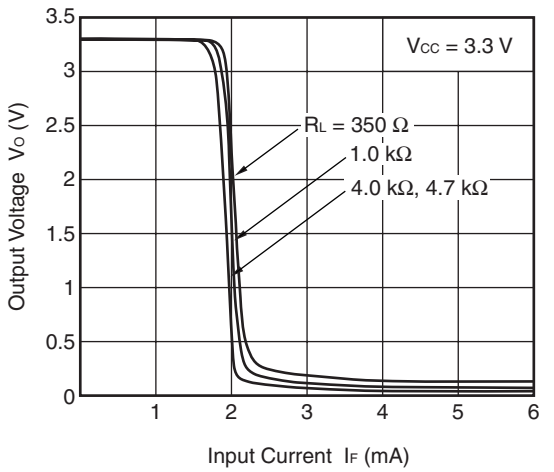
FORWARD CURRENT vs. FORWARD VOLTAGE



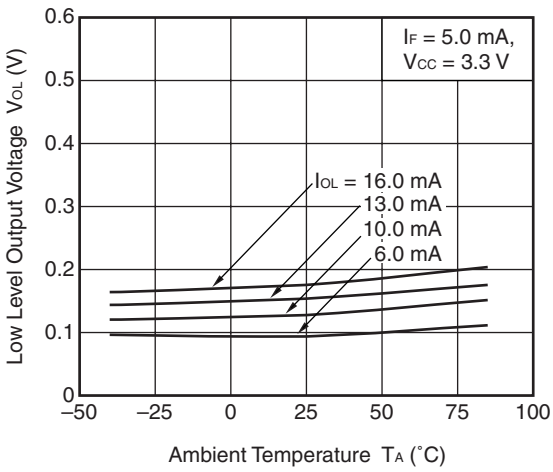
SUPPLY CURRENT vs. AMBIENT TEMPERATURE



OUTPUT VOLTAGE vs. INPUT CURRENT

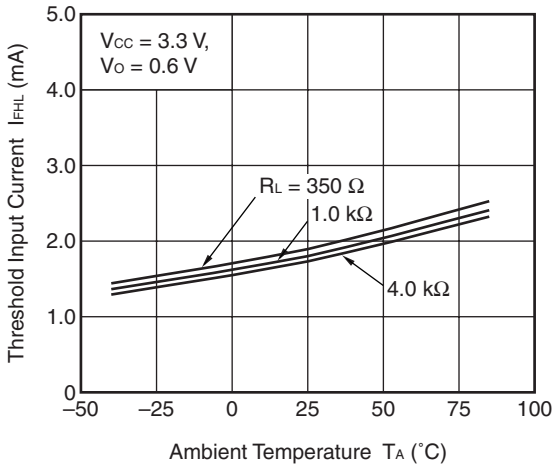


LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE

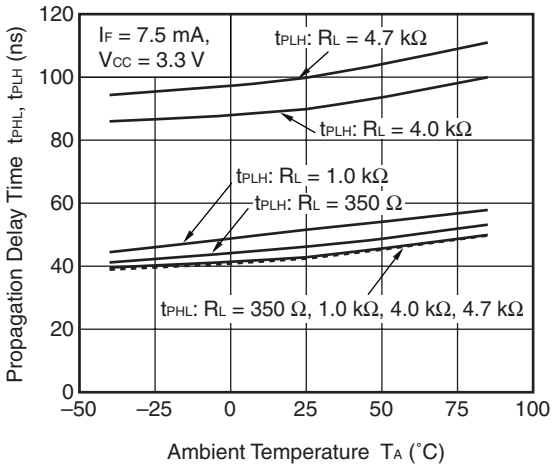


Remark The graphs indicate nominal characteristics.

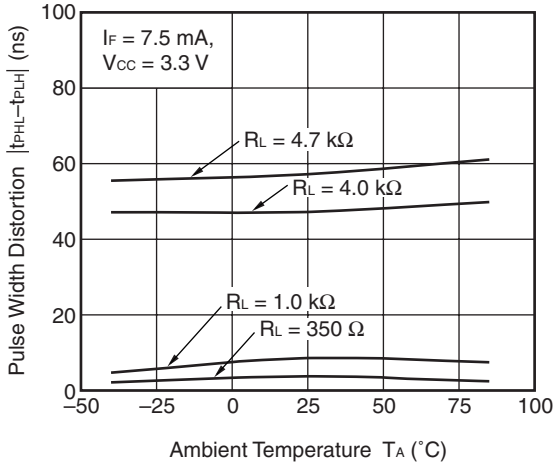
THRESHOLD INPUT CURRENT vs.
AMBIENT TEMPERATURE



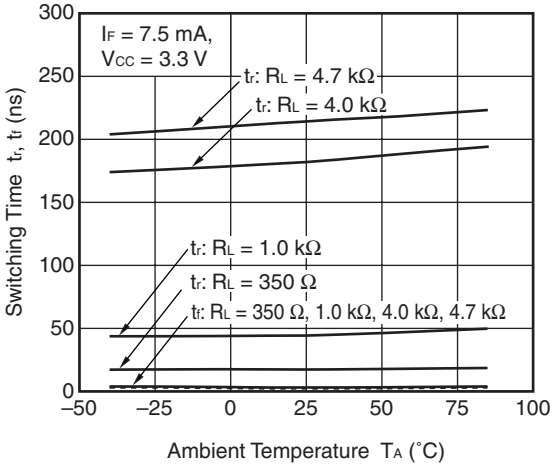
PROPAGATION DELAY TIME vs.
AMBIENT TEMPERATURE



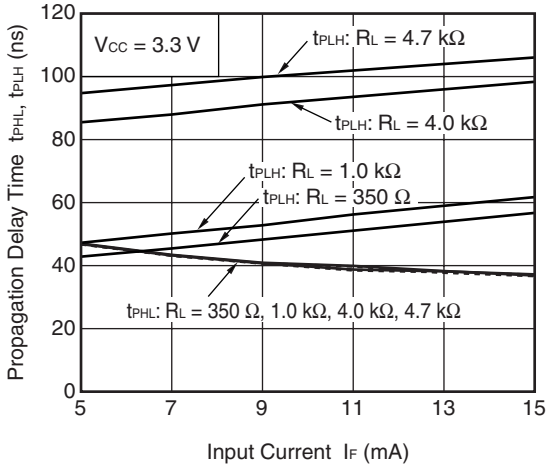
PULSE WIDTH DISTORTION vs.
AMBIENT TEMPERATURE



SWITCHING TIME vs.
AMBIENT TEMPERATURE



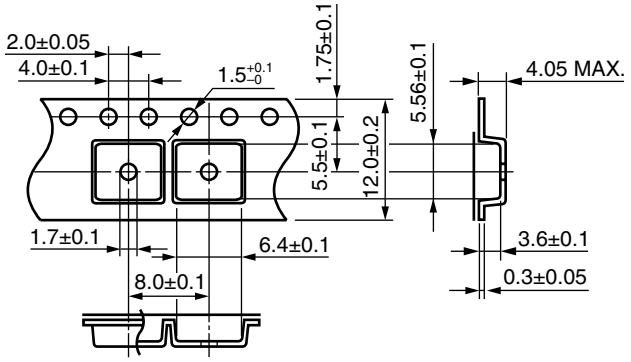
PROPAGATION DELAY TIME vs.
INPUT CURRENT



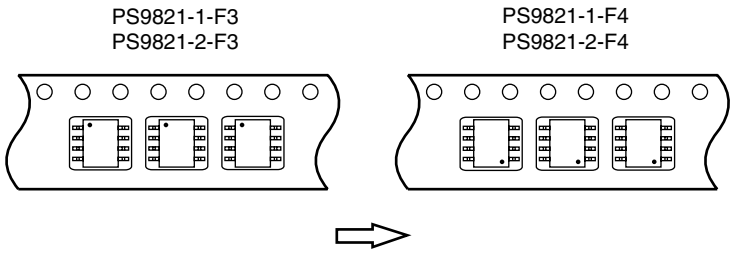
Remark The graphs indicate nominal characteristics.

TAPING SPECIFICATIONS (UNIT: mm)

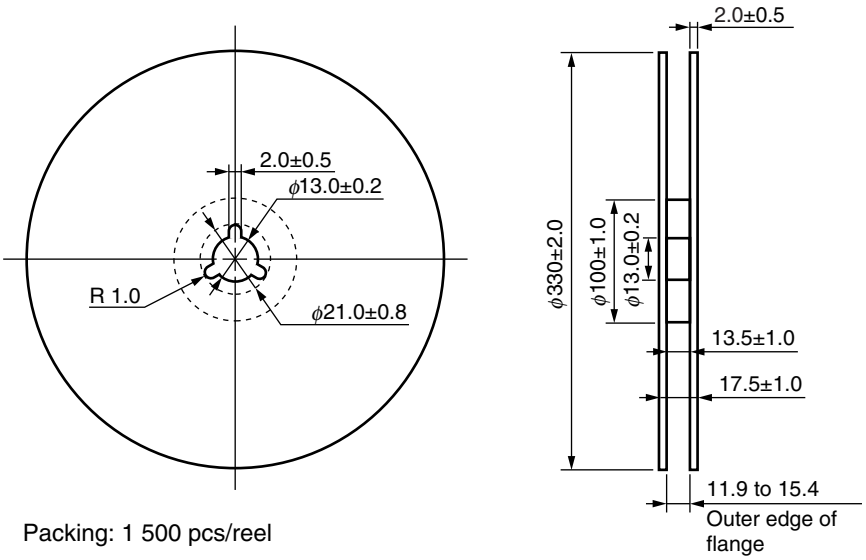
Outline and Dimensions (Tape)



Taping Direction

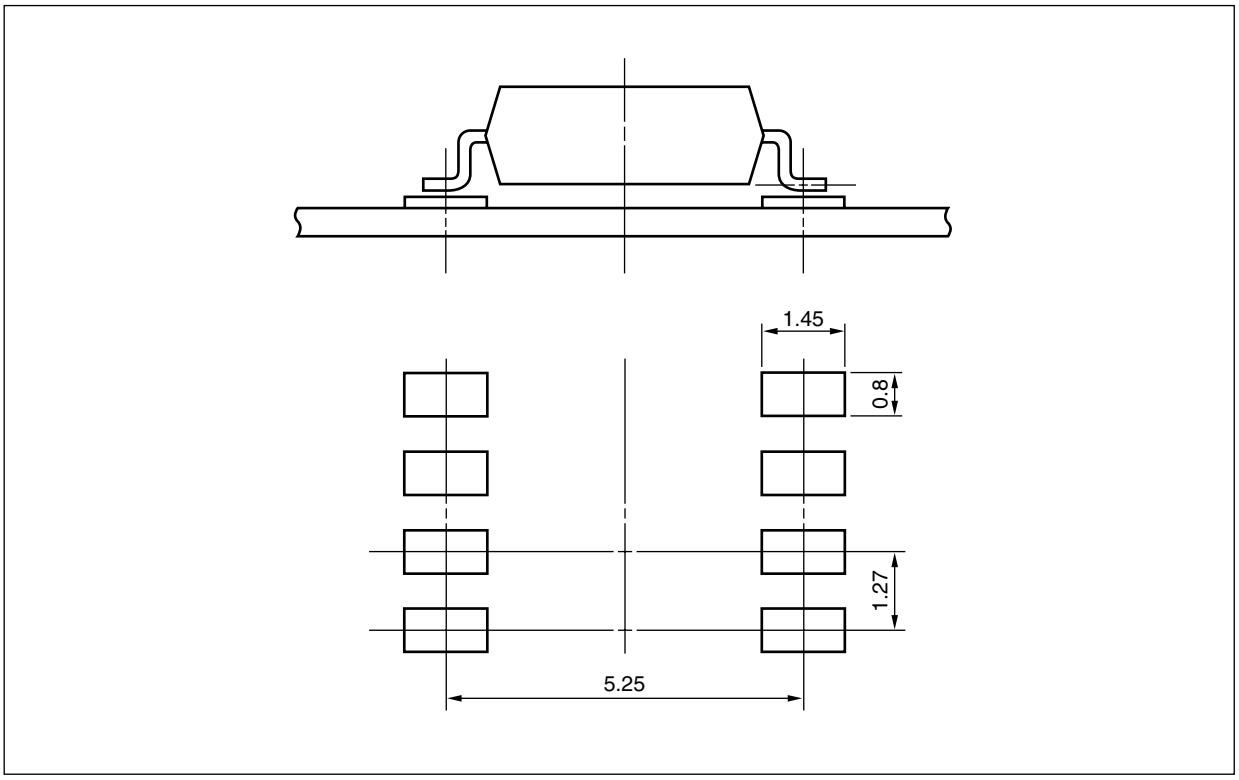


Outline and Dimensions (Reel)



Packing: 1 500 pcs/reel

RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



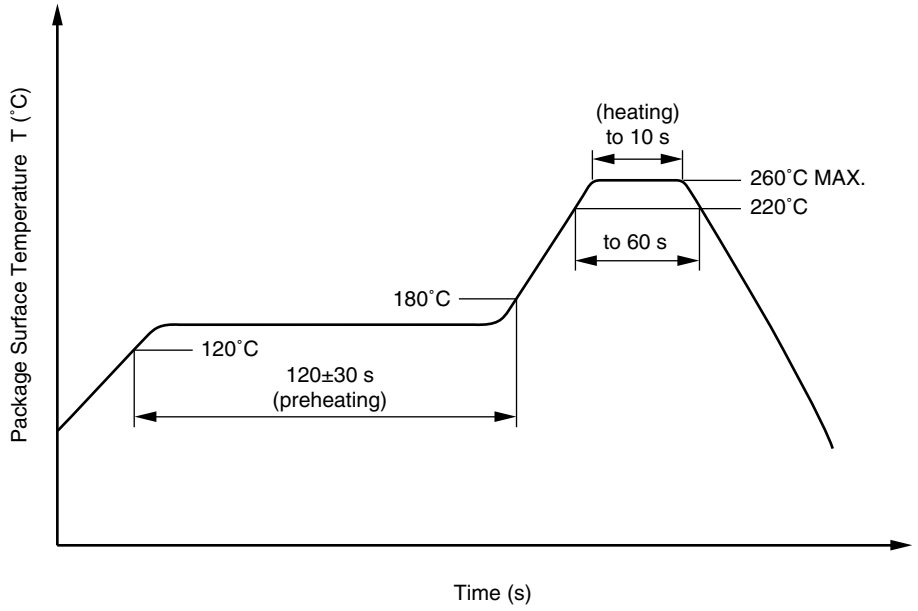
NOTES ON HANDLING

1. Recommended soldering conditions

(1) Infrared reflow soldering

• Peak reflow temperature	260°C or below (package surface temperature)
• Time of peak reflow temperature	10 seconds or less
• Time of temperature higher than 220°C	60 seconds or less
• Time to preheat temperature from 120 to 180°C	120±30 s
• Number of reflows	Three
• Flux	Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



(2) Wave soldering

• Temperature	260°C or below (molten solder temperature)
• Time	10 seconds or less
• Preheating conditions	120°C or below (package surface temperature)
• Number of times	One (Allowed to be dipped in solder including plastic mold portion.)
• Flux	Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by soldering iron

• Peak temperature (lead part temperature)	350°C or below
• Time (each pins)	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.

(b) Please be sure that the temperature of the package would not be heated over 100°C.

(4) Cautions

- Fluxes

  Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

1. Protect against static electricity when handling.
2. Avoid storage at a high temperature and high humidity.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Application classification (DIN EN 60664-1 VDE0110 Part 1) for rated line voltages ≤ 300 Vr.m.s. for rated line voltages ≤ 600 Vr.m.s.		 IV III	
Climatic test class (DIN EN 60664-1 VDE0110)		55/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}$, $P_d < 5$ pC	 U_{IORM} U_{pr}	 566 849	 V_{peak} V_{peak}
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{IORM}$, $P_d < 5$ pC	U_{pr}	1 061	V_{peak}
Highest permissible overvoltage	U_{TR}	4 000	V_{peak}
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Clearance distance		>4.0	mm
Creepage distance		>4.0	mm
Comparative tracking index (DIN IEC 112/VDE 0303 Part 1)	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	T_{stg}	–55 to +125	°C
Operating temperature range	T_A	–40 to +85	°C
Isolation resistance, minimum value V_{IO} = 500 V dc at T_A = 25°C V_{IO} = 500 V dc at T_A MAX. at least 100°C	 Ris MIN. Ris MIN.	 10^{12} 10^{11}	 Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I_F, Psi = 0) Power (output or total power dissipation) Isolation resistance V_{IO} = 500 V dc at T_A = Tsi	 Tsi Isi Psi Ris MIN.	 150 150 600 10^9	 °C mA mW Ω

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M8E 02. 11-1

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