

# 74LVC1G02

## Single 2-input NOR gate

Rev. 13 — 8 February 2019

Product data sheet

## 1. General description

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The 74LVC1G02 provides the single 2-input NOR function.

Input can be driven from either 3.3 V or 5 V devices. These features allow the use of these devices in a mixed 3.3 V and 5 V environment.

Schmitt-trigger action at all inputs makes the circuit tolerant for slower input rise and fall time.

This device is fully specified for partial power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

## 2. Features and benefits

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- Wide supply voltage range from 1.65 V to 5.5 V
- High noise immunity
- Complies with JEDEC standard:
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8-B/JESD36 (2.7 V to 3.6 V)
- $\pm 24$  mA output drive ( $V_{CC} = 3.0$  V)
- CMOS low power consumption
- Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Inputs accept voltages up to 5 V
- Multiple package options
- ESD protection:
  - HBM JESD22-A114F exceeds 2 000 V
  - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

| Type number | Package           |        |                                                                                                                        |          |
|-------------|-------------------|--------|------------------------------------------------------------------------------------------------------------------------|----------|
|             | Temperature range | Name   | Description                                                                                                            | Version  |
| 74LVC1G02GW | -40 °C to +125 °C | TSSOP5 | plastic thin shrink small outline package; 5 leads; body width 1.25 mm                                                 | SOT353-1 |
| 74LVC1G02GV | -40 °C to +125 °C | SC-74A | plastic surface-mounted package; 5 leads                                                                               | SOT753   |
| 74LVC1G02GM | -40 °C to +125 °C | XSON6  | plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm                            | SOT886   |
| 74LVC1G02GF | -40 °C to +125 °C | XSON6  | plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm                               | SOT891   |
| 74LVC1G02GN | -40 °C to +125 °C | XSON6  | extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm                                  | SOT1115  |
| 74LVC1G02GS | -40 °C to +125 °C | XSON6  | extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm                                  | SOT1202  |
| 74LVC1G02GX | -40 °C to +125 °C | X2SON5 | X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm | SOT1226  |

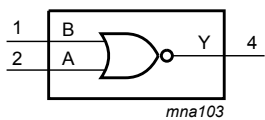
4. Marking

Table 2. Marking

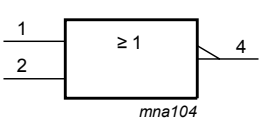
| Type number | Marking code[1] |
|-------------|-----------------|
| 74LVC1G02GW | VB              |
| 74LVC1G02GV | V02             |
| 74LVC1G02GM | VB              |
| 74LVC1G02GF | VB              |
| 74LVC1G02GN | VB              |
| 74LVC1G02GS | VB              |
| 74LVC1G02GX | VB              |

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

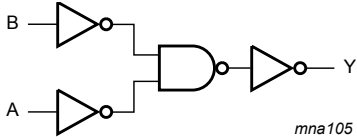
5. Functional diagram



**Fig. 1. Logic symbol**



**Fig. 2. IEC logic symbol**



**Fig. 3. Logic diagram**

6. Pinning information

6.1. Pinning

74LVC1G02

Diagram showing pin configuration for SOT353-1 and SOT753 packages. Pin 1 (B) is top-left, Pin 2 (A) is bottom-left, Pin 3 (GND) is bottom-right, Pin 4 (Y) is top-right, Pin 5 (VCC) is middle-right. Reference: 001aab610

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Diagram showing pin configuration for SOT886 package. Pin 1 (B) is top-left, Pin 2 (A) is middle-left, Pin 3 (GND) is bottom-left, Pin 4 (Y) is top-right, Pin 5 (n.c.) is middle-right, Pin 6 (VCC) is bottom-right. Reference: 001aab611

Transparent top view

74LVC1G02

Diagram showing pin configuration for SOT891, SOT1115 and SOT1202 packages. Pin 1 (B) is top-left, Pin 2 (A) is middle-left, Pin 3 (GND) is bottom-left, Pin 4 (Y) is top-right, Pin 5 (n.c.) is middle-right, Pin 6 (VCC) is bottom-right. Reference: 001aaf052

Transparent top view

74LVC1G02

Diagram showing pin configuration for SOT1226 (X2SON5) package. Pin 1 (B) is top-left, Pin 2 (A) is middle-left, Pin 3 (GND) is center, Pin 4 (Y) is top-right, Pin 5 (VCC) is middle-right. Reference: aaa-003019

Transparent top view

**Fig. 4. Pin configuration SOT353-1 and SOT753**

**Fig. 5. Pin configuration SOT886**

**Fig. 6. Pin configuration SOT891, SOT1115 and SOT1202**

**Fig. 7. Pin configuration SOT1226 (X2SON5)**

6.2. Pin description

Table 3. Pin description

| Symbol          | Pin               |       | Description    |
|-----------------|-------------------|-------|----------------|
|                 | TSSOP5 and X2SON5 | XSON6 |                |
| B               | 1                 | 1     | data input     |
| A               | 2                 | 2     | data input     |
| GND             | 3                 | 3     | ground (0 V)   |
| Y               | 4                 | 4     | data output    |
| n.c.            | -                 | 5     | not connected  |
| V <sub>CC</sub> | 5                 | 6     | supply voltage |

## 7. Functional description

**Table 4. Function table**

*H = HIGH voltage level; L = LOW voltage level.*

| Inputs |   | Outputs |
|--------|---|---------|
| A      | B | Y       |
| L      | L | H       |
| L      | H | L       |
| H      | L | L       |
| H      | H | L       |

## 8. Limiting values

**Table 5. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).*

| Symbol    | Parameter               | Conditions                        | Min  | Max            | Unit |
|-----------|-------------------------|-----------------------------------|------|----------------|------|
| $V_{CC}$  | supply voltage          |                                   | -0.5 | +6.5           | V    |
| $I_{IK}$  | input clamping current  | $V_I < 0$ V                       | -50  | -              | mA   |
| $V_I$     | input voltage           | [1]                               | -0.5 | +6.5           | V    |
| $I_{OK}$  | output clamping current | $V_O > V_{CC}$ or $V_O < 0$ V     | -    | $\pm 50$       | mA   |
| $V_O$     | output voltage          | Active mode [1][2]                | -0.5 | $V_{CC} + 0.5$ | V    |
|           |                         | Power-down mode [1][2]            | -0.5 | +6.5           | V    |
| $I_O$     | output current          | $V_O = 0$ V to $V_{CC}$           | -    | $\pm 50$       | mA   |
| $I_{CC}$  | supply current          |                                   | -    | +100           | mA   |
| $I_{GND}$ | ground current          |                                   | -100 | -              | mA   |
| $P_{tot}$ | total power dissipation | $T_{amb} = -40$ °C to +125 °C [3] | -    | 250            | mW   |
| $T_{stg}$ | storage temperature     |                                   | -65  | +150           | °C   |

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] When  $V_{CC} = 0$  V (Power-down mode), the output voltage can be 5.5 V in normal operation.

[3] For TSSOP5 and SC-74A packages: above 87.5 °C the value of  $P_{tot}$  derates linearly with 4.0 mW/K.  
For XSON6 and X2SON5 packages: above 118 °C the value of  $P_{tot}$  derates linearly with 7.8 mW/K.

## 9. Recommended operating conditions

**Table 6. Recommended operating conditions**

| Symbol              | Parameter                           | Conditions                      | Min  | Typ | Max      | Unit |
|---------------------|-------------------------------------|---------------------------------|------|-----|----------|------|
| $V_{CC}$            | supply voltage                      |                                 | 1.65 | -   | 5.5      | V    |
| $V_I$               | input voltage                       |                                 | 0    | -   | 5.5      | V    |
| $V_O$               | output voltage                      | Active mode                     | 0    | -   | $V_{CC}$ | V    |
|                     |                                     | $V_{CC} = 0$ V; Power-down mode | 0    | -   | 5.5      | V    |
| $T_{amb}$           | ambient temperature                 |                                 | -40  | -   | +125     | °C   |
| $\Delta t/\Delta V$ | input transition rise and fall rate | $V_{CC} = 1.65$ V to 2.7 V      | -    | -   | 20       | ns/V |
|                     |                                     | $V_{CC} = 2.7$ V to 5.5 V       | -    | -   | 10       | ns/V |

## 10. Static characteristics

**Table 7. Static characteristics**

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                 | Conditions                                                                                                | -40 °C to +85 °C      |        |                     | -40 °C to +125 °C     |                     | Unit |
|------------------|---------------------------|-----------------------------------------------------------------------------------------------------------|-----------------------|--------|---------------------|-----------------------|---------------------|------|
|                  |                           |                                                                                                           | Min                   | Typ[1] | Max                 | Min                   | Max                 |      |
| V <sub>IH</sub>  | HIGH-level input voltage  | V <sub>CC</sub> = 1.65 V to 1.95 V                                                                        | 0.65V <sub>CC</sub>   | -      | -                   | 0.65V <sub>CC</sub>   | -                   | V    |
|                  |                           | V <sub>CC</sub> = 2.3 V to 2.7 V                                                                          | 1.7                   | -      | -                   | 1.7                   | -                   | V    |
|                  |                           | V <sub>CC</sub> = 2.7 V to 3.6 V                                                                          | 2.0                   | -      | -                   | 2.0                   | -                   | V    |
|                  |                           | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                          | 0.7V <sub>CC</sub>    | -      | -                   | 0.7V <sub>CC</sub>    | -                   | V    |
| V <sub>IL</sub>  | LOW-level input voltage   | V <sub>CC</sub> = 1.65 V to 1.95 V                                                                        | -                     | -      | 0.35V <sub>CC</sub> | -                     | 0.35V <sub>CC</sub> | V    |
|                  |                           | V <sub>CC</sub> = 2.3 V to 2.7 V                                                                          | -                     | -      | 0.7                 | -                     | 0.7                 | V    |
|                  |                           | V <sub>CC</sub> = 2.7 V to 3.6 V                                                                          | -                     | -      | 0.8                 | -                     | 0.8                 | V    |
|                  |                           | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                          | -                     | -      | 0.3V <sub>CC</sub>  | -                     | 0.3V <sub>CC</sub>  | V    |
| V <sub>OH</sub>  | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                                       |                       |        |                     |                       |                     |      |
|                  |                           | I <sub>O</sub> = -100 µA; V <sub>CC</sub> = 1.65 V to 5.5 V                                               | V <sub>CC</sub> - 0.1 | -      | -                   | V <sub>CC</sub> - 0.1 | -                   | V    |
|                  |                           | I <sub>O</sub> = -4 mA; V <sub>CC</sub> = 1.65 V                                                          | 1.2                   | -      | -                   | 0.95                  | -                   | V    |
|                  |                           | I <sub>O</sub> = -8 mA; V <sub>CC</sub> = 2.3 V                                                           | 1.9                   | -      | -                   | 1.7                   | -                   | V    |
|                  |                           | I <sub>O</sub> = -12 mA; V <sub>CC</sub> = 2.7 V                                                          | 2.2                   | -      | -                   | 1.9                   | -                   | V    |
|                  |                           | I <sub>O</sub> = -24 mA; V <sub>CC</sub> = 3.0 V                                                          | 2.3                   | -      | -                   | 2.0                   | -                   | V    |
| V <sub>OL</sub>  | LOW-level output voltage  | I <sub>O</sub> = -32 mA; V <sub>CC</sub> = 4.5 V                                                          | 3.8                   | -      | -                   | 3.4                   | -                   | V    |
|                  |                           | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                                       |                       |        |                     |                       |                     |      |
|                  |                           | I <sub>O</sub> = 100 µA; V <sub>CC</sub> = 1.65 V to 5.5 V                                                | -                     | -      | 0.1                 | -                     | 0.1                 | V    |
|                  |                           | I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V                                                           | -                     | -      | 0.45                | -                     | 0.70                | V    |
|                  |                           | I <sub>O</sub> = 8 mA; V <sub>CC</sub> = 2.3 V                                                            | -                     | -      | 0.3                 | -                     | 0.45                | V    |
|                  |                           | I <sub>O</sub> = 12 mA; V <sub>CC</sub> = 2.7 V                                                           | -                     | -      | 0.4                 | -                     | 0.60                | V    |
| I <sub>I</sub>   | input leakage current     | I <sub>O</sub> = 24 mA; V <sub>CC</sub> = 3.0 V                                                           | -                     | -      | 0.55                | -                     | 0.80                | V    |
|                  |                           | I <sub>O</sub> = 32 mA; V <sub>CC</sub> = 4.5 V                                                           | -                     | -      | 0.55                | -                     | 0.80                | V    |
| I <sub>OFF</sub> | power-off leakage current | V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V                                             | -                     | ±0.1   | ±1                  | -                     | ±1                  | µA   |
| I <sub>CC</sub>  | supply current            | V <sub>CC</sub> = 0 V; V <sub>I</sub> or V <sub>O</sub> = 5.5 V                                           | -                     | ±0.1   | ±2                  | -                     | ±2                  | µA   |
| I <sub>CC</sub>  | supply current            | V <sub>I</sub> = 5.5 V or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 1.65 V to 5.5 V                    | -                     | 0.1    | 4                   | -                     | 4                   | µA   |
| ΔI <sub>CC</sub> | additional supply current | V <sub>I</sub> = 5.5 V or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 1.65 V to 5.5 V                    | -                     | 0.1    | 4                   | -                     | 4                   | µA   |
| ΔI <sub>CC</sub> | additional supply current | V <sub>CC</sub> = 2.3 V to 5.5 V; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; per pin | -                     | 5      | 500                 | -                     | 500                 | µA   |
| C <sub>I</sub>   | input capacitance         | V <sub>CC</sub> = 3.3 V; V <sub>I</sub> = GND to V <sub>CC</sub>                                          | -                     | 5      | -                   | -                     | -                   | pF   |

[1] All typical values are measured at V<sub>CC</sub> = 3.3 V and T<sub>amb</sub> = 25 °C.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for load circuit see Fig. 9.

| Symbol          | Parameter                     | Conditions                                                            | -40 °C to +85 °C |        |     | -40 °C to +125 °C |      | Unit |
|-----------------|-------------------------------|-----------------------------------------------------------------------|------------------|--------|-----|-------------------|------|------|
|                 |                               |                                                                       | Min              | Typ[1] | Max | Min               | Max  |      |
| t <sub>pd</sub> | propagation delay             | A, B to Y; see Fig. 8 [2]                                             |                  |        |     |                   |      |      |
|                 |                               | V <sub>CC</sub> = 1.65 V to 1.95 V                                    | 1.0              | 3.2    | 8.0 | 1.0               | 10.5 | ns   |
|                 |                               | V <sub>CC</sub> = 2.3 V to 2.7 V                                      | 0.5              | 2.2    | 5.5 | 0.5               | 7.0  | ns   |
|                 |                               | V <sub>CC</sub> = 2.7 V                                               | 0.5              | 2.5    | 5.5 | 0.5               | 7.0  | ns   |
|                 |                               | V <sub>CC</sub> = 3.0 V to 3.6 V                                      | 0.5              | 2.1    | 4.5 | 0.5               | 6.0  | ns   |
|                 |                               | V <sub>CC</sub> = 4.5 V to 5.5 V                                      | 0.5              | 1.7    | 4.0 | 0.5               | 5.5  | ns   |
| C <sub>PD</sub> | power dissipation capacitance | V <sub>I</sub> = GND to V <sub>CC</sub> ; V <sub>CC</sub> = 3.3 V [3] | -                | 14     | -   | -                 | -    | pF   |

- [1] Typical values are measured at T<sub>amb</sub> = 25 °C and V<sub>CC</sub> = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.
- [2] t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.
- [3] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).  
P<sub>D</sub> = C<sub>PD</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>i</sub> × N + Σ(C<sub>L</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>o</sub>) where:  
f<sub>i</sub> = input frequency in MHz;  
f<sub>o</sub> = output frequency in MHz;  
C<sub>L</sub> = output load capacitance in pF;  
V<sub>CC</sub> = supply voltage in V;  
N = number of inputs switching;  
Σ(C<sub>L</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>o</sub>) = sum of outputs.

11.1. Waveforms

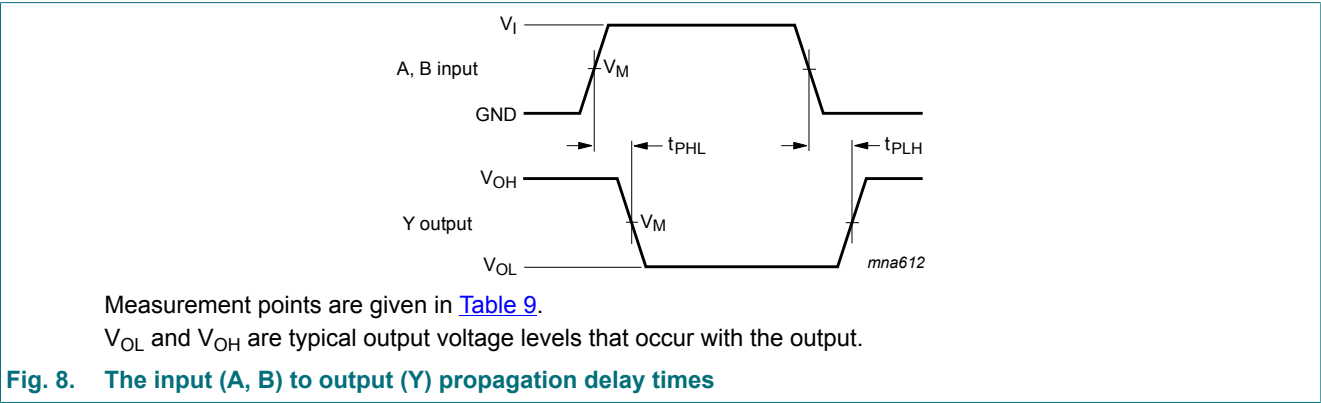
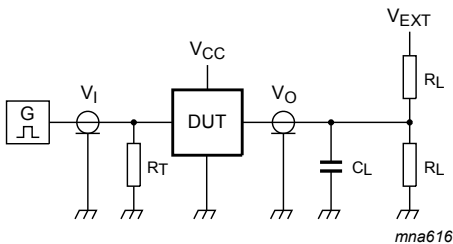


Table 9. Measurement points

| Supply voltage   | Input              | Output             |
|------------------|--------------------|--------------------|
| V <sub>CC</sub>  | V <sub>M</sub>     | V <sub>M</sub>     |
| 1.65 V to 1.95 V | 0.5V <sub>CC</sub> | 0.5V <sub>CC</sub> |
| 2.3 V to 2.7 V   | 0.5V <sub>CC</sub> | 0.5V <sub>CC</sub> |
| 2.7 V            | 1.5 V              | 1.5 V              |
| 3.0 V to 3.6 V   | 1.5 V              | 1.5 V              |
| 4.5 V to 5.5 V   | 0.5V <sub>CC</sub> | 0.5V <sub>CC</sub> |



Test data is given in [Table 10](#).  
Definitions for test circuit:  
 $R_L$  = Load resistance.  
 $C_L$  = Load capacitance including jig and probe capacitance.  
 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.  
 $V_{EXT}$  = External voltage for measuring switching times.

Fig. 9. Test circuit for measuring switching times

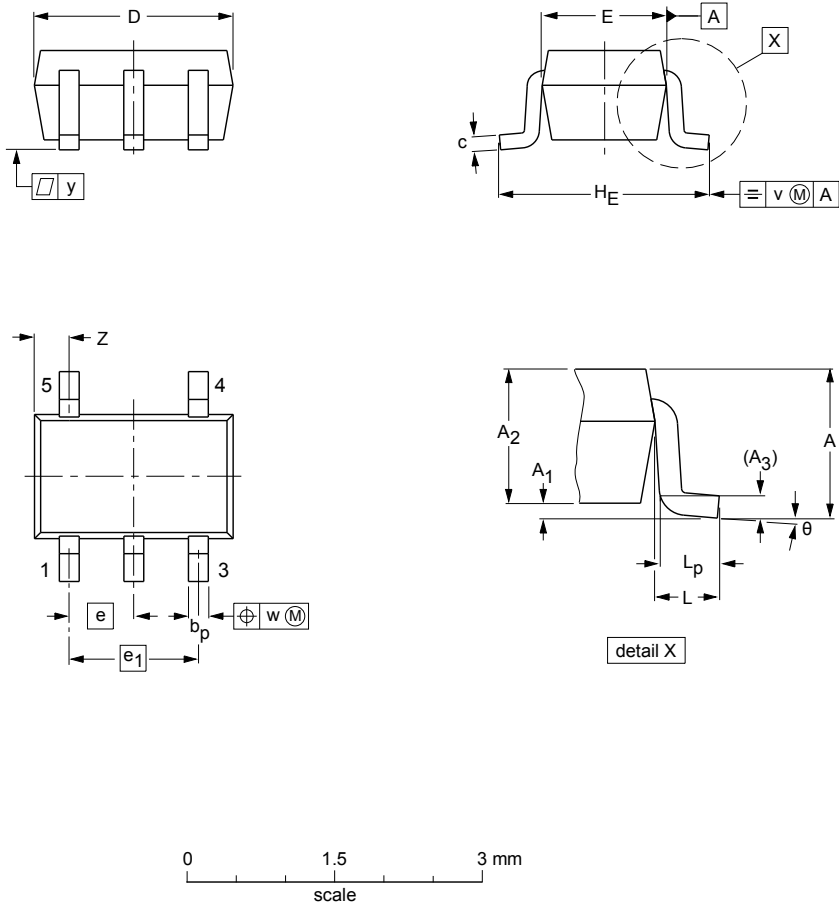
Table 10. Test data

| Supply voltage   | Input    |                       | Load  |              | $V_{EXT}$          |
|------------------|----------|-----------------------|-------|--------------|--------------------|
| $V_{CC}$         | $V_I$    | $t_r = t_f$           | $C_L$ | $R_L$        | $t_{PLH}, t_{PHL}$ |
| 1.65 V to 1.95 V | $V_{CC}$ | $\leq 2.0 \text{ ns}$ | 30 pF | 1 k $\Omega$ | open               |
| 2.3 V to 2.7 V   | $V_{CC}$ | $\leq 2.0 \text{ ns}$ | 30 pF | 500 $\Omega$ | open               |
| 2.7 V            | 2.7 V    | $\leq 2.5 \text{ ns}$ | 50 pF | 500 $\Omega$ | open               |
| 3.0 V to 3.6 V   | 2.7 V    | $\leq 2.5 \text{ ns}$ | 50 pF | 500 $\Omega$ | open               |
| 4.5 V to 5.5 V   | $V_{CC}$ | $\leq 2.5 \text{ ns}$ | 50 pF | 500 $\Omega$ | open               |

12. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



DIMENSIONS (mm are the original dimensions)

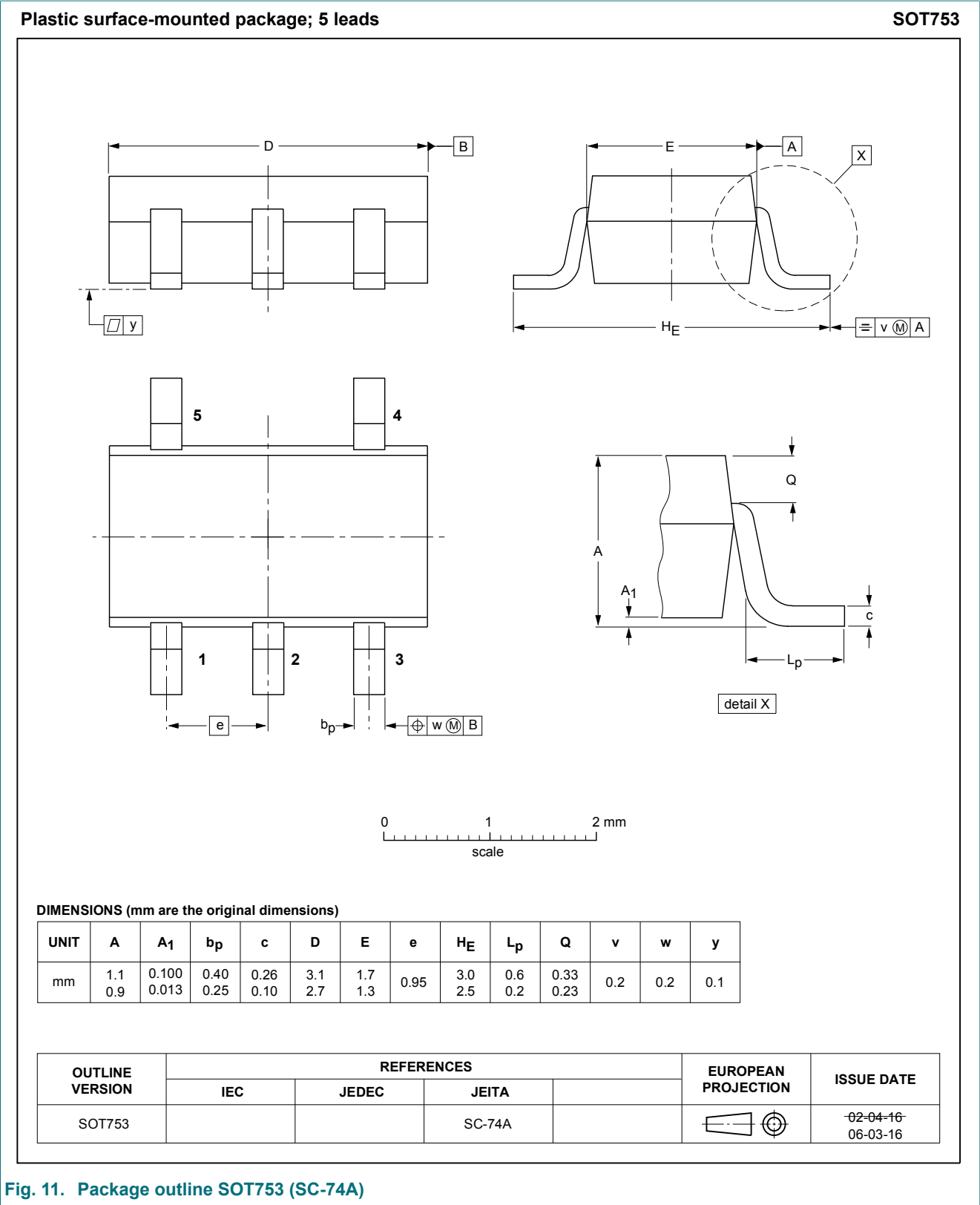
| UNIT | A<br>max. | A <sub>1</sub> | A <sub>2</sub> | A <sub>3</sub> | b <sub>p</sub> | c            | D <sup>(1)</sup> | E <sup>(1)</sup> | e    | e <sub>1</sub> | H <sub>E</sub> | L     | L <sub>p</sub> | v   | w   | y   | Z <sup>(1)</sup> | θ        |
|------|-----------|----------------|----------------|----------------|----------------|--------------|------------------|------------------|------|----------------|----------------|-------|----------------|-----|-----|-----|------------------|----------|
| mm   | 1.1       | 0.1<br>0       | 1.0<br>0.8     | 0.15           | 0.30<br>0.15   | 0.25<br>0.08 | 2.25<br>1.85     | 1.35<br>1.15     | 0.65 | 1.3            | 2.25<br>2.0    | 0.425 | 0.46<br>0.21   | 0.3 | 0.1 | 0.1 | 0.60<br>0.15     | 7°<br>0° |

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

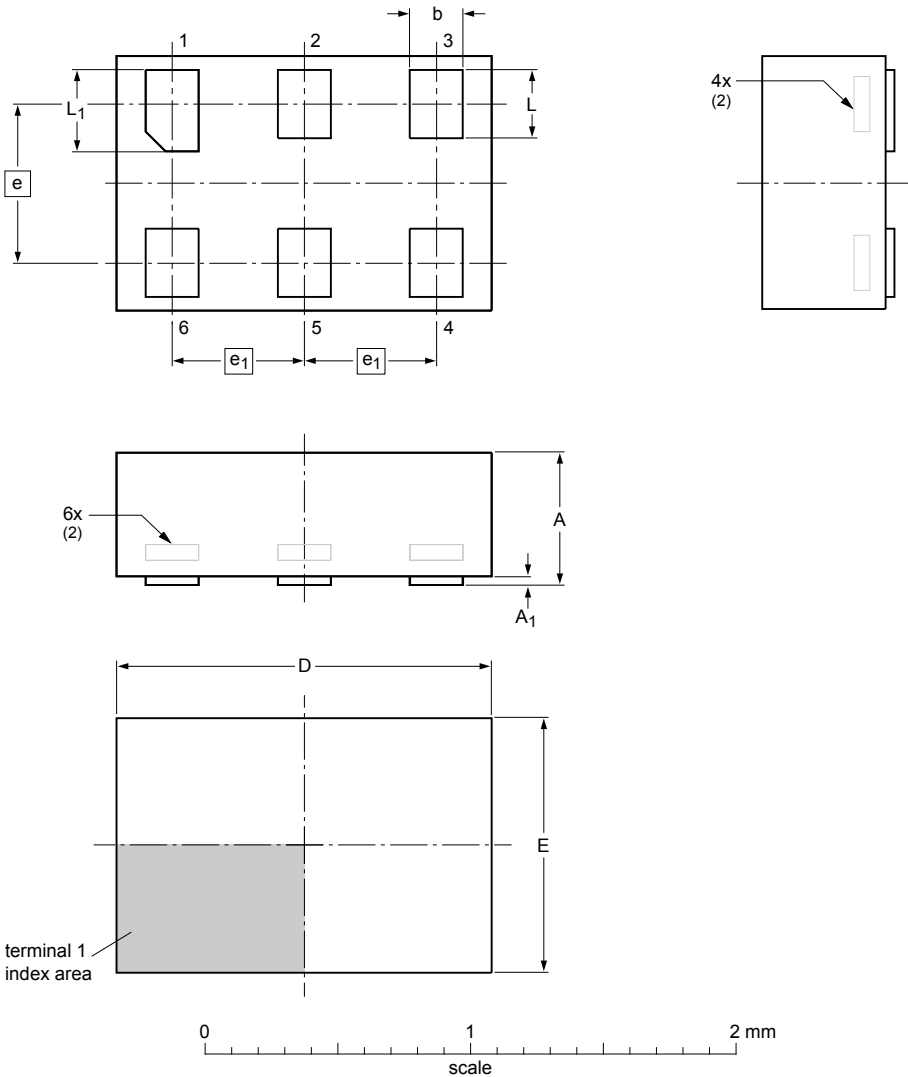
| OUTLINE<br>VERSION | REFERENCES |        |        |  | EUROPEAN<br>PROJECTION                                                                | ISSUE DATE           |
|--------------------|------------|--------|--------|--|---------------------------------------------------------------------------------------|----------------------|
|                    | IEC        | JEDEC  | JEITA  |  |                                                                                       |                      |
| SOT353-1           |            | MO-203 | SC-88A |  |  | 00-09-01<br>03-02-19 |

Fig. 10. Package outline SOT353-1 (TSSOP5)



XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886



Dimensions (mm are the original dimensions)

| Unit | A <sup>(1)</sup> | A <sub>1</sub> | b    | D    | E    | e    | e <sub>1</sub> | L    | L <sub>1</sub> |
|------|------------------|----------------|------|------|------|------|----------------|------|----------------|
| mm   | max              | 0.5            | 0.04 | 0.25 | 1.50 | 1.05 |                | 0.35 | 0.40           |
|      | nom              |                |      | 0.20 | 1.45 | 1.00 | 0.6            | 0.30 | 0.35           |
|      | min              |                |      | 0.17 | 1.40 | 0.95 |                | 0.27 | 0.32           |

- Notes
- 1. Including plating thickness.
  - 2. Can be visible in some manufacturing processes.

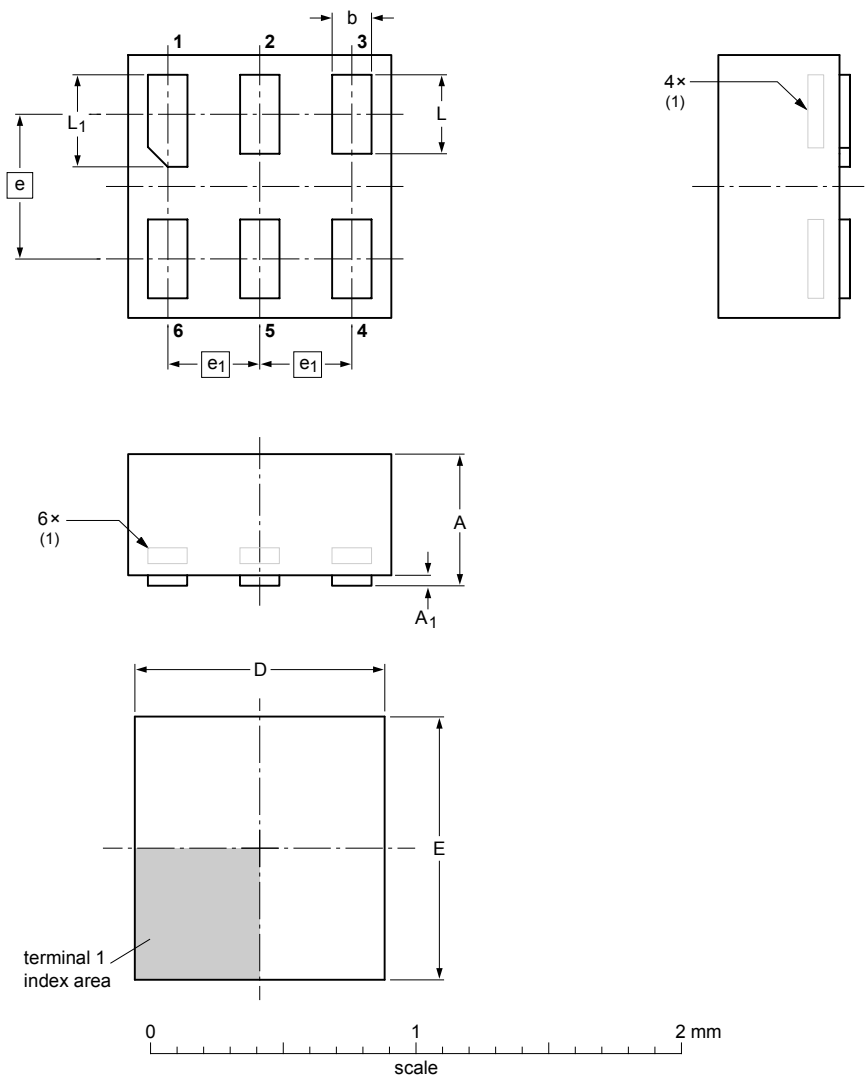
sot886\_po

| Outline version | References |        |       |  | European projection                                                                   | Issue date             |
|-----------------|------------|--------|-------|--|---------------------------------------------------------------------------------------|------------------------|
|                 | IEC        | JEDEC  | JEITA |  |                                                                                       |                        |
| SOT886          |            | MO-252 |       |  |  | -04-07-22-<br>12-01-05 |

Fig. 12. Package outline SOT886 (XSON6)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm

SOT891



DIMENSIONS (mm are the original dimensions)

| UNIT | A <sub>max</sub> | A <sub>1max</sub> | b            | D            | E            | e    | e <sub>1</sub> | L            | L <sub>1</sub> |
|------|------------------|-------------------|--------------|--------------|--------------|------|----------------|--------------|----------------|
| mm   | 0.5              | 0.04              | 0.20<br>0.12 | 1.05<br>0.95 | 1.05<br>0.95 | 0.55 | 0.35           | 0.35<br>0.27 | 0.40<br>0.32   |

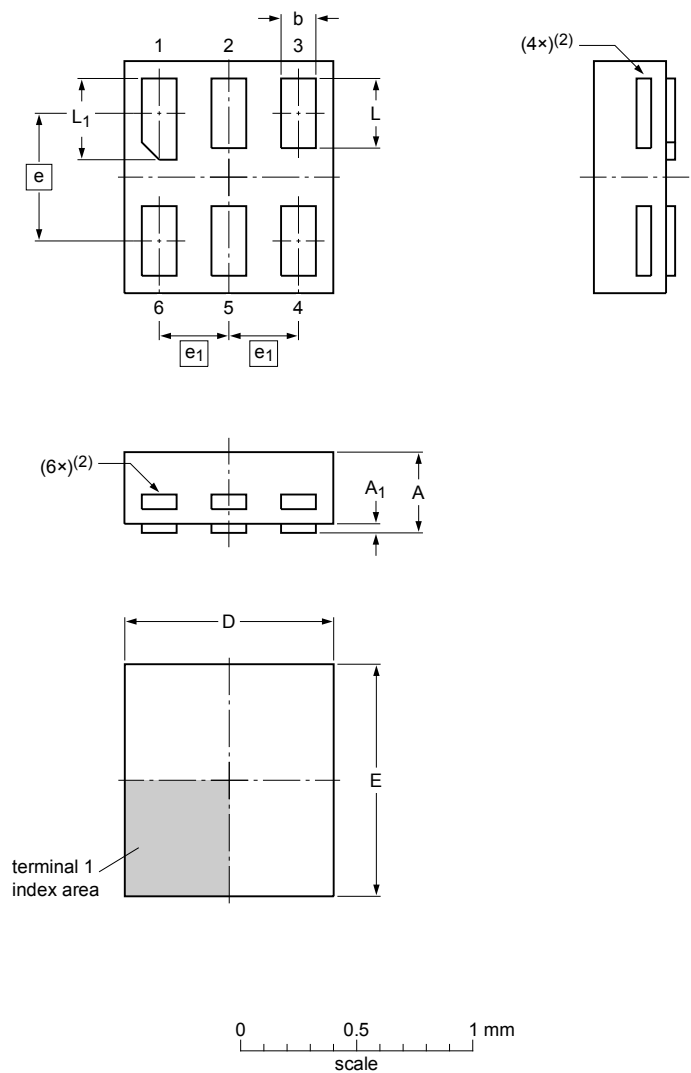
Note  
1. Can be visible in some manufacturing processes.

| OUTLINE<br>VERSION | REFERENCES |       |       |  | EUROPEAN<br>PROJECTION | ISSUE DATE            |
|--------------------|------------|-------|-------|--|------------------------|-----------------------|
|                    | IEC        | JEDEC | JEITA |  |                        |                       |
| SOT891             |            |       |       |  |                        | -05-04-06<br>07-05-15 |

Fig. 13. Package outline SOT891 (XSON6)

XSON6: extremely thin small outline package; no leads;  
6 terminals; body 0.9 x 1.0 x 0.35 mm

SOT1115



Dimensions

| Unit | A <sup>(1)</sup> | A <sub>1</sub> | b    | D    | E    | e    | e <sub>1</sub> | L    | L <sub>1</sub> |
|------|------------------|----------------|------|------|------|------|----------------|------|----------------|
| mm   | max              | 0.35           | 0.04 | 0.20 | 0.95 | 1.05 |                | 0.35 | 0.40           |
|      | nom              |                |      | 0.15 | 0.90 | 1.00 | 0.55           | 0.30 | 0.35           |
|      | min              |                |      | 0.12 | 0.85 | 0.95 |                | 0.27 | 0.32           |

Note

- 1. Including plating thickness.
- 2. Visible depending upon used manufacturing technology.

sot1115\_po

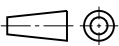
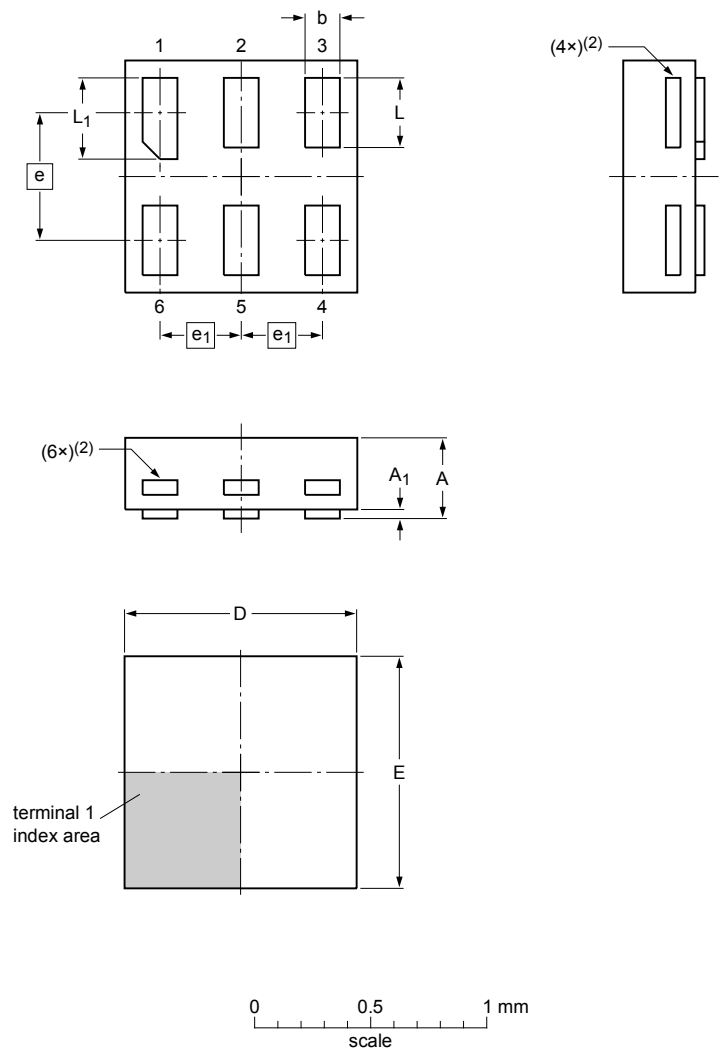
| Outline version | References |       |       |  | European projection                                                                   | Issue date                      |
|-----------------|------------|-------|-------|--|---------------------------------------------------------------------------------------|---------------------------------|
|                 | IEC        | JEDEC | JEITA |  |                                                                                       |                                 |
| SOT1115         |            |       |       |  |  | <del>10-04-02</del><br>10-04-07 |

Fig. 14. Package outline SOT1115 (XSON6)

XSON6: extremely thin small outline package; no leads;  
6 terminals; body 1.0 x 1.0 x 0.35 mm

SOT1202



Dimensions

| Unit | A <sup>(1)</sup> | A <sub>1</sub> | b    | D    | E    | e    | e <sub>1</sub> | L    | L <sub>1</sub> |
|------|------------------|----------------|------|------|------|------|----------------|------|----------------|
| mm   | max              | 0.35           | 0.04 | 0.20 | 1.05 | 1.05 |                | 0.35 | 0.40           |
|      | nom              |                |      | 0.15 | 1.00 | 1.00 | 0.55           | 0.30 | 0.35           |
|      | min              |                |      | 0.12 | 0.95 | 0.95 |                | 0.27 | 0.32           |

Note

1. Including plating thickness.  
2. Visible depending upon used manufacturing technology.

sot1202\_po

| Outline version | References |       |       |  | European projection                                                                   | Issue date           |
|-----------------|------------|-------|-------|--|---------------------------------------------------------------------------------------|----------------------|
|                 | IEC        | JEDEC | JEITA |  |                                                                                       |                      |
| SOT1202         |            |       |       |  |  | 10-04-02<br>10-04-06 |

Fig. 15. Package outline SOT1202 (XSON6)

X2SON5: plastic thermal enhanced extremely thin small outline package; no leads;  
5 terminals; body 0.8 x 0.8 x 0.35 mm

SOT1226

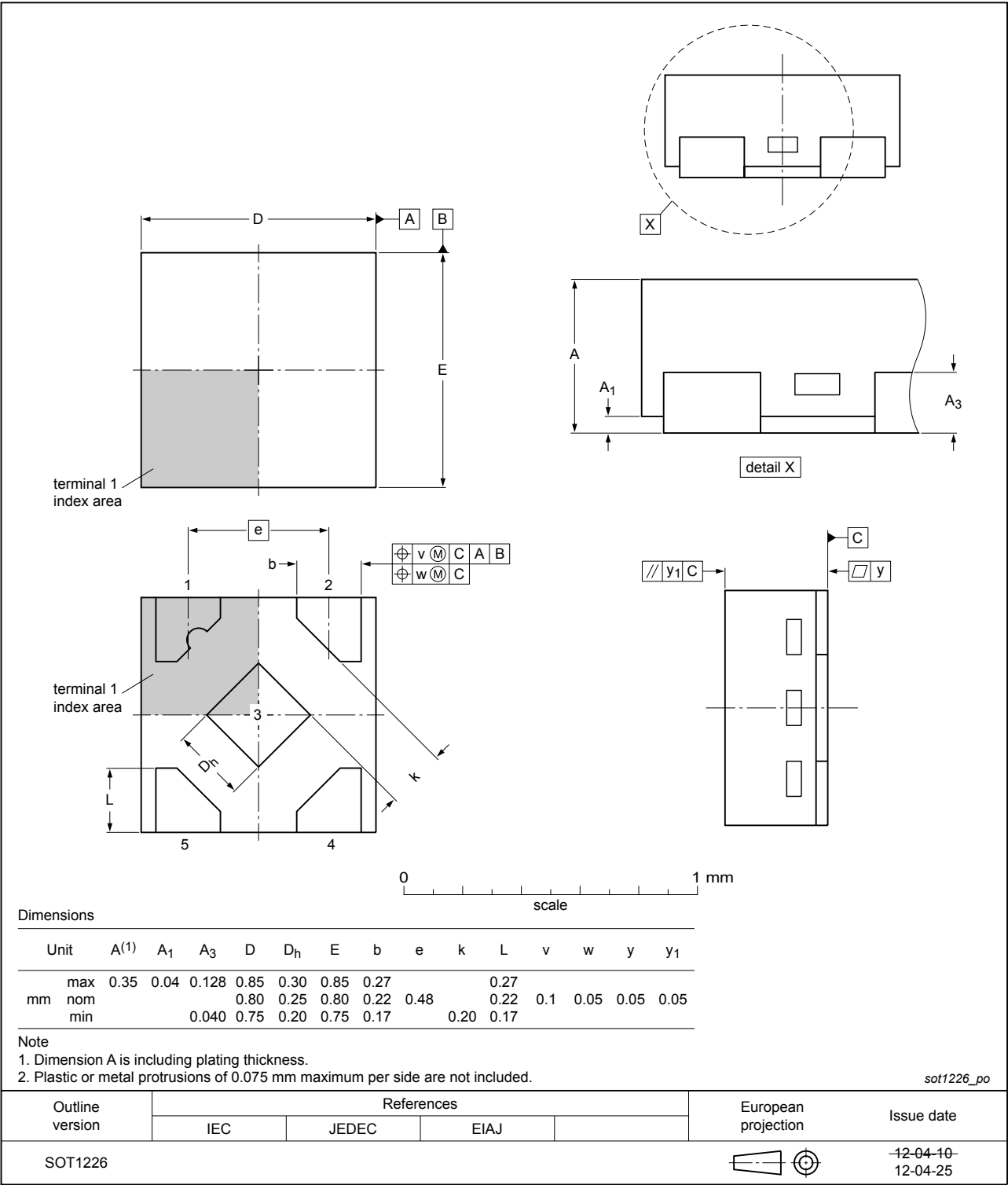


Fig. 16. Package outline SOT1226 (X2SON5)

## 13. Abbreviations

Table 11. Abbreviations

| Acronym | Description                             |
|---------|-----------------------------------------|
| CMOS    | Complementary Metal Oxide Semiconductor |
| DUT     | Device Under Test                       |
| ESD     | ElectroStatic Discharge                 |
| HBM     | Human Body Model                        |
| MM      | Machine Model                           |
| TTL     | Transistor-Transistor Logic             |

## 14. Revision history

Table 12. Revision history

| Document ID    | Release date                                                                                                                                                                                                                              | Data sheet status     | Change notice | Supersedes     |
|----------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|---------------|----------------|
| 74LVC1G02 v.13 | 20190208                                                                                                                                                                                                                                  | Product data sheet    | -             | 74LVC1G02 v.12 |
| Modifications: | <ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul> |                       |               |                |
| 74LVC1G02 v.12 | 20161129                                                                                                                                                                                                                                  | Product data sheet    | -             | 74LVC1G02 v.11 |
| Modifications: | <ul style="list-style-type: none"> <li><a href="#">Table 7</a>: The maximum limits for leakage current and supply current have changed.</li> </ul>                                                                                        |                       |               |                |
| 74LVC1G02 v.11 | 20120629                                                                                                                                                                                                                                  | Product data sheet    | -             | 74LVC1G02 v.10 |
| Modifications: | <ul style="list-style-type: none"> <li>Added type number 74LVC1G02GX (SOT1226)</li> </ul>                                                                                                                                                 |                       |               |                |
| 74LVC1G02 v.10 | 20120305                                                                                                                                                                                                                                  | Product data sheet    | -             | 74LVC1G02 v.9  |
| Modifications: | <ul style="list-style-type: none"> <li>Package outline drawing of SOT886 (<a href="#">Fig. 12</a>) modified.</li> </ul>                                                                                                                   |                       |               |                |
| 74LVC1G02 v.9  | 20111209                                                                                                                                                                                                                                  | Product data sheet    | -             | 74LVC1G02 v.8  |
| Modifications: | <ul style="list-style-type: none"> <li>Legal pages updated.</li> </ul>                                                                                                                                                                    |                       |               |                |
| 74LVC1G02 v.8  | 20101020                                                                                                                                                                                                                                  | Product data sheet    | -             | 74LVC1G02 v.7  |
| 74LVC1G02 v.7  | 20070718                                                                                                                                                                                                                                  | Product data sheet    | -             | 74LVC1G02 v.6  |
| 74LVC1G02 v.6  | 20060914                                                                                                                                                                                                                                  | Product data sheet    | -             | 74LVC1G02 v.5  |
| 74LVC1G02 v.5  | 20040907                                                                                                                                                                                                                                  | Product specification | -             | 74LVC1G02 v.4  |
| 74LVC1G02 v.4  | 20021002                                                                                                                                                                                                                                  | Product specification | -             | 74LVC1G02 v.3  |
| 74LVC1G02 v.3  | 20020515                                                                                                                                                                                                                                  | Product specification | -             | 74LVC1G02 v.2  |
| 74LVC1G02 v.2  | 20010411                                                                                                                                                                                                                                  | Product specification | -             | 74LVC1G02 v.1  |
| 74LVC1G02 v.1  | 20001114                                                                                                                                                                                                                                  | Product specification | -             | -              |

## 15. Legal information

### Data sheet status

| Document status<br>[1][2]      | Product status [3] | Definition                                                                            |
|--------------------------------|--------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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