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		F	Product Sp	pecificat	tion		
Applicant / Hardware	/ Manufact	turer	Panasonic Indus Zeppelinstrasse 21337 Lüneburg Germany		GmbH		
Applicant Software	/ Manufact	turer	Please refer to c	hapter 24 Ordering I	nformation		
Software V	/ersion		Please refer to c	hapter 24 Ordering I	nformation		
documer	nt's valid mmenda	ity and ations.	products described ir declares their agree Panasonic reserves	ement and under	standing of its	contents	
WI	RELESS	CONN	R&D CENTER ECTIVITY NAL DEVICES	APPROVED	CHECKED	DESIG	NED

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CUSTOMER'S PAN4580 / PA						
use • 2.4 • 3 a	ailable as e) with pir GHz RF antenna o	s small n heade Freque ptions:	size SMD device (29.8mm x 19.0mm er (33.9 x 33.9 x 7.3mm) ency Single port 50Ω, ceramic antenna or pl nnels with up to 2Mbps RF data rate		r ETU (Easy t	O
 Low power modes for increased battery life 						

- High sensitivity: -99dBm typ. @ 250kbps & 1% Packet Error Rate
- +3.0 dBm @ 2.4GHz Pout programmable over a 20 dB range
- Low supply voltage (1.9 V to 3.6 V, 3.0 V typ.)
- Operating temperature range -40°C to +85°C
- 128k total Flash, when using SNAP: 58.5k free for uploadad user SNAPpy scripts.
- 2 UART ports for control or transparent data
- 38 GPIOs and up to 7 A/D inputs with 10 Bit ADC for fast and easy conversion from analog inputs -such as temperature, pressure and fluid levels- to digital values.
- Spread Spectrum (DSSS) technology surmounts noisy environments when using SNAP®
- Low power mode: 1.5µA with internal timer running
- Mesh (SNAP®) Mesh networking Topology with 16 Channels
- Manufactured in conformance with RoHS

2. APPLICATIONS FOR THE MODULE

- Automatic Meter Reading
- Inventory management, Factory- and home automation
- Wireless Sensor Networks, e.g. Lighting Control
- Monitoring (environmental, patient or fitness)

3. DESCRIPTION FOR THE MODULE

The PAN4580 module is a short range, low power, 2.4GHz ISM band transceiver which includes a complete 802.15.4 physical layer (PHY). It is designed for the IEEE 802.15.4 wireless standard. Using an appropriate microcontroller (MCU) with reference oscillator provides a cost effective solution for short range data links and networks.

The PAN4580 and PAN4580ETU comes optionally pre-loaded with the Synapse SNAP® mesh network operating system.

This module is CE and FCC / IC certified.

The PAN4580 use the MCU with integrated Transceiver ATmega128RFA1 from Atmel.

Please contact your local sales office for further details on additional options and services, by visiting <u>www.panasonic.com/rfmodules</u> or write an e-mail to <u>wireless@eu.panasonic.com</u>.

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4. SCOPE OF THIS DOCUMENT

This product specification applies to Panasonic's 802.15.4 Modem PAN4580 // ENWC9A31xxEF and PAN4580ETU // ENWC9A33xxEF. Different versions of the PAN4580 are available (refer to chapter 24).

5. HISTORY FOR THIS DOCUMENT

Revision	Date	Modification / Remarks
0.1	20.03.2012	Initial draft version
1.0	19.10.2012	Add ETU-module, add FCC/IC certification



¹ When using SNAP Software by Synapse Wireless, Inc.

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CUSTOMER'S PAN4580 / PAN		PANASONIC'S CODE ENWC9A31xxEF / EN		XEF DATE 19.10.2012
Pin No. PAN4580	Pin No. PAN4580ETU	Pin Name	SNAPpy IO ¹	Description
B4	14	PF1_ADC1	25	IO or ADC input channel 1
В5		PG1_DIG1	33	IO or Radio Transceiver Antenna Divers Control Output
В6	18	PF5_ADC5_TMS	29	IO or ADC input channel 5 or JTAG Tes Mode Select
В7	20	PF7_ADC7_TDI	31	IO or ADC input channel 7 or JTAG Tes Data Input or I ² C SCL
C1	5	PE0_RXD0_PCINT8	16	IO or USART0 Receive Pin or Pin Chan Interrupt8
C2	6	PE1_TXD0	17	IO or USART0 Transmit Pin
C3	7	PE4_OC3B_INT4	20	IO or External Interrupt4 Input or Output Compare and PWM Output B for Timer/Counter3
C4		PE6_T3_INT6	22	IO or External Interrupt6 Input or Timer/Counter3 Clock Input
C5	12	PE7_ICP3_INT7_CLK 0	23	IO or UART1 RTS or External Interrupt' Input, Timer/Counter3 Input Capture Trigger or Divided System Clock
C6		PF3_ADC3_DIG4		IO or ADC input channel 3 or Radio Transceiver RX/TX Indicator Output
D1	4	PB5_OC1A_PCINT5	5	IO or Output Compare and PWM Output A for Timer/Counter1 or Pin Change Interrupt 5
D2	3	PB6_OC1B_PCINT6	6	IO or Output Compare and PWM Output for Timer/Counter1 or Pin Change Interrupt 6
D3	2	PB7_OC0A_OC1C_ PCINT7	7	IO or Output Compare and PWM Output A for Timer/Counter0, Output Compare and PWM Output C for Timer/Counter1 Pin Change Interrupt 7
E1		PB2_MOSI_PCINT2	2	IO or SPI Bus Master Output/Slave Inpu Programming Data Input or Pin Change Interrupt 2
E2		PB3_MISO_PCINT3	3	IO or SPI Bus Master Input/Slave Output Programming Data Output or Pin Change Interrupt 3
E3		PB4_OC2A_PCINT4	4	IO or Output Compare and PWM Output A for Timer/Counter2 or Pin Change Interrupt 4
E8		RF IN/OUT		Peripheral transmit 50 Ohm RF output / input pin (only ENWC9A31CxEF, other modules NC)
F1		PB0_SSN_PCINT0	0	IO or SPI Slave Select input or Pin Chan Interrupt 0
F2		PB1_SCK_PCINT1	1	IO or SPI Bus Serial Clock or Pin Chang Interrupt 1
F3		PD1_SDA_INT1	9	IO or External Interrupt1 Input or I ² C

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	STOMER'S CODEPANASONIC'S CODEDATEI4580 / PAN4580ETUENWC9A31xxEF / ENWC9A33xxEFDATE					19.10.2	012
Pin No. PAN4580	Pin No. PAN4580E	ETU Pin Name	SNAPpy IO ¹	Descri	ption		
F4		PD0 SCL INT0	8	IO or]	External Intern	rupt0 Input or 1	I ² C
F5		PG0_DIG3			Radio Transce	eiver RX/TX Ir	
F6		PG2_AMR			Automated Me for Timer/Cou	eter Reading - inter2	Counter
G1		CLKI		Must b	Must be pulled low during normal operation		
G2		PD7_T0	15	IO or '	IO or Timer/Counter0 Clock Input		
G3	11	PD4_ICP1	12	Timer/	IO or UART1 CTS output or Timer/Counter1Input Capture Trigger		
G4	9	PD2_RXD1_INT2	10	IO or I Interru	UART1 Recei 1pt2 Input	ve Pin or Exte	rnal
G5		PG5_OC0B	37	IO or 0		are and PWM (Output B
H2		PD6_T1	14	IO or '	Timer/Counter	r1 Clock Input	
Н3		PD5_XCK1	13		USART1 Exte Output	ernal Clock	
H4	10	PD3_TXD1_INT3	11		External Intern mit Pin	rupt3 Input or V	UART1
Н5	23	RESET#		Modu	le Reset, Activ	e Low	
Н6		TST		Must b	*	during normal	
C7, D4, D5, D6, D7, E4, E5, E6, E7, F7, G6, G7, H7	, ,	NC		-			



8. KEY PARTS LIST

Part Name	Material
P.W.Board	Glass cloth epoxide resin with gold plating
Casing	Material: C7521, thickness 0.15mm
IC part name	ATmega128RFA1 (Atmel, <u>www.atmel.com</u>)

9. TEST CONDITIONS

Measurements are made under room temperature and humidity unless otherwise specified.

Temperature	25 ± 10°C
Humidity	40 to 85%RH
Supply voltage	3.0V

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10. ABSOLUTE MAXIMUM RATINGS

The maximum ratings may not be exceeded under any circumstances, not even momentarily and individually, as permanent damage to the module will result.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
T _{STOR}	Storage temperature		-40		+85	°C
V _{ESD}	ESD robustness	Human Body Model (HBM)			±2	kV
P _{RF}	Input RF level				+14	dBm
V _{DDMAX}	Maximum voltage	Maximum voltage from any pin to ground	-0.3		3.6	V
V _{DIG}	Voltage on all pins	except pins B3,C4,F3,F4	-0.3		V _{DDMAX}	V
V _{ANA}	Voltage on pins B3,C4,F3,F4		-0.3		2.0	V
V_{COMP_IN}	Comparator input voltage	Pins with Comparator input connected by the analog multiplexer	-0.3		V _{DDMAX}	V
V _{PGA_IN}	PGA input voltage	Pins with PGA input connected by the analog multiplexer	-0.3		V _{DDMAX}	V
V _{ADC_IN}	ADC input voltage	Pins with ADC input connected by the analog multiplexer (PGA bypassed)	-0.3		2.0	V
T _{Death}	Surface Mount Solder Reflow Temperature	Refer to chapter 16				

11. OPERATING CONDITIONS

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
Т _{ОР}	Operating temperature range		-40		+85	°C
V _{DD}	Supply voltage	Voltage on pins A2,A3	1.9	3.0	3.6	V
V _{OVRDRV}	Pin Overdrive voltage	Pin Voltage exceeding supply voltage except pin E8			+0.3	V

12. DIGITAL PIN CHARACTERISTICS

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
V _{IH}	High level input voltage ⁽¹⁾	Except pin RSTN pin H5	$0.7 V_{DD}$			V
V _{IL}	Low level input	Except pin RSTN pin H5			$0.3 V_{\text{DD}}$	V

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	ECT 802.15.4-MODEM OMER'S CODE PANASONIC'S CODE 580 / PAN4580ETU PANASONIC'S CODE Symbol Parameter Voltage ⁽¹⁾ Condition VIHRSTN High level input voltage ⁽¹⁾ VILRSTN Low level input voltage ⁽¹⁾ VILRSTN Low level input voltage ⁽¹⁾			3xxEF	DATE		19.10.	2012
Symbol	Parame	eter	Condition	Ν	/lin.	Тур.	Max.	Units
	voltage	(1)						
V _{IHRSTN}	High le voltage	vel input	Pin H5	0	.9V _{DD}			v
V _{ILRSTN}	Low le	vel input	Pin H5				0.1V _{DD}	V
V _{OH}	High le voltage	evel output	t $I_{OH} = -12mA, V_{DD} = 3.6V$ $I_{OH} = -6mA, V_{DD} = 1.8V$ Max. drive strength by DPDS	0	7 _{DD} – .4			v
V _{OL}	Low le voltage	vel output	$I_{OL} = 16mA, V_{DD} = 3.6V$ $I_{OL} = 10mA, V_{DD} = 1.8V$ Max. drive strength by DPDS	S0/1			0.4	V
V _{OHMIN}	High le voltage	evel output	$I_{OU} = -3mA$ $V_{DD} = 3.6V$	N O	/ _{DD} – .4			V
V _{OLMIN}	Low le voltage	vel output	$I_{OL} = 4mA, V_{DD} = 3.6V$ $I_{OL} = 2.5mA, V_{DD} = 1.8V$ Min. drive strength by DPDS	50/1			0.4	V
RGRIO	GPIO p	oull-up	If pull-up resistor is enabled	1	20		360	kΩ

Notes:

R_{GPIO}

(1) The capacitive load should not be larger than 50 pF for all I/Os when using the default driver strength settings. Generally, large capacitance loads increase the overall current consumption.

If pull-up resistor is enabled

120

360

kΩ

13. DC ELECTRICAL CHARACERISTICS

resistor

Assume V_{CC} = 3.0V, $T_{am}b$ = 25°C if nothing else stated

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
I _{TX}	Supply current transmit state ⁽²⁾	PTX = 3.5 dBm		20	27	mA
I _{RX}	Supply current receive state ⁽²⁾	RX_ON state		17	23	mA
I _{SLEEP}	Supply current SLEEP state ⁽²⁾⁽³⁾	SLEEP state		1.5		μΑ

Notes:

(2) When using the SNAP-core

(3) For sleep-mode settings refer to the SNAP Reference Manual [3]

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14. A/D CONVERTER CHARACTERISTICS

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
V _{REFH} ⁽⁴⁾	Voltage Reference, High	Programmable	1.5	1.6	1.8	V
X 7	A 1	Single Ended	0		1.8	V
V _{INDC}	Analog input voltage	Diferential ⁽⁵⁾	0		3.3	V
R _{AS}	Source impedance at input ⁽⁶⁾				3	kΩ
DEC	Conversion	Single Ended CLKADC <= 4MHz		10		D:4-
RES	Resolution	Single Ended CLKADC = 8MHz		8	В	— Bits
DNL	Differential non- linearity	V _{REFH} = 1.6V, CLKADC=4MHz	-0.5			LSB
INL	Integral non-linearity	V _{REFH} = 1.6V, CLKADC=4MHz		0.8		LSB
Ezs	Zero-scale error			1.5		LSB
E _G	Gain error			1		LSB

(4) V_{REFH} is programmable to three fixed values; 1.5V, 1.6V, and 1.8V. The default is 1.6V

(5) Each differential analogue input may be as high as 3.3V but the differential voltage is still limited

(6) Any analog source with a source impedance greater the $3k\Omega$ will increase the sampling time

15.AC ELECTRICAL CHARACTERISTICS

15.1. TRANSMITTER CHARACTERISTICS

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
P _{TX}	TX Output power	Maximum configurable TX output power value	0	3	6	dBm
P _{RANGE}	Output power range	16 steps		20		dB
P _{ACC}	Output power tolerance				±3	dB
P _{HARM}	Harmonics 2 nd harmonic 3 rd harmonic				-35 -35	dBm

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15.2.RECE	EIVER CHARA	\CTEF	RISTICS				
Symbol	Parameter		Condition	Min.	Тур.	Max.	Units
P _{SENS}	Receiver sensit	tivity	250kb/s, PER ≤ 1%		-98		dBm
P _{RXMAX}	Maximum RX level	-	$PER \le 1\%$		10		dBm
P _{ACRN}	Adjacent chann rejection: -5 M	Hz	$PER \le 1\%, PRF = -82 \text{ dBm}$		34		
P _{ACRP}	Adjacent channel rejection: +5 MHz		$PER \le 1\%$, $PRF = -82 \text{ dBm}$		38		
P _{AACRN}	Alternate chann rejection: -10 N	-	$PER \le 1\%$, $PRF = -82 \text{ dBm}$		54		
P _{AACRP}	Alternate chann rejection: +10 M		$PER \le 1\%$, $PRF = -82 \text{ dBm}$		54		
P _{SPUR}	Spurious emiss LO leakage $30 - \le 1000$ MI >1 - 12.75 GH	Hz			-71	-57 -47	dBm dBm dBm
f _{RXTXOFFS}	TX/RX carrier frequency offse		Sensitivity loss < 2 dB	-300 ⁽⁷⁾		+300	kHz
	RSSI tolerance	;	Tolerance within gain step			±5	dB
	RSSI dynamic	range			81		dB
	RSSI resolution	n			3		dB
	RSSI sensitivit	y	Defined as RSSI_BASE_VAL		-90		dBm
	Minimum RSS value	I	$P_{RF} \leq RSSI_BASE_VAL$		0		
	Maximum RSS value	Ы	$P_{RF} > RSSI_BASE_VAL + 81 dB$		28		

(7) Offset equals ±120 ppm



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17. MODULE DIMENSIONS

17.1. SMD MODULE PAN4580 / ENWC9A31XXEF

Item	Dimension	Tolerance	Remark
Width	19.00	± 0.25	
Lenght	29.80	± 0.25	
Height	2.55	± 0.25	With case

17.2. PIN HEADER MODULE PAN4580ETU / ENWC9A33XXEF

Item	Dimension	Tolerance	Remark
Width	33.86	± 0.25	
Lenght	33.86	± 0.25	
Height	7.31	± 0.25	With pin-header



All dimensions are in millimeters.

The outer dimensions have a tolerance of \pm 0.25mm.

Mechanical Requirements

No.	Item	Limit	Condition
1	Solderability	Nara than 15% at the caldering area chall be	Reflow soldering with recommendable temperature profile





For the solder paste screen, use as a first guideline the same foot print as shown in the figure above. Solder paste screen cutouts (with slightly different dimensions) might be optimum depending on your soldering process. For example, the solder paste screen thickness chosen might have an effect. The solder screen thickness depends on your production standard - $100\mu m$ to $120\mu m$ is recommended.

When using a module with an embedded antenna, for optimum RF performance, place the antenna on the edge of your application PCB

If you have any questions on these points, please contact your local Panasonic representative.

Schematics and layouts may be sent to <u>wireless@eu.panasonic.com</u> for final review.



Dimensions in millimeters.

The land pattern dimensions above are meant to serve only as a guid. This information is provided without any legal liability.

When using a module with an embedded antenna, for optimum RF performance, place the antenna on the edge of your application PCB

If you have any questions on these points, please contact your local Panasonic representative.

Schematics and layouts may be sent to <u>wireless@eu.panasonic.com</u> for final review.



The 2D-Barcode contains the following information separated by a semicolon:

Value	Description
Date-code	Date code in the format Year - Calender Week - Day of Week – Line - Lot [YYWWDLL]
MAC-adress/ENWC9A31x5EF (SNAP-software): Last 8 digits of the MAC-adressSerial-numberENWC9A31x1EF (without software): Sequential serial-number	
C9A31xxx (SMD) C9A33xxx (ETU)	Ordering number [8 signs; without the first 3 digits (ENW) and the last digit (F), refer also to chapter 0.
	Ordering Information]
yyzz	The identifier for the hardware release [2 signs, here yy] and the software release [2 signs, here zz]

The point on the marking (below left) is the identifier for pin 1 of the module.

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Meas	ABILITY TE urements she erature and h	ould be c	completed after the module for 1 hour.	has been e	xposed to roo	om	
No.	Item		Limit	Condition			
1	Heat-shock- test	-resitance	Electrical parameter should be in specification	Voltage: of Period: 500	Ocycles	0°C +125°C each 7m	nin.
2	Humidity lo test	bad life	the same as above	Tempreratu Humidity: Period: 500 Voltage: or	ure: +85°C 85% 0hrs		
3	Vibration te	est	the same as above	Vibration A Vibr. Freq.	Ampl.: 1.5mm .: 10~55~10Hz : X; Y; Z each	(
4	ESD test		the same as above	Regarding contact wit	JEDEC JESD- th 100pF, 1.5k0 ody Model HB	-22a114D each Ohm min. 2kV	
5	MSL test		the same as above	Hu	ioning: emperature: +85 umidity: 85°C eriod: 96hrs	5°V	

22. CAUTIONS

Failure to do so may result in degrading of the product's functions and damage to the product.

22.1. DESIGN NOTES

- (1) Follow the conditions written in this specification, especially the control signals of this module.
- (2) The supply voltage has to be free of AC ripple voltage (for example from a battery or a low noise regulator output). For noisy supply voltages, provide a decoupling circuit (for example a ferrite in series connection and a blocking capacitor to ground of at least 47uF directly at the module).

Soldering condition:

Peak: 260°C for ~10s, 2 times

- (3) This product should not be mechanically stressed when installed.
- (4) Heat is the major cause of shortening the life of these products. Keep this product away from heat.
 Avoid assembly and use of the target equipment in conditions where the
- products' temperature may exceed the maximum tolerance. (5) The supply voltage should not be exceedingly high or reversed. It should not
- (5) The supply voltage should not be exceedingly high or reversed. It should not carry noise and/or spikes.
- (6) Keep this product away from other high frequency circuits.

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22.2.INS	TALLATIO		TES			
(1)	Reflow	solderii	ng is possible twice based on the	conditions in cha	pter 16.	
		he tem	perature at the soldering portion of			
(2)			ion the products so that their heat or affect the other components that			
(3)			e these products so that their tem cts of heat generated by neighbor		increase	
(4)	If a viny will mel	l-cover t and g	ed wire comes into contact with the enerate toxic gas, damaging the in	ne products, then		
(5)	This pro	oduct sl	over and these products to occur. hould not be mechanically stresse			
(6)	detailed	l in this	nt to repair your board by hand sol chapter.	ldering, follow the	conditions	
(7) (8)	Refer to	the re	nis product. commended pattern when designi			
(9)			on the metal cover or fastening o module.	bjects to the cove	er will cause	
22.3.USA	GE CON	DITION	IS NOTES			
(1)	If pulses	s or oth to the	s to protect the unit against static her transient loads (a large load ap products, check and evaluate their oducts	oplied in a short ti		
(2)	Do not i	use dro	pped products.			
(3) (4)	Follow t	he reco	damage or place dirt on the pins. commended condition ratings abou	t the power suppl	y applied to	
(5)		de peel	ing strength: Do not add pressure	of more than 4.9	N when	
(6)		g on pa	arts of the metal cover or fastening	g objects to the m	etal cover	
(7)	electror	product nic equi	age. s are intended for general purpose pment, such as home appliances, d communication equipment.			
22.4.STC	RAGE N	OTES				
(1) (2)	Do not s	store th eristics	ay not be stressed mechanically d lese products in the following cond of the product, such as RF perfor	ditions or the perf		
(3)	Storage	in salt	y air or in an environment with a h l2, H2S, NH3, SO2, or NOX	igh concentratior	n of corrosive	
(4) (5)	Storage	in dire	ct sunlight environment where the temperature	re may be outside	e the range o	f
(6)	5°C to 3	35°C ra	nge, or where the humidity may b products for more than one year a	e outside the 45	to 85% range	e.

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 company if the avoidance all the above conditions (1) (7) Storage period: Check the adhesive strength of the e soldering after 6 months of storage. (8) Keep this product away from water, poisonous gas at (9) This product should not be stressed or shocked wher (10) Follow the specification when stacking packed crates 	mbossed tap nd corrosive g n transported	e and gas.	

These specifications are intended to preserve the quality assurance of products and individual components.

Before use, check and evaluate the operation when mounted on your products. Abide by these specifications, without deviation when using the products. These products may short-circuit. If electrical shocks, smoke, fire, and/or accidents involving human life are anticipated when a short circuit occurs, then provide the following failsafe functions, as a minimum.

- (1) Ensure the safety of the whole system by installing a protection circuit and a protection device.
- (2) Ensure the safety of the whole system by installing a redundant circuit or another system to prevent a single fault causing an unsafe status.

22.6. OTHER CAUTIONS

- (1) This specification sheet is copyrighted. Do not disclose it to a third party.
- (2) Do not use the products for other purposes than those listed.
- (3) Be sure to provide an appropriate fail-safe function on your product to prevent an additional damage that may be caused by the abnormal function or the failure of the product.
- (4) This product has not been manufactured with any ozone chemical controlled under the Montreal Protocol.
- (5) These products are not intended for other uses, other than under the special conditions shown below. Before using these products under such special conditions, check their performance and reliability under the said special conditions carefully to determine whether or not they can be used in such a manner.
- (6) In liquid, such as water, salt water, oil, alkali, or organic solvent, or in places where liquid may splash.
- (7) In direct sunlight, outdoors, or in a dusty environment
- (8) In an environment where condensation occurs.
- (9) In an environment with a high concentration of harmful gas (e.g. salty air, HCl, Cl2, SO2, H2S, NH3, and NOX)
- (10) If an abnormal voltage is applied due to a problem occurring in other components or circuits, replace these products with new products because they may not be able to provide normal performance even if their electronic characteristics and appearances appear satisfactory.
- (11) When you have any question or uncertainty, both of you and Panasonic sincerely cope with it.





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23.5.LAB	EL FOR	PACKAGE				
The label	below sł	nows only a	in example.			
Panas	onic /		0801511			
(1P):PA			(2P):ENWC9A31xxEF			
(9D): 080	01 (Q): 5		(HW/SW): 01/01			
		ISL/Temp: 3/260				
			[[G]]			
(1T)	:	Lotco YY	de [YYWWDLL] year	above 08		
		WW D	normal calendar week day	above 01 above 5 (F	ridav)	
		L	line identifier, if more as one lot identifier per day	actual 1 e.g. 1, 2, 3		
(1P)			mers Order Code.	0, , ,		
(2P)	Pa	anasonic Pa	rt Number, refer to chapter 0			
PANASC	NIC IND	USTRIAL [DEVICES EUROPE GMBH	www.pide	eu.panasonic	.de

UBJECT SUSTOMER'S C PAN4580 / PAN4 Orderin (9D) (Q) (HW/S)		802.15.4-MODEM PANASONIC'S CODE ENWC9A31xxEF / ENWC9A33xxEF	PAGE DATE	27 of 34 19.10.2012
2AN4580 / PAN4 Orderin (9D) (Q)	580ETU		DATE	19.10.2012
(9D) (Q)	ng Informatio			
[[G]]	Q W) H	n atecode as [2xYear, 2xMonth, 2xDay] uantity [XXXX], variable ardware /Software Release identifier lentifier that the product is RoHS conform	n, refer to chapi	ter 25.



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24. ORDERING INFORMATION

No.	Ordering part number	Description
		PAN4580 – 2.4GHz
1		802.15.4 Mesh Network SMD-Module, which includes Ceramic Antenna
1	ENWC9A31A1EF	and 128kbyte Flash Memory.
		No software included.
		PAN4580 – 2.4GHz
-		802.15.4 Mesh Network SMD-Module, which includes UFL connector and
2	ENWC9A31B1EF	128kbyte Flash Memory.
		No software included.
		PAN4580 – 2.4GHz
		802.15.4 Mesh Network SMD-Module, whith <i>RF out on SMD pad</i> and
3	ENWC9A31C1EF	128kbyte Flash Memory.
		No software included.
		PAN4580 – 2.4GHz
		802.15.4 Mesh Network SMD-Module, which includes <i>Ceramic Antenna</i>
4	ENWC9A31A5EF	and 128kbyte Flash Memory, with 69.5kbyte used by SNAP core, 58.5kbyte
•		free for uploadable SNAPpy scripts.
		Synapse SNAP software included, refer also to [3].
		PAN4580 – 2.4GHz
		802.15.4 Mesh Network SMD-Module, which includes <i>UFL connector</i> and
5	ENWC9A31B5EF	128kbyte Flash Memory, with 69.5kbyte used by SNAP core, 58.5kbyte free
		for uploadable SNAPpy scripts.
		Synapse SNAP software included, refer also to [3].
		PAN4580 – 2.4GHz
		802.15.4 Mesh Network SMD-Module, whith <i>RF out on SMD pad</i> and
6	ENWC9A31C5EF	128kbyte Flash Memory, with 69.5kbyte used by SNAP core, 58.5kbyte free
-		for uploadable SNAPpy scripts.
		Synapse SNAP software included, refer also to [3].
		PAN4580ETU – 2.4GHz
_		802.15.4 Mesh Network Pin-Header-Module, which includes Ceramic
7	ENWC9A33A1EF	Antenna and 128kbyte Flash Memory.
		No software included.
		PAN4580ETU – 2.4GHz
		802.15.4 Mesh Network Pin-Header-Module, which includes <i>UFL</i>
8	ENWC9A33B1EF	<i>connector</i> and 128kbyte Flash Memory.
		No software included.
		PAN4580ETU – 2.4GHz
		802.15.4 Mesh Network Pin-Header-Module, which includes <i>Ceramic</i>
9	ENWC9A33A5EF	Antenna and 128kbyte Flash Memory, with 69.5kbyte used by SNAP core,
/	EITWOMODISEI	58.5kbyte free for uploadable SNAPpy scripts.
		Synapse SNAP software included, refer also to [3].
		PAN4580ETU – 2.4GHz
		802.15.4 Mesh Network Pin-Header-Module, which includes <i>UFL</i>
10	ENWC9A33B5EF	<i>connector</i> and 128kbyte Flash Memory, with 69.5kbyte used by SNAP core,
10		58.5kbyte free for uploadable SNAPpy scripts.
		Synapse SNAP software included, refer also to [3].
		synupse shart software metadou, tetet also to [5].

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25.ROHS [DECLAR	ATION				
Hereby v this prod 2002/95/	we declare luct does	e to our not cont S) or co	ntal compatibility for supplied products: best present knowledge based on decla tain the following substances which are b ntains a maximum concentration of 0.1% or	banned by l	Directive	t
•	Lead and Mercury a Chromiur PBB (pol	d lead co and mei m (VI) ybromin	ompounds rcury compounds nated biphenyl) category inated biphenyl ether) category			
			ation of 0.01% by weight in homogeneou admium compounds	us materials	s for	
be publis to chang possible	a sheet co shed at a je the spe product.	ontains o later dat cificatio	data from the PRELIMINARY specification te. Panasonic Electronic Devices Europe n without notice, in order to improve the	e GmbH res design and	serves the right supply the bes	t
			issued data sheet before initiating or co updates: <u>PAN4580 Latest Data Sheet!</u>	mpleting a	design.	
27.REGUL	ATORY	INFOR	MATION			
The UFI anto The pub (1) any The	L type (EN ennas, wh e device n blic Notice This devic / interfere e FCC ide	PAN458 NWC9A nich are neets th DA00- ce may nce rece	0, including the ceramic antenna (ENWC 31BxEF) and SMD type (ENWC9A31Cx listed in 27.5, complies with Part 15 of the e requirements for modular transmitter a 1407.transmitter Operation is subject to the not cause harmful interference, and (2) eived, including interference that may cause FCC ID: T7V-4580 .	EF), includi he FCC Ru approval as the followin This device	ing with the les. detailed in FC g two condition must accept	
dev 👔	e FCC req /ice that a	ire not e	e user to be notified that any changes of xpressly approved by Panasonic Electro authority to operate the equipment.			

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide

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reas	sonable p	orotectic	n against harmful interference in a resic	lential installa	tion. This	

equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help

27.3. LABELING REQUIREMENTS



The Original Equipment Manufacturer (OEM) must ensure that FCC labeling requirements are met. This includes a clearly visible label (laser marking) on the outside of the OEM enclosure specifying the appropriate Panasonic FCC identifier for this product as well as the FCC Notice above. The FCC identifier is **FCC ID: T7V-4580**. In any case end product must be labelled exterior with "Contains FCC ID: T7V-4580"

27.4. ANTENNA WARNING



The related part number for this device are ENWC9A31BxEF (PAN4580 with U.FL connector) and ENWC9A31CxEF (PAN4580 with SMD pad). For details, see the chapter 24.Ordering Information. This device is tested with a standard SMA connector and with the antennas listed below. When integrated in the OEMs product, these fixed antennas require installation preventing end-users from replacing them with non-approved antennas. Any antenna not in the following table must be tested to comply with FCC Section 15.203 for unique antenna connectors and Section 15.247 for emissions. The FCC identifier for this device with the antenna listed in item 1 are the same (FCC ID: T7V-2580).

27.5. APPROVED ANTENNA LIST

Note: We are able to qualify your antenna and will add to this list as that process is completed.

Item	Part Number	Manufacturer	Frequency Band	Туре	Gain (dBi)
1	LDA312G4413H-280	Murata	2.4GHz	Chip	-2.3
2					

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To co (OEN The p produ FCC Any r integ The r (FCC the P durin End u integ	I) must ensure preceding state ucts operating RF Exposure of notification to the rated radio mo radiated output D: T7V-4580 AN4580 shall g normal opera- users may not rators and end	C RF Exposure requirements, the O e that the approved antenna in the p ement must be included as a CAUT with the approved antennas in the p	revious table must ION statement in n previous table to al al instructions about ceramic antenna exposure limits. N potential for humar ation instructions. (be installed. nanuals for ert users on ut the evertheless, n contact DEM	
PAN4580 IC: 216Q-4 Manufactu clarify any Users ca www.ic.gc This devic Antennas required a	is licensed to 4580 regulatory qu n obtain Ca ca. ce has been d not included ntenna impeda	CERTIFICATION meet the regulatory requirements of s, fixed or portable devices incorpor estions and ensure compliance for anadian information on RF exp lesigned to operate with the anten in this list are strictly prohibited ance is 50 ohms. The antenna used in conjunction with any other anten	ating this module SAR and/or RF e posure and con nas listed in Tabl for use with this d for this transmitt	are advised xposure limit ppliance fro e 27.5 abov s device. Th	to s. m e. ne
Hereby, P and their	anasonic Elec versions is in	ECLARATION OF CONFORMIT stronic Devices Europe GmbH, dec compliance with the essential re	lares that the mo- equirements and	other releva	nt
provisions described labelled as PAN4580 countries: Germany, Portugal,	of Directive in Annex III o s follows: and their vers Austria, Belgi Greece, Hung	1999/5/EC. As a result of the co f the Directive 1999/5/EC, the end ions in the specified reference des um, Cyprus, Czech Republic, Den gary, Ireland, Italy, Latvia, Lithuan venia, Spain, Sweden, The Netl	nformity assessm -customer equipm ign can be used in mark, Estonia, Fin ia, Luxembourg, I	ent procedu hent should t n the followir nland, Franc Malta, Polan	re be ng e, d,

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30.RELATED DOCUMENTS												
For an u	pdate, ple	ase sea	arch in the suitable homepage.									
La		E Standard 802.15.4 –2003 Wireless Medium Access Control (MAC) and Physical er (PHY) Specifications for Low-Rate Wireless Personal Area Networks (LR- ANs)										
		hnical Datasheet ATmega128RFA1 I can download the latest datasheet from the Atmel homepage <u>here</u> .										
Pa]	anasonic [nual to the Evaluation Kit from Synapse, which fits to module hardware from nasonic [Downloads: SNAP [®] <u>Reference Guide</u> ; SNAP [®] <u>Hardware Technical Manual</u>										
	Each new release from Synapse, will be posted <u>here</u> . Be sure to be registered free under <u>http://forums.synapse-wireless.com</u> .											
31.GENER	31. GENERAL INFORMATION											
	© Panasonic Electronic Devices Europe GmbH 2012.											
All rights reserved. This product description does not claim to be complete and free of mistakes. Please contact the related product manager with any errata inquries.												
If we deliver samples to the customer, these samples have the status Engineering Samples. This means, the design of this product is not yet completed. Engineering Samples may be partially or fully functional, and there may be differences published in the												
Data Sheet. Engineering Samples are not qualified and are not to be used for reliability testing or series production.												
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32. LIFE SUPPORT POLICY

This Panasonic Electronic Devices Europe GmbH product is not designed for use in life support appliances, devices, or systems where malfunction can reasonably be expected to result in a significant personal injury to the user, or as a critical component in any life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panasonic Electronic Devices Europe GmbH for any damages resulting.



Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

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С нами вы становитесь еще успешнее!

Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

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