



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

ECH8657 — General-Purpose Switching Device Applications

Features

- 4V drive
- Halogen free compliance
- Protection diode in

Specifications

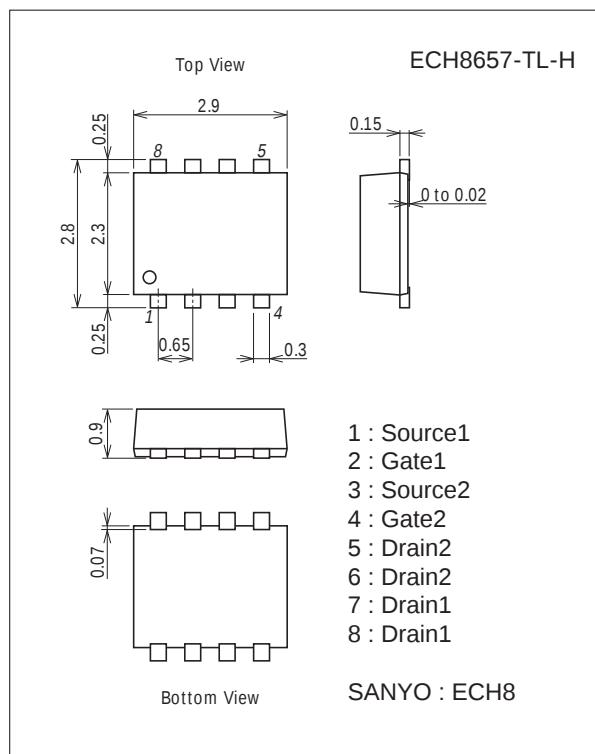
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		35	V
Gate-to-Source Voltage	V _{GSS}		±20	V
Drain Current (DC)	I _D		4.5	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	30	A
Allowable Power Dissipation	P _D	When mounted on ceramic substrate (1200mm ² ×0.8mm) 1unit	1.3	W
Total Dissipation	P _T	When mounted on ceramic substrate (1200mm ² ×0.8mm)	1.5	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Package Dimensions

unit : mm (typ)

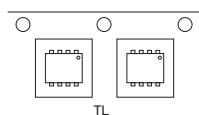
7011A-001



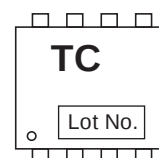
Product & Package Information

- Package : ECH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

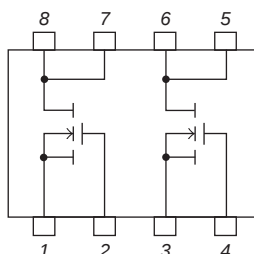
Packing Type : TL



Marking



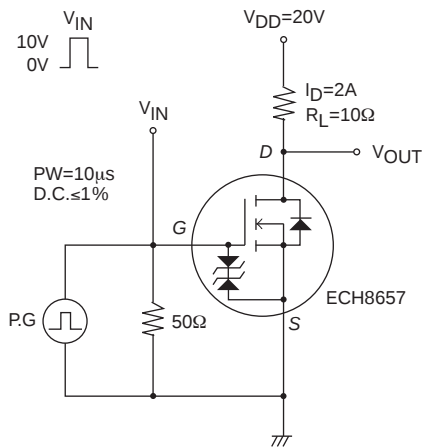
Electrical Connection



Electrical Characteristics at Ta=25°C

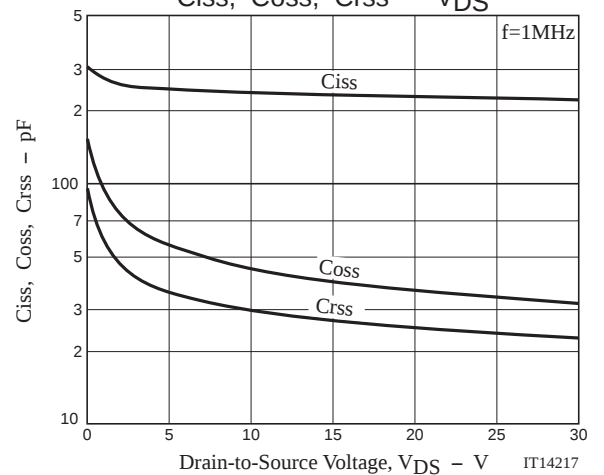
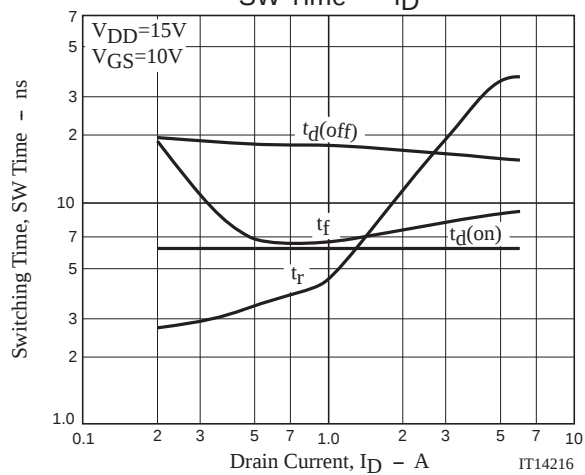
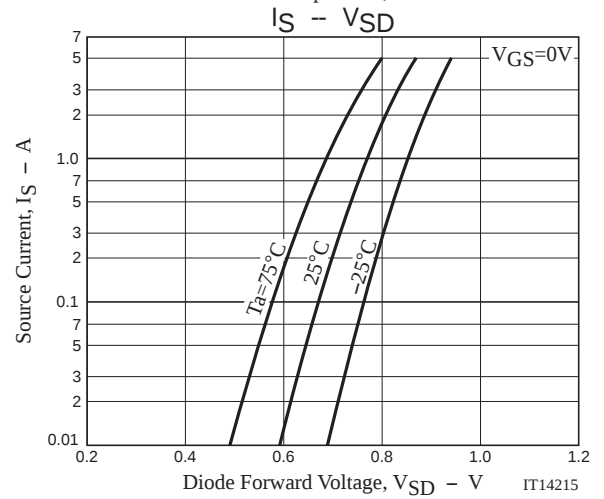
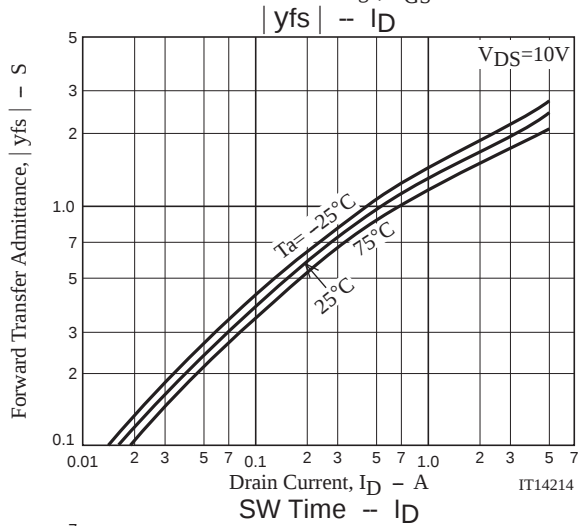
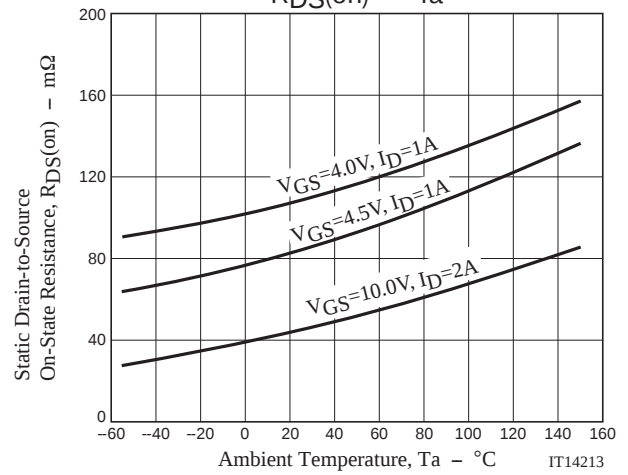
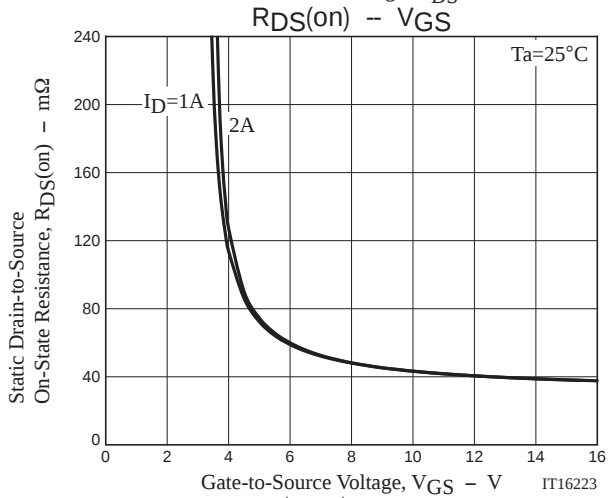
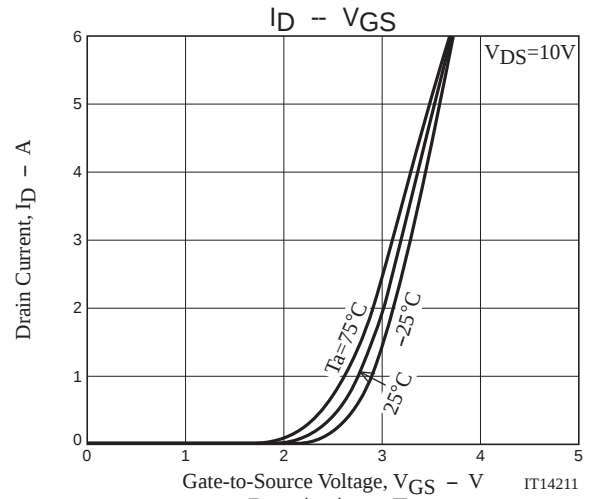
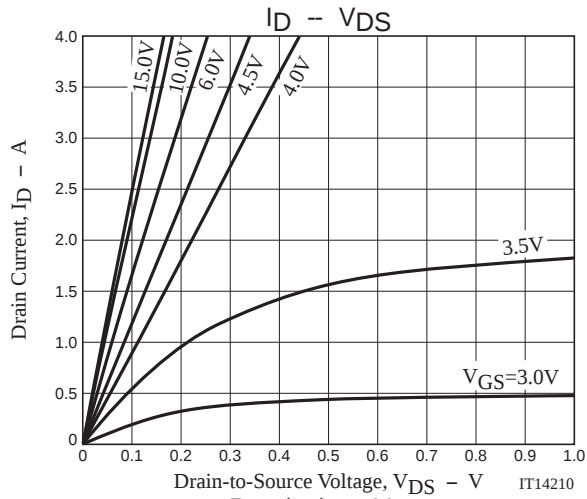
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1mA, V_{GS}=0V$	35			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=35V, V_{GS}=0V$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16V, V_{DS}=0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	1.2		2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=2A$		1.66		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=2A, V_{GS}=10V$		45	59	m Ω
	$R_{DS(on)2}$	$I_D=1A, V_{GS}=4.5V$		85	119	m Ω
	$R_{DS(on)3}$	$I_D=1A, V_{GS}=4V$		110	155	m Ω
Input Capacitance	C_{iss}	$V_{DS}=20V, f=1MHz$		230		pF
Output Capacitance	C_{oss}			37		pF
Reverse Transfer Capacitance	C_{rss}			25		pF
Turn-ON Delay Time	$t_d(on)$			6		ns
Rise Time	t_r	See specified Test Circuit.		11		ns
Turn-OFF Delay Time	$t_d(off)$			17		ns
Fall Time	t_f			9		ns
Total Gate Charge	Q_g	$V_{DS}=20V, V_{GS}=10V, I_D=4.5A$		4.6		nC
Gate-to-Source Charge	Q_{gs}			1.0		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			1.0		nC
Diode Forward Voltage	V_{SD}	$I_S=4.5A, V_{GS}=0V$		0.85	1.2	V

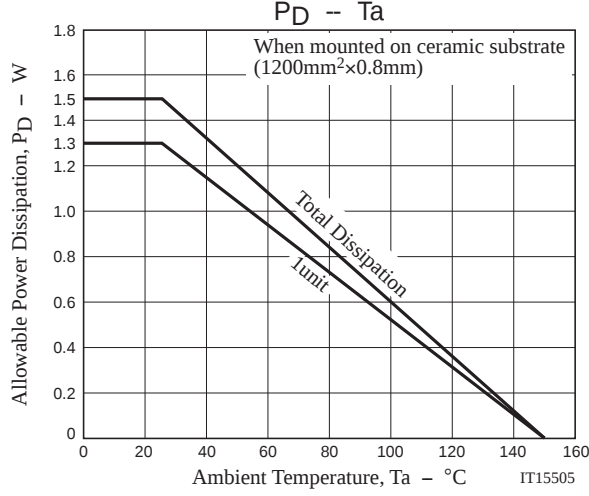
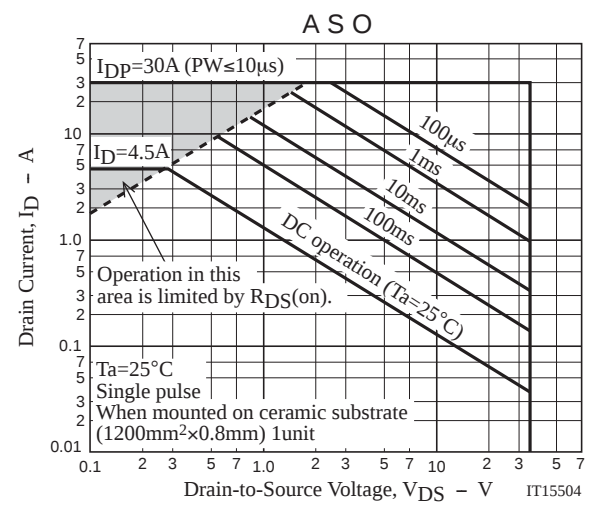
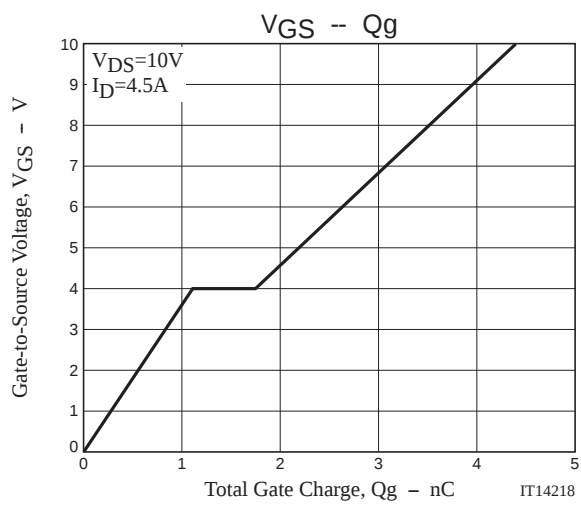
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
ECH8657-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free





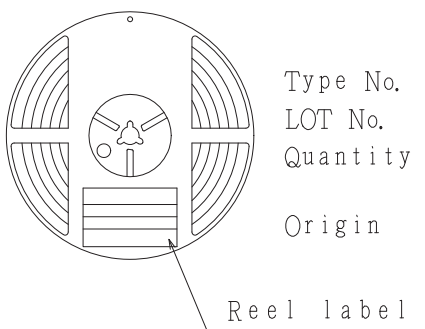
Embossed Taping Specification

ECH8657-TL-H

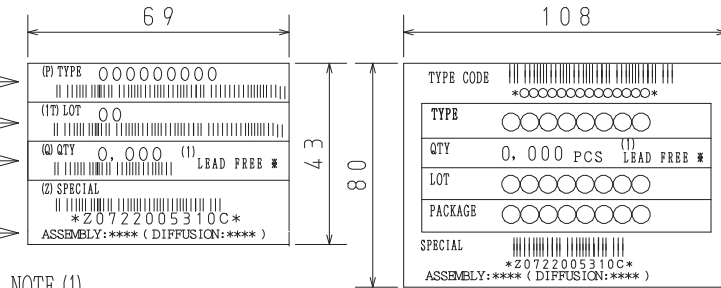
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
ECH8	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method

Reel label, Inner box label
(unit:mm)

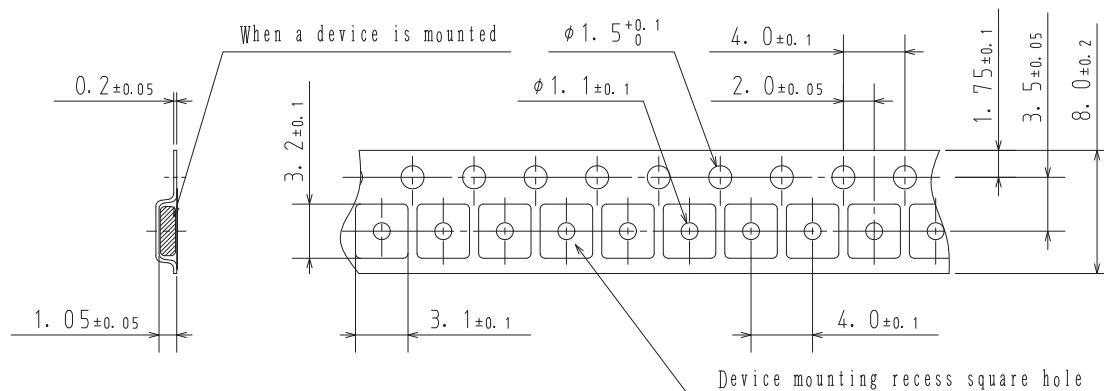
Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

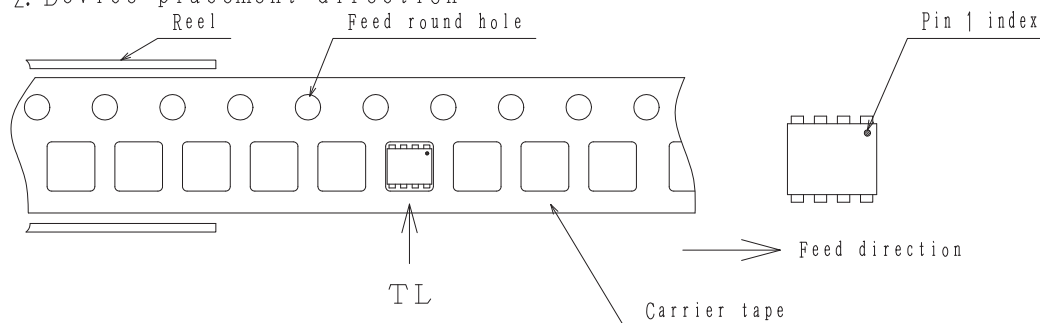
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

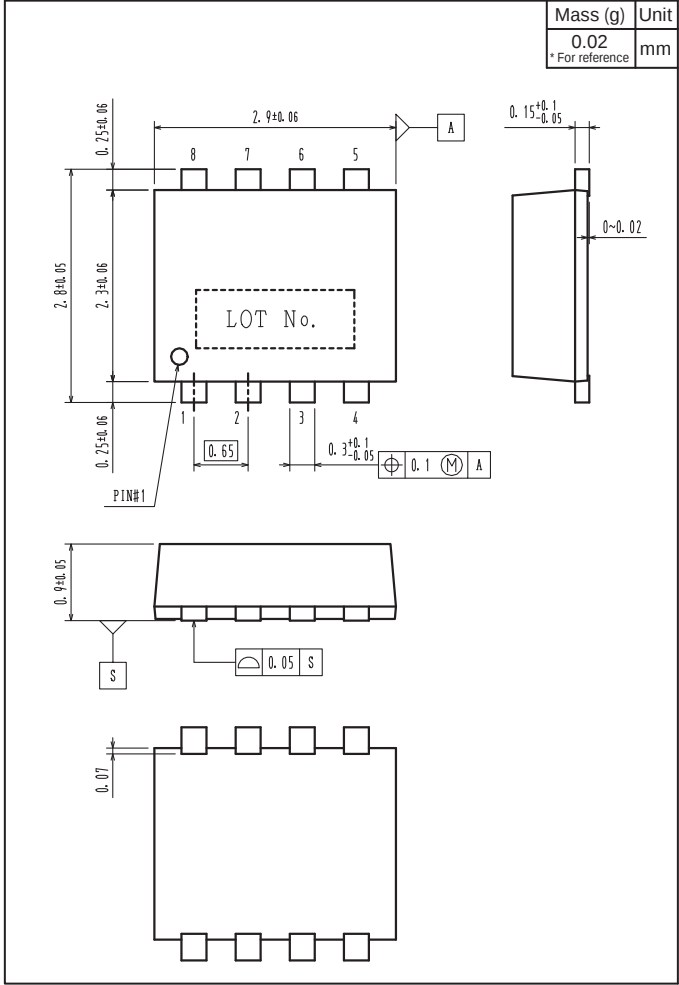


2-2. Device placement direction

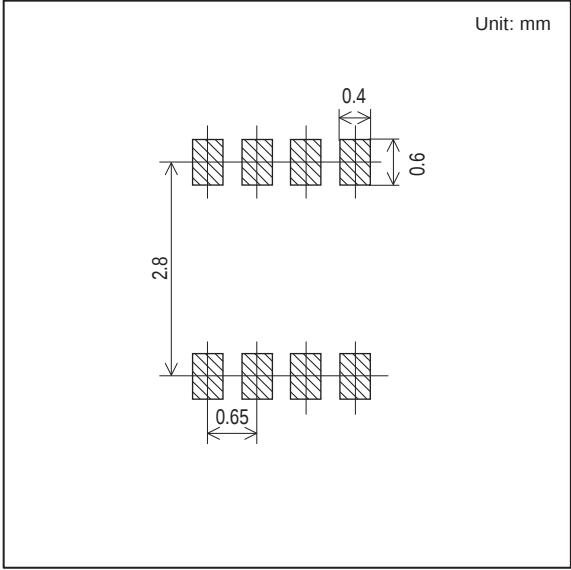


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
ECH8657-TL-H



Land Pattern Example



Note on usage : Since the ECH8657 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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