

BUK765R2-40B

N-channel TrenchMOS standard level FET Rev. 3 — 22 November 2011

Product data sheet

1. **Product profile**

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

- Q101 compliant
- Suitable for standard level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

1.3 Applications

- 12 V loads
- Automotive systems

- General purpose power switching
- Motors, lamps and solenoids

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	40	V
I _D	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C}; \text{ see } \underline{\text{Figure 1}}; \underline{11}$ see $\underline{\text{Figure 3}}$	-	-	75	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	203	W
Static chara	cteristics					
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 ^{\circ}\text{C};$ see <u>Figure 11</u> ; see <u>Figure 12</u>	-	4.4	5.2	mΩ
Dynamic ch	aracteristics					
Q_{GD}	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; V_{DS} = 32 \text{ V};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure } 13}{\text{ Composition}}$	-	16	-	nC
Avalanche r	uggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 75 A; $V_{sup} \le 40$ V; R_{GS} = 50 Ω ; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped	-	-	494	mJ

^[1] Continuous current is limited by package.



2. Pinning information

Table 2. Pinning information

I doic 2.		inioniation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain[1]	mb	D
3	S	source		。(巨 木)
mb	mb D mounting base; connected	mounting base; connected to drain		mbb076 S
			SOT404 (D2PAK)	

^[1] It is not possible to make a connection to pin 2.

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK765R2-40B	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	40	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$		-	40	V
V_{GS}	gate-source voltage			-20	20	V
I _D	drain current	$T_{mb} = 25 \text{ °C}; V_{GS} = 10 \text{ V}; \text{see } \frac{\text{Figure 1}}{\text{Figure 1}};$	<u>[1]</u>	-	143	Α
		see <u>Figure 3</u>	[2]	-	75	Α
		T _{mb} = 100 °C; V _{GS} = 10 V; see Figure 1	[2]	-	75	Α
I _{DM}	peak drain current	T_{mb} = 25 °C; pulsed; $t_p \le 10 \mu s$; see Figure 3		-	573	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	203	W
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-dra	ain diode					
I _S	source current	T _{mb} = 25 °C	<u>[1]</u>	-	143	Α
			[2]	-	75	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	573	Α
Avalanche	ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 75 A; $V_{sup} \le 40$ V; R_{GS} = 50 Ω ; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped		-	494	mJ

- [1] Current is limited by power dissipation chip rating.
- [2] Continuous current is limited by package.

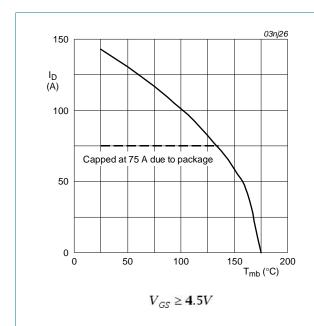


Fig 1. Continuous drain current as a function of mounting base temperature

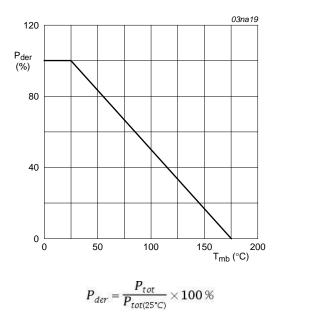
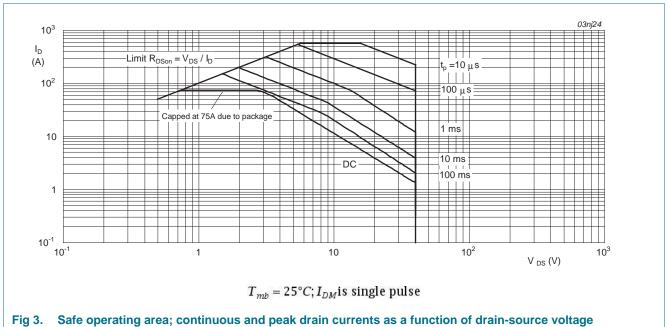


Fig 2. Normalized total power dissipation as a function of mounting base temperature

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5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see <u>Figure 4</u>	-	-	0.74	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	minimum footprint; mounted on a printed-circuit board	-	50	-	K/W

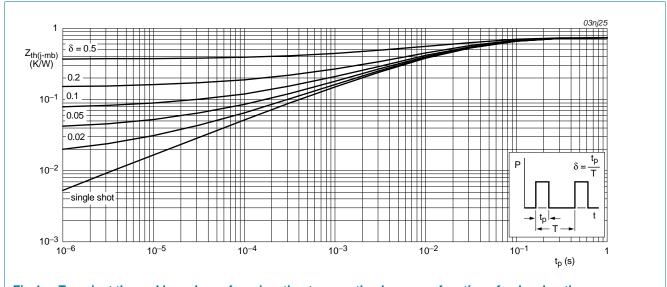


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Characteristics

Table 6. Characteristics

Council - I	Characteristics	Conditions	N#!	T	NA	1124
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
	racteristics					.,
$V_{(BR)DSS}$	drain-source breakdown	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	40	-	-	V
	voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	36	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ °C}$; see <u>Figure 10</u>	2	3	4	V
		$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = -55$ °C; see Figure 10	-	-	4.4	V
		$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = 175$ °C; see Figure 10	1	-	-	V
I _{DSS}	drain leakage current	$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	1	μΑ
		V _{DS} = 40 V; V _{GS} = 0 V; T _i = 175 °C	-	-	500	μΑ
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _i = 25 °C	-	2	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
R _{DSon} drain-source on-state resistance		$V_{GS} = 10 \text{ V}$; $I_D = 25 \text{ A}$; $T_j = 175 \text{ °C}$; see Figure 11; see Figure 12	-	-	9.9	mΩ
	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C};$ see Figure 11; see Figure 12	-	4.4	5.2	mΩ	
Dynamic	characteristics					
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 32 \text{ V}; V_{GS} = 10 \text{ V};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure } 13}{\text{ Figure } 13}$	-	52	-	nC
Q _{GS}	gate-source charge		-	12	-	nC
Q_{GD}	gate-drain charge		-	16	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$	-	2842	3789	pF
C _{oss}	output capacitance	T _j = 25 °C; see <u>Figure 14</u>	-	711	853	pF
C _{rss}	reverse transfer capacitance		-	296	406	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1.2 \Omega; V_{GS} = 10 \text{ V};$	-	15	-	ns
t _r	rise time	$R_{G(ext)} = 10 \Omega$; $T_j = 25 °C$	-	51	-	ns
t _{d(off)}	turn-off delay time		-	81	-	ns
t _f	fall time		-	56	-	ns
L _D	internal drain inductance	from drain lead 6 mm from package to centre of die; $T_j = 25 ^{\circ}\text{C}$	-	4.5	-	nΗ
		from upper edge of drain mounting base to centre of die; $T_j = 25$ °C	-	2.5	-	nΗ
L _S	internal source inductance	from source lead to source bond pad ; $T_j = 25 ^{\circ}\text{C}$	-	7.5	-	nΗ
Source-dr	rain diode					
V_{SD}	source-drain voltage	$I_S = 25 \text{ A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ °C}$; see Figure 15	-	0.85	1.2	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s};$	-	54	-	ns
Qr	recovered charge	$V_{GS} = -10 \text{ V}; V_{DS} = 20 \text{ V}; T_j = 25 \text{ °C}$	-	38	-	nC

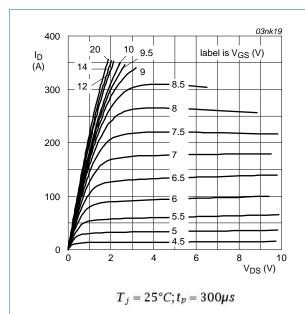


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values

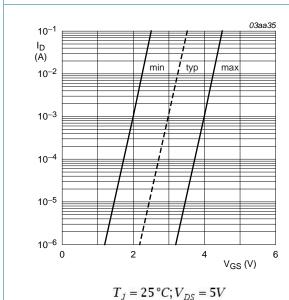
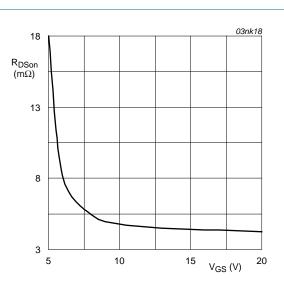


Fig 7. Sub-threshold drain current as a function of gate-source voltage



 $T_j = 25^{\circ}C; I_D = 25A$

Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

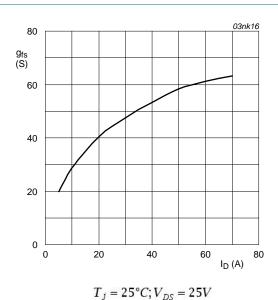


Fig 8. Forward transconductance as a function of drain current; typical values

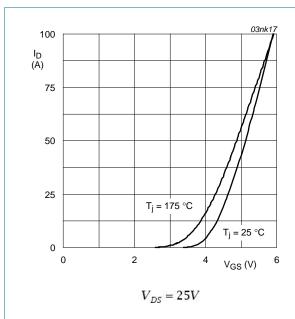


Fig 9. Transfer characteristics: drain current as a function of gate-source voltage; typical values

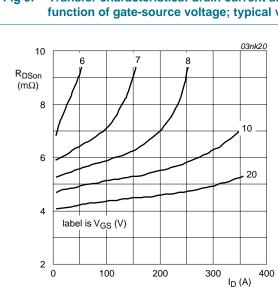


Fig 11. Drain-source on-state resistance as a function of drain current; typical values

 $T_j = 25^{\circ}C$

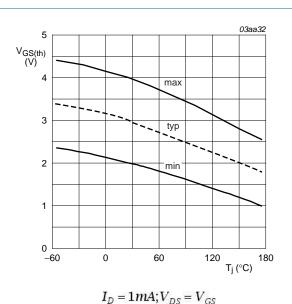


Fig 10. Gate-source threshold voltage as a function of junction temperature

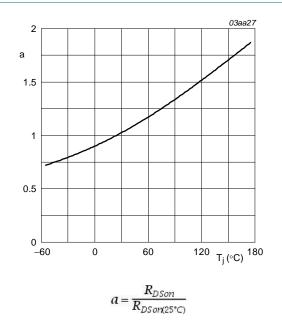


Fig 12. Normalized drain-source on-state resistance factor as a function of junction temperature

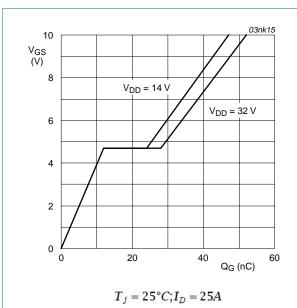


Fig 13. Gate-source voltage as a function of gate charge; typical values

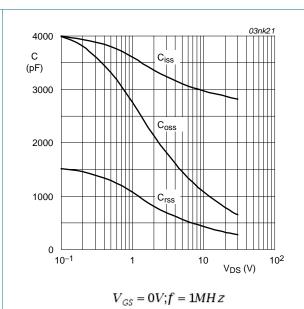


Fig 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

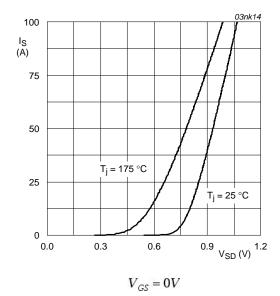


Fig 15. Source current as a function of source-drain voltage; typical values

7. Package outline

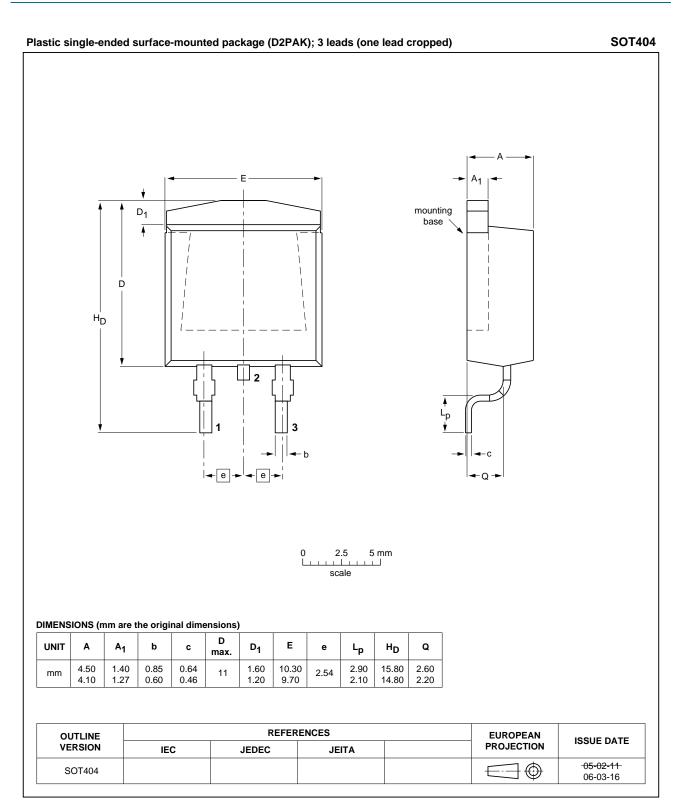


Fig 16. Package outline SOT404 (D2PAK)

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK765R2-40B v.3	20111122	Product data sheet	-	BUK765R2-40B v.2
Modifications:	 Various change 	es to content.		
BUK765R2-40B v.2	20090116	Product data sheet	-	BUK75_765R2_40B v.1

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9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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