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September 2015

# **KSP55 / KSP56 PNP Epitaxial Silicon Transistor**

#### **Features**

- Collector-Emitter Voltage: V<sub>CEO</sub> = KSP55: -60 V
  - KSP56: -80 V
- Collector Dissipation: P<sub>C</sub> (max.) = 625 mW
- Complement to KSP05/06



#### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
KSP55TA	KSP55	TO-92 3L	Ammo
KSP56TA	KSP56	TO-92 3L	Ammo

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter		Value	Unit	
V <sub>CBO</sub>	Collector-Base Voltage	KSP55	-60	- V	
	Collector-base voltage	KSP56	-80		
V <sub>CEO</sub>	Collector-Emitter Voltage	KSP55	-60	V	
	Collector-Emitter voltage	KSP56	-80		
V <sub>EBO</sub>	Emitter-Base Voltage	-4	V		
I <sub>C</sub>	Collector Current	-500	mA		
P <sub>C</sub>	Collector Power Dissipation	625	mW		
$T_J$	Junction Temperature	150	°C		
T <sub>STG</sub>	Storage Temperature	-55 to 150	°C		

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Max.	Unit
D\/	Collector-Emitter Breakdown Voltage <sup>(1)</sup>	KSP55	I <sub>C</sub> = -1 mA, I <sub>B</sub> = 0	-60		V
		KSP56		-80		
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage		$I_E = -100  \mu A,  I_C = 0$	-4		V
I <sub>CBO</sub> Co	Collector Cut-Off Current	KSP55	$V_{CB} = -60 \text{ V}, I_{E} = 0$		-0.1	^
		KSP56	$V_{CB} = -80 \text{ V}, I_{E} = 0$		-0.1	μΑ
I <sub>CEO</sub>	Collector Cut-Off Current		$V_{CE} = -60 \text{ V}, I_{B} = 0$		-0.1	μΑ
h <sub>FE</sub> DC Curre	DC Current Gain		$V_{CE} = -1 \text{ V}, I_{C} = -10 \text{ mA}$	50		
	DO Current Gain		$V_{CE} = -1 \text{ V}, I_{C} = -100 \text{ mA}$	50		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage		$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$		-0.25	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage		$V_{CE} = -1 \text{ V}, I_{C} = -100 \text{ mA}$		-1.2	V
f <sub>T</sub>	Current Gain Bandwidth Product		$V_{CE} = -2 \text{ V}, I_{C} = -10 \text{ mA},$ f = 100 MHz	105		MHz

#### Note:

1. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%.

## **Typical Performance Characteristics**

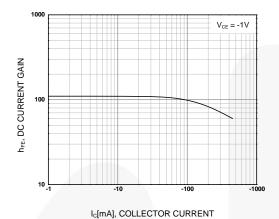


Figure 1. DC Current Gain

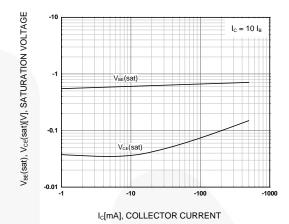


Figure 2. Collector-Emitter Saturation Voltage and Base-Emitter Saturation Voltage

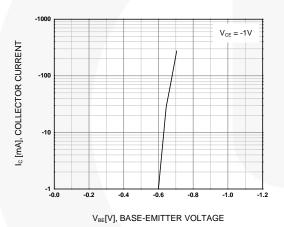


Figure 3. Base-Emitter On Voltage

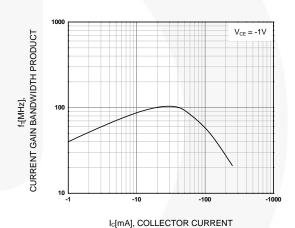


Figure 4. Current Gain Bandwidth Product

## **Physical Dimensions**

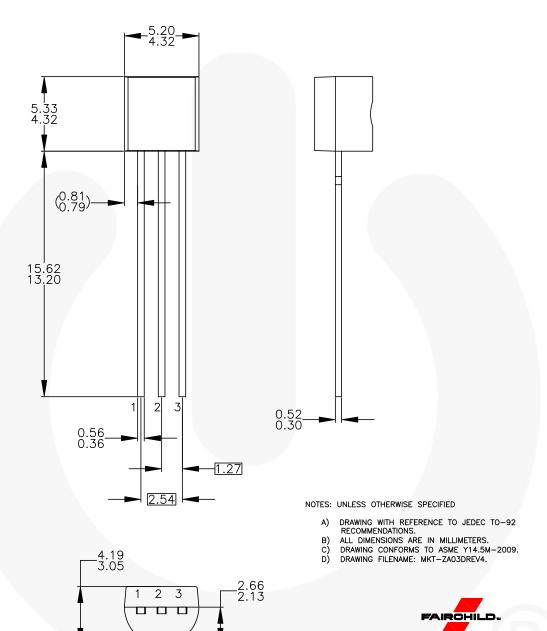
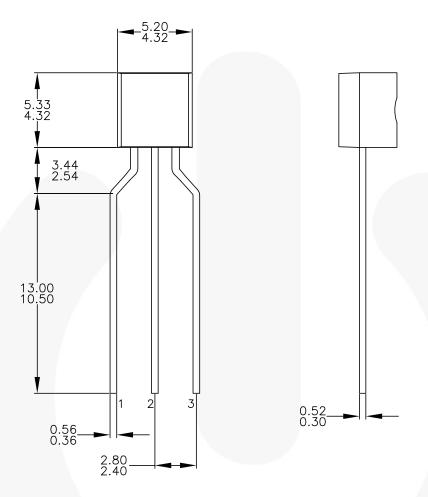
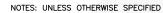


Figure 5. 3-Lead, TO-92, JEDEC TO-92 Compliant Straight Lead Configuration, Bulk Type

### Physical Dimensions (Continued)





- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC.
  ALL DIMENSIONS ARE IN MILLIMETERS.
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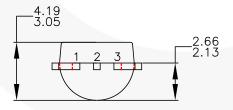


Figure 6. 3-Lead, TO-92, Molded, 0.2 In Line Spacing Lead Form, Ammo, Tape and Reel Type





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