TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4W66F,TC4W66FU

Dual Bilateral Switch

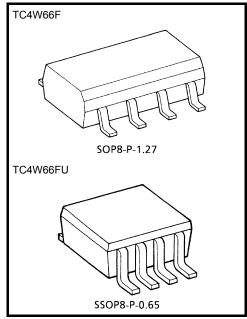
The TC4W66 contains two independence circuits of bidirectional switches.

When control input CONT is set to "H" level, the impedance between input and output of the switch becomes low and when it is set to "L" level, the switch becomes high. This can be applied for switching of analog signals and digital signals.

Features

• ON-resistance, RON

• OFF-resistance, ROFF $ROFF \ (typ.) > 10^9 \ \Omega$



Weight SOP8-P-1.27: 0.05 g (typ.) SSOP8-P-0.65: 0.02 g (typ.)

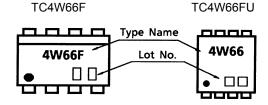
Absolute Maximum Ratings

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V _{SS} – 0.5 to V _{SS} + 20	V
Control input voltage	V _{C IN}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
Switch I/O voltage	V _{I/O}	V _{SS} – 0.5 to V _{DD} + 0.5	V
Power dissipation	PD	300	mW
Potential difference across I/O during ON	V _I -V _O	±0.5	٧
Control input current	I _{C IN}	±10	mA
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature	T _{stg}	-65 to 150	°C
Lead temp./time	TL	260°C/10 s	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

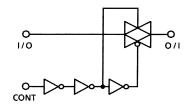
Marking



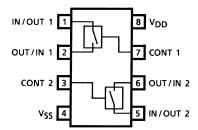
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Logic Diagram

(1/2 TC4W66F)



Pin Assignment (top view)



Truth Table

Control	Impedance Between IN/OUT-OUT/IN (Note 1)
Н	0.5 to $5 \times 10^2 \Omega$
L	>10 ⁹ Ω

Note 1: See static electrical characteristics.

Operating Ranges (V_{SS} = 0 V)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input/output voltage	V _{DD} /V _{OUT}	_	0	_	V_{DD}	V

Static Electrical Characteristics (in case not specifically appointed, V_{SS} = 0 V)

Characteristics		Symbol	Test Circuit	T . 0		Ta = -40°C		Ta = 25°C			Ta = 85°C		
				Test Condition	V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
			_	I _{IS} = 10 μA	5	3.5	_	3.5	2.75	_	3.5	_	
Control ir voltage	Control input high voltage	V _{IH}			10	7.0	_	7.0	5.50	_	7.0	_	V
					15	11.0	_	11.0	8.25	_	11.0	_	
					5	_	1.5	_	2.25	1.5	_	1.5	
Control in voltage	nput low	V _{IL}	_	$ I_{IS} = 10 \mu A$	10	_	3.0	_	4.5	3.0	_	3.0	V
					15	_	4.0	_	6.75	4.0	_	4.0	
		R _{ON}	l	$0 \le V_{IS} \le V_{DD}$ $R_L = 10 \text{ k}\Omega$	5	_	800	_	290	950	_	1200	
On-state resistanc	е				10	_	210	_	120	250	_	300	Ω
					15	_	140	_	85	160		200	
∆On-state					5	_	_	_	10	_	_	_	
	resistance (between any 2		_	_	10	_	_	_	6	_	_	_	Ω
switches))				15	_	_	_	4	_	_	_	
	Input/output		_	V _{IN} = 18 V, V _{OUT} = 0 V	18	_	±100	_	±0.1	±100	_	±1000	nA
leakage current		loff		V _{IN} = 0 V, V _{OUT} = 18 V	18	_	±100		±0.1	±100		±1000	TIPA
					5	_	0.25	_	0.001	0.25		7.5	
Quiescent device current		I _{DD} –	_	$ V_{IN} = V_{DD},$ V_{SS}^*	10	_	0.5	_	0.001	0.5	_	15	μΑ
					15		1.0		0.002	1.0		30	
Input	H level	l _{IH}	_	V _{IH} = 18 V	18	_	0.1	_	10 ⁻⁵	0.1	_	1.0	^
current	L level	I _{IL}	_	V _{IL} = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ

Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Characteristics	Symbol	Test	Test Condition			Min	Тур.	Max	Unit
Characteristics	Cymbol	Circuit	rest containen	V _{SS} (V)	V _{DD} (V)	141111	Typ.	Wax	Offic
				0	5	_	15	40	
Phase difference between input to output	φI-O	_	C _L = 50 pF	0	10	_	8	20	ns
F				0	15	_	5	15	
	+		B. 110	0	5	_	55	120	
Propagation delay time (CONTROL-OUT)	t _{pZL}	_	$R_L = 1 \text{ k}\Omega$ $C_1 = 50 \text{ pF}$	0	10	_	25	40	ns
,	t _{pZH}		CL = 50 pr	0	15	_	20	30	
	+ . -		R _L = 1 kΩ	0	5	_	45	80	
Propagation delay time (CONTROL-OUT)	t _{pLZ}	_	$C_L = 50 \text{ pF}$	0	10	_	30	70	ns
(001111102 001)	t _{pHZ}		CL = 50 pF	0	15	_	25	60	
	f _{max} (C)	_	$R_L = 1 \text{ k}\Omega$	0	5	_	10	_	
Max control input repetition Rate			$C_L = 50 \text{ pF}$	0	10	_	12	_	MHz
			CL = 50 pF	0	15	_	12	_	
-3dB cutoff frequency	f _{max} (I-O)	_	$R_L = 1 \text{ k}\Omega$	-5	5	_	30	_	MHz
-3db cutoff frequency			$C_L = 50 \text{ pF}$ (Note 1)						
Total harmonic distortion			$R_L = 10 \text{ k}\Omega$	-5	5	_	0.03		%
Total Harmonic distortion			f = 1 kHz (Note 2)	_5					70
-50dB feed through frequency	_	_	$R_L = 1 \text{ k}\Omega$ (Note 3)	-5	5	_	600	_	kHz
-50dB crosstalk frequency	_	_	$R_L = 1 \text{ k}\Omega$ (Note 4)	-5	5	_	1	_	MHz
Crosstalk (CONTROL-OUT)	_		R _{IN} = 1 kΩ	0	5	_	200	_	
		_	$R_{OUT} = 10 \text{ k}\Omega$	0	10	_	400	_	mV
			C _L = 15 pF	0	15	_	600	_	
Input capacitance	C _{IN}	_	Control input				5	7.5	pF
		_	Switch I/O				10	_	μ
Feed through capacitance	C _{IN-OUT}	_	_		_	0.5	_	pF	

Note 1: Since wave of $\pm 2.5 \text{ V}_{p-p}$ shall be used for VIS and the frequency of 20 log $_{10}$ $\frac{\text{V}_{OS}}{\text{V}_{IS}} = -3 \text{dB}$ shall be f_{max}.

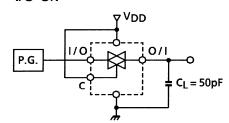
Note 2: V_{IS} shall be sine wave of $\pm 2.5 V_{p-p}$.

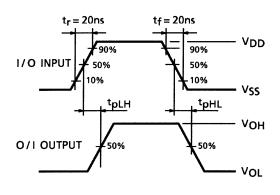
Note 2: VIS shall be sine wave of $\pm 2.5 \text{ V}_{p-p}$. Note 3: Sine wave of $\pm 2.5 \text{ V}_{p-p}$ shall be used for VIS and the frequency of 20 $\log_{10} \frac{\text{V}_{OUT}}{\text{V}_{IS}} = -50 \text{dB}$ shall be feed-through.

Note 4: Sine wave of $\pm 2.5 \text{ V}_{\text{p-p}}$ shall be used for VIS and the frequency of 20 log $_{10}$ $\frac{\text{V}_{\text{OUT}}}{\text{V}_{\text{IS}}} = -50 \text{dB}$ shall be crosstalk.

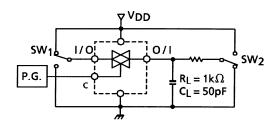
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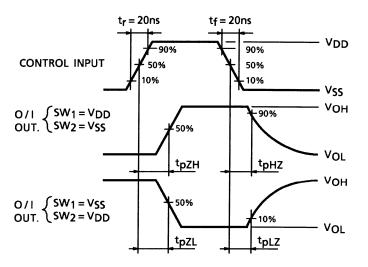
1. t_{pLH}, t_{pHL}



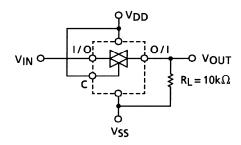


2. t_{pZL}, t_{pZH}, t_{pLZ}, t_{pHZ} Control-O/I



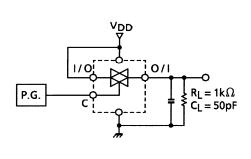


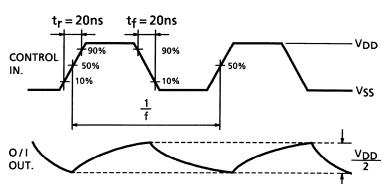
3. Ron



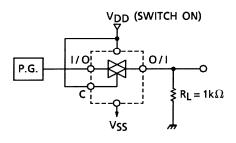
$$R_{ON} = 10 \times \frac{(V_{IN} - V_{OUT})}{V_{OUT}} (k\Omega)$$

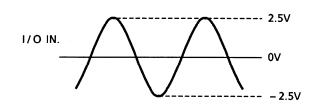
4. f_{max} (C)

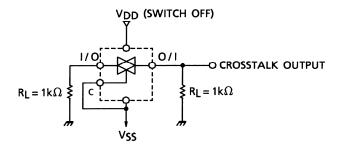




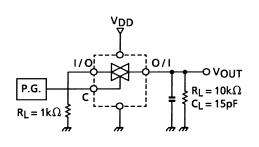
5. Crosstalk (switch I/O)

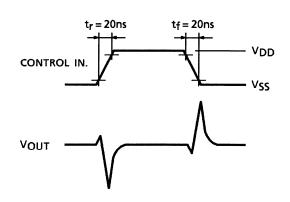




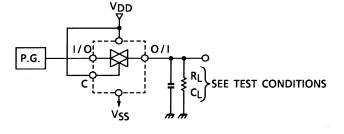


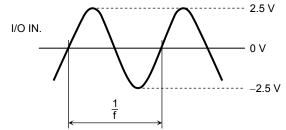
6. Crosstalk (control input)





7. Total Harmonic Distortion, f_{max} (I/O-O/I), Feedthrough (switch OFF)

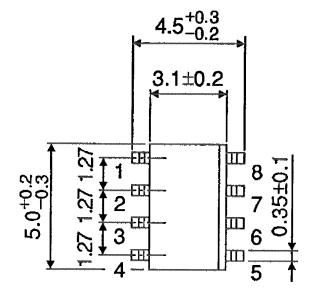


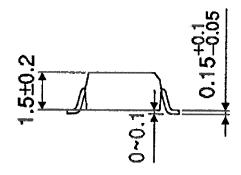


Package Dimensions

SOP8-P-1.27

Unit: mm

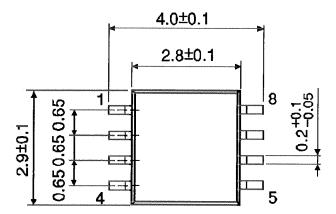


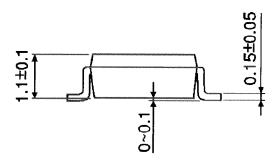


Weight: 0.05 g (typ.)

Package Dimensions

SSOP8-P-0.65 Unit: mm





Weight: 0.02 g (typ.)

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