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NDT454P P-Channel Enhancement Mode Field Effect Transistor

General Description

Features

Power SOT P-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, high cell density, DMOS technology. This very high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where fast switching, low in-line power loss, and resistance to transients are needed.

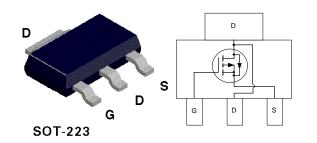
$\label{eq:constraint} \begin{array}{l} \bullet & -5.9 \text{A}, \ -30 \text{V}. \ \text{R}_{\text{DS}(\text{ON})} = 0.05 \Omega \ @ \ \text{V}_{\text{GS}} = -10 \text{V} \\ \text{R}_{\text{DS}(\text{ON})} = 0.07 \Omega \ @ \ \text{V}_{\text{GS}} = -6 \text{V} \\ \text{R}_{\text{DS}(\text{ON})} = 0.09 \Omega \ @ \ \text{V}_{\text{GS}} = -4.5 \text{V}. \end{array}$

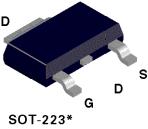
- High density cell design for extremely low R_{DS(ON)}.
- High power and current handling capability in a widely used surface mount package.

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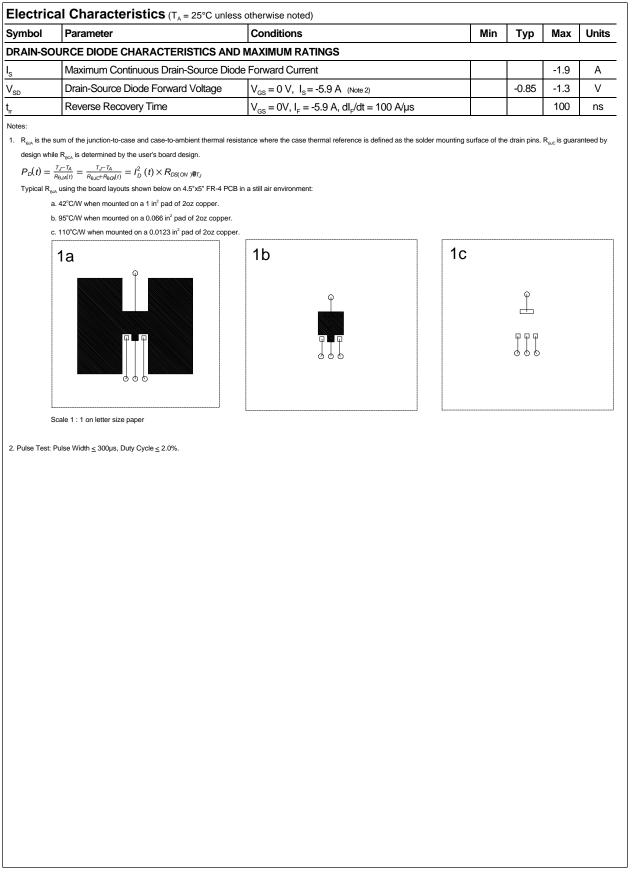
Absolute Maximum Ratings $T_A = 25^{\circ}C$ unless otherwise noted

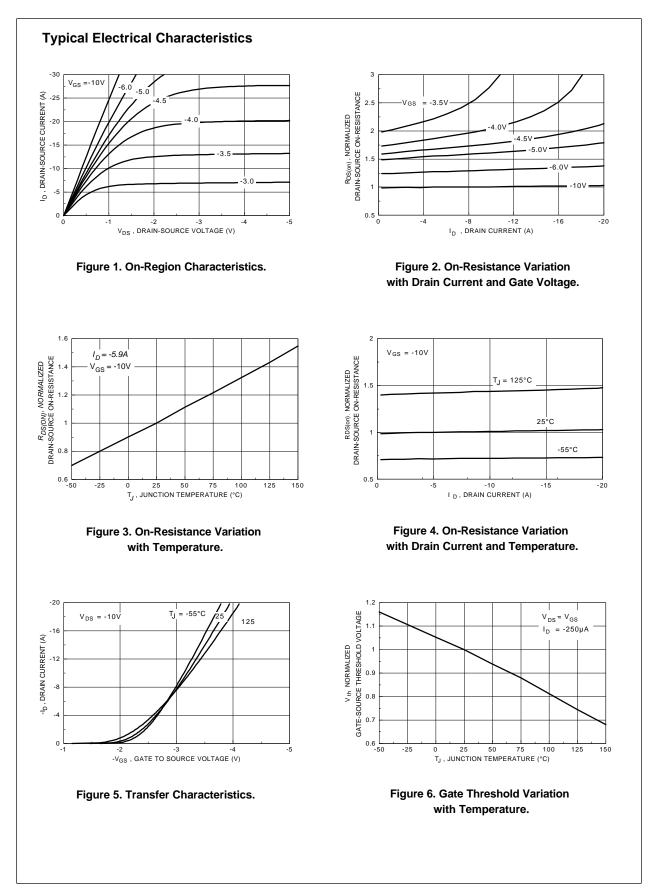
Parameter		NDT454P	Units
Drain-Source Voltage		-30	V
Gate-Source Voltage		<u>+2</u> 0	V
Drain Current - Continuous	(Note 1a)	±5.9	A
- Pulsed		±15	
Maximum Power Dissipation	(Note 1a)	3	W
	(Note 1b)	1.3	
	(Note 1c)	1.1	
Operating and Storage Temperature Range		-65 to 150	°C
L CHARACTERISTICS			
Thermal Resistance, Junction-to-Ambient	(Note 1a)	42	°C/W
Thermal Resistance, Junction-to-Case	(Note 1)	12	°C/W
· · · ·	Drain-Source Voltage Gate-Source Voltage Drain Current - Continuous - Pulsed Maximum Power Dissipation Operating and Storage Temperature Rang CHARACTERISTICS Thermal Resistance, Junction-to-Ambient	Drain-Source Voltage	Drain-Source Voltage -30 Gate-Source Voltage ±20 Drain Current - Continuous (Note 1a) - Pulsed ±15 Maximum Power Dissipation (Note 1a) (Note 1b) 1.3 (Note 1c) 1.1 Operating and Storage Temperature Range -65 to 150 CHARACTERISTICS Thermal Resistance, Junction-to-Ambient (Note 1a)

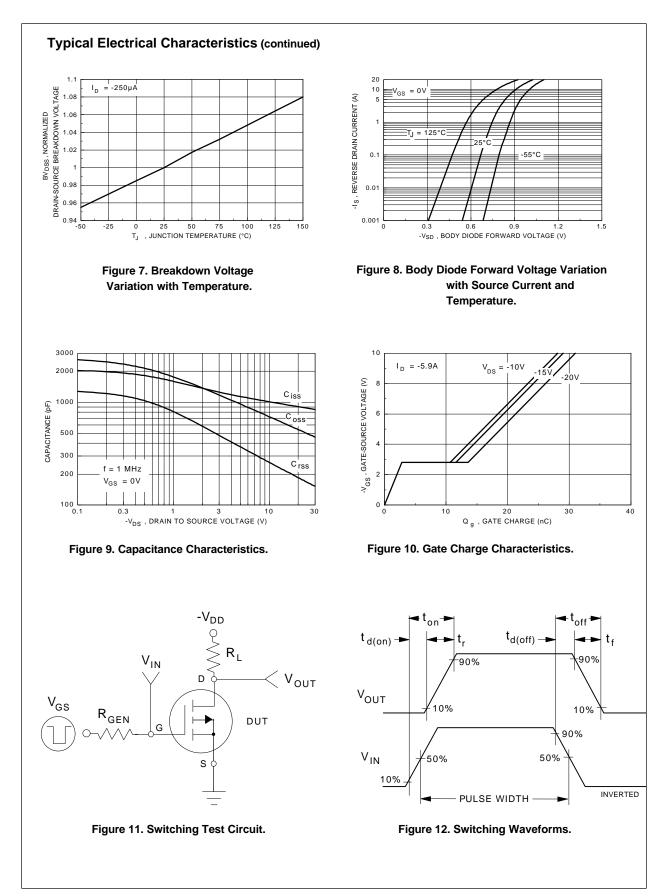
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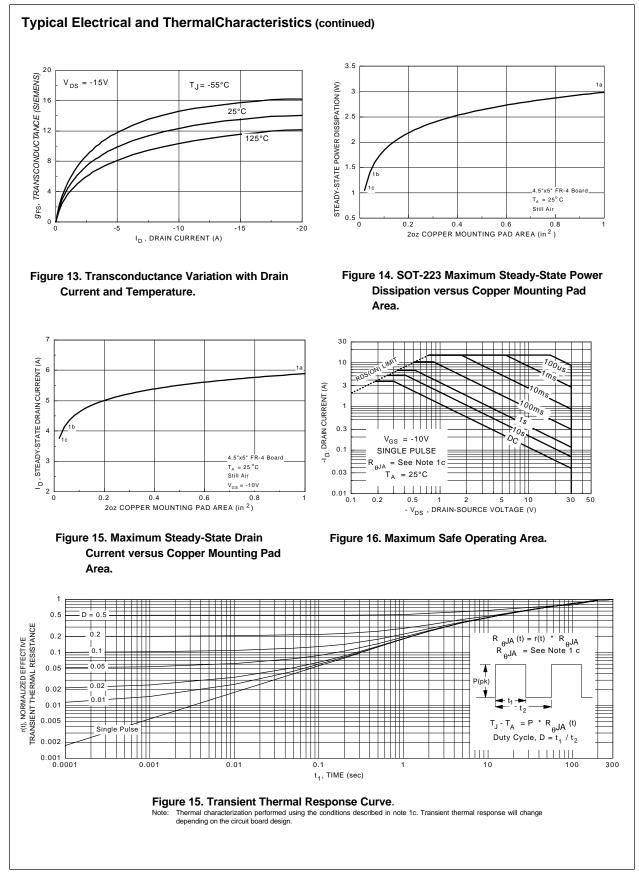
June 1996

Symbol	Parameter	Conditions		Min	Тур	Max	Units
OFF CHA	RACTERISTICS						
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$		-30			V
	Zero Gate Voltage Drain Current	$V_{DS} = -24 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$				-1	μA
		$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}$	$T_{J} = 70^{\circ}C$			-5	μA
I _{GSSF}	Gate - Body Leakage, Forward	$V_{GS} = 20 V, V_{DS} = 0 V$				100	nA
I _{GSSR}	Gate - Body Leakage, Reverse	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$				-100	nA
ON CHAR	ACTERISTICS (Note 2)						
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$		-1	-2.7		V
R _{DS(ON)} Static Dra	Static Drain-Source On-Resistance	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -5.9 \text{ A}$			0.038	0.05	Ω
		$V_{GS} = -6 \text{ V}, \text{ I}_{D} = -5.2 \text{ A}$			0.046	0.07	
		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -4.6 \text{ A}$			0.064	0.09	
I _{D(on)}	On-State Drain Current	$V_{GS} = -10 \text{ V}, V_{DS} = -5 \text{ V}$		-15			А
		$V_{GS} = -4.5, V_{DS} = -5V$		-5			
9 _{FS}	Forward Transconductance	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5.9 \text{ A}$			10		S
DYNAMIC	CHARACTERISTICS					-	
C _{iss}	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz			950		pF
C _{oss}	Output Capacitance				610		pF
C _{rss}	Reverse Transfer Capacitance				220		pF
SWITCHIN	IG CHARACTERISTICS (Note 2)						
t _{D(on)}	Tum - On Delay Time	$V_{\text{DD}} = -15 \text{ V}, \text{ I}_{\text{D}} = -1 \text{ A},$ $V_{\text{GEN}} = -10 \text{ V}, \text{ R}_{\text{GEN}} = 6 \Omega$			10	30	ns
t,	Turn - On Rise Time				18	60	ns
t _{D(off)}	Turn - Off Delay Time				80	120	ns
t _r	Turn - Off Fall Time				45	100	ns
Q _g	Total Gate Charge	$V_{DS} = -15 V,$ $I_D = -5.9 A, V_{GS} = -10 V$			29	40	nC
Q_{gs}	Gate-Source Charge				3		
Q_{gd}	Gate-Drain Charge				11		









NDT454P Rev. D2

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