



Product data sheet

1. Product profile

1.1 General description

Silicon Monolitic Microwave Integrated Circuit (MMIC) wideband amplifier with internal matching circuit in a 6-pin SOT363 plastic SMD package.

1.2 Features and benefits

- Internally matched to 50 Ω
- A gain of 20 dB at 250 MHz increasing to 20.6 dB at 2150 MHz
- Output power at 1 dB gain compression = -1 dBm
- Supply current = 10.5 mA at a supply voltage of 3.3 V
- Reverse isolation > 30 dB up to 2 GHz
- Good linearity with low second order and third order products
- Noise figure = 4 dB at 950 MHz
- Unconditionally stable (K > 1)

1.3 Applications

- LNB IF amplifiers
- General purpose low noise wideband amplifier for frequencies between DC and 2.2 GHz

2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	V _{CC}		
2, 5	GND2		
3	RF_OUT		6
4	GND1		
6	RF_IN		4 2, 5 /// /// sym052



Ordering information 3.

Table 2. Order	ing inform	ation				
Type number	Type number Package					
	Name	Description	Version			
BGA2800	-	plastic surface-mounted package; 6 leads	SOT363			

Marking 4.

Table 3. Marking	3	
Type number	Marking code	Description
BGA2800	*E7	* = - : made in Hong Kong
		* = p : made in Hong Kong
		* = W : made in China
		* = t : made in Malaysia

Limiting values 5.

Table 4. **Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage	RF input AC coupled	-0.5	+5.0	V
I _{CC}	supply current		-	55	mA
P _{tot}	total power dissipation	T _{sp} = 90 °C	-	200	mW
T _{stg}	storage temperature		-40	+125	°C
Tj	junction temperature		-	125	°C
P _{drive}	drive power		-	+10	dBm

Thermal characteristics 6.

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point	P_{tot} = 200 mW; T_{sp} = 90 °C	300	K/W

Characteristics 7.

Table 6.Characteristics

 $V_{CC} = 3.3 V; Z_S = Z_L = 50 \Omega; P_i = -40 dBm; T_{amb} = 25 °C; measured on demo board; unless otherwise specified.$

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		3.0	3.3	3.6	V
I _{CC}	supply current		8.8	10.5	12.1	mA

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _p	power gain	f = 250 MHz	19.4	19.9	20.5	dB
		f = 950 MHz	19.8	20.5	21.2	dB
		f = 2150 MHz	18.7	20.2	21.7	dB
RL _{in}	input return loss	f = 250 MHz	19	21	23	dB
		f = 950 MHz	19	21	23	dB
		f = 2150 MHz	10	15	22	dB
RL _{out}	output return loss	f = 250 MHz	15	19	24	dB
		f = 950 MHz	16	17	18	dB
		f = 2150 MHz	15	17	20	dB
ISL	isolation	f = 250 MHz	46	67	87	dB
		f = 950 MHz	44	46	47	dB
		f = 2150 MHz	35	37	40	dB
NF	noise figure	f = 250 MHz	3.2	3.7	4.2	dB
		f = 950 MHz	3.1	3.6	4.0	dB
		f = 2150 MHz	3.3	3.7	4.2	dB
B _{-3dB}	-3 dB bandwidth	3 dB below gain at 1 GHz	2.9	3.2	3.5	GHz
K	Rollett stability factor	f = 250 MHz	49	105	160	
		f = 950 MHz	8	9	10	
		f = 2150 MHz	2.8	3.4	4.0	
P _{L(sat)}	saturated output power	f = 250 MHz	1	1	2	dBm
		f = 950 MHz	0	1	3	dBm
		f = 2150 MHz	-2	0	+1	dBm
P _{L(1dB)}	output power at 1 dB gain compression	f = 250 MHz	-2	-1	0	dBm
		f = 950 MHz	-2	-1	0	dBm
		f = 2150 MHz	-3	-2	-1	dBm
IP3 _I	input third-order intercept point	$P_{drive} = -36 \text{ dBm}$ (for each tone)				
		f ₁ = 250 MHz; f ₂ = 251 MHz	-11	-9	-7	dBm
		f ₁ = 950 MHz; f ₂ = 951 MHz	-12	-10	-7	dBm
		f ₁ = 2150 MHz; f ₂ = 2151 MHz	-15	-12	-9	dBm
IP3 ₀	output third-order intercept point	$P_{drive} = -36 \text{ dBm}$ (for each tone)				
		f ₁ = 250 MHz; f ₂ = 251 MHz	9	11	13	dBm
		f ₁ = 950 MHz; f ₂ = 951 MHz	9	11	13	dBm
		f ₁ = 2150 MHz; f ₂ = 2151 MHz	5	8	11	dBm
P _{L(2H)}	second harmonic output power	P _{drive} = -34 dBm				
. ,		f _{1H} = 250 MHz; f _{2H} = 500 MHz	-62	-60	-58	dBm
		f _{1H} = 950 MHz; f _{2H} = 1900 MHz	-51	-49	-48	dBm
۵IM2	second-order intermodulation distance	$P_{drive} = -36 \text{ dBm}$ (for each tone)				-
		f ₁ = 250 MHz; f ₂ = 251 MHz	42	53	64	dBc
		f ₁ = 950 MHz; f ₂ = 951 MHz	44	55	67	dBc

Table 6.Characteristics ...continued $V_{co} = 3.3 V: Z_S = Z_I = 50 \Omega: P_i = -40 \text{ dBm}; T_{an}$

 $r = 25 \circ C$: measured on demo hoard: unless otherwise specified

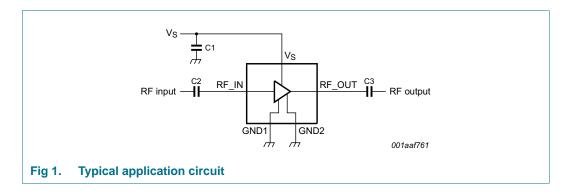
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8. Application information

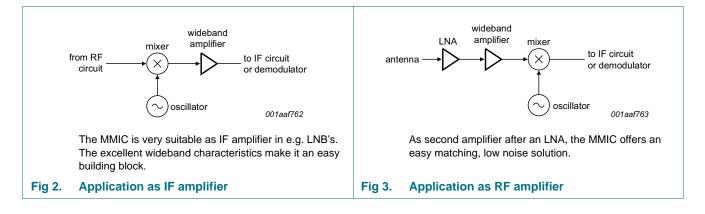
<u>Figure 1</u> shows a typical application circuit for the BGA2800 MMIC. The device is internally matched to 50 Ω , and therefore does not need any external matching. The value of the input and output DC blocking capacitors C2 and C3 should not be more than 100 pF for applications above 100 MHz. However, when the device is operated below 100 MHz, the capacitor value should be increased.

The 22 nF supply decoupling capacitor C1 should be located as close as possible to the MMIC.

The PCB top ground plane, connected to pins 2, 4 and 5 must be as close as possible to the MMIC, preferably also below the MMIC. When using via holes, use multiple via holes as close as possible to the MMIC.



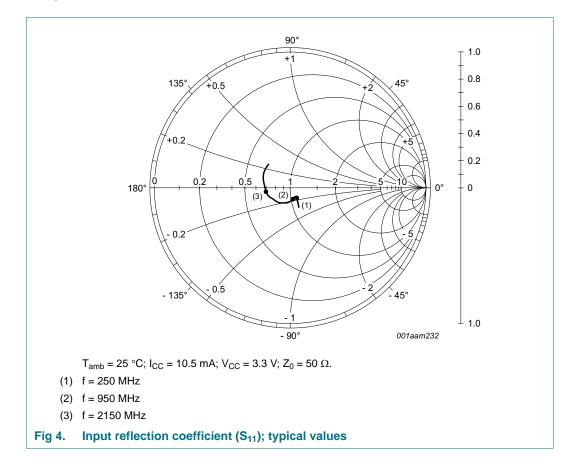
8.1 Application examples



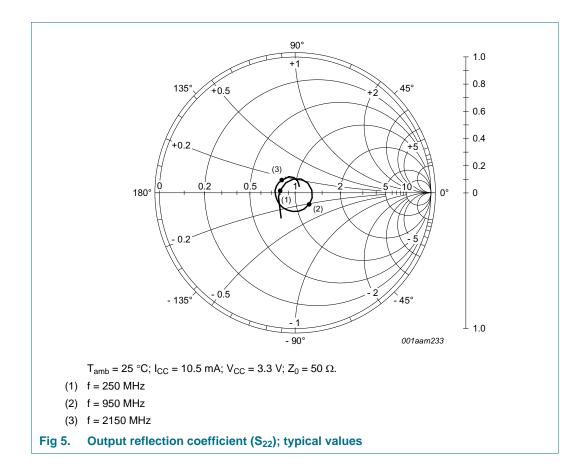
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BGA2800 MMIC wideband amplifier

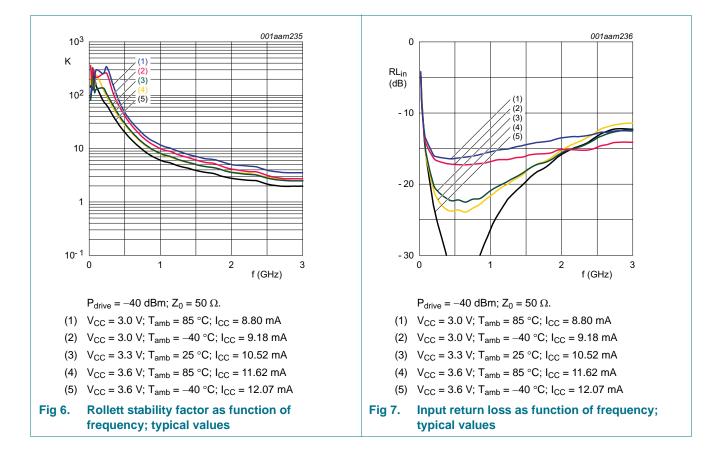
8.2 Graphs



MMIC wideband amplifier

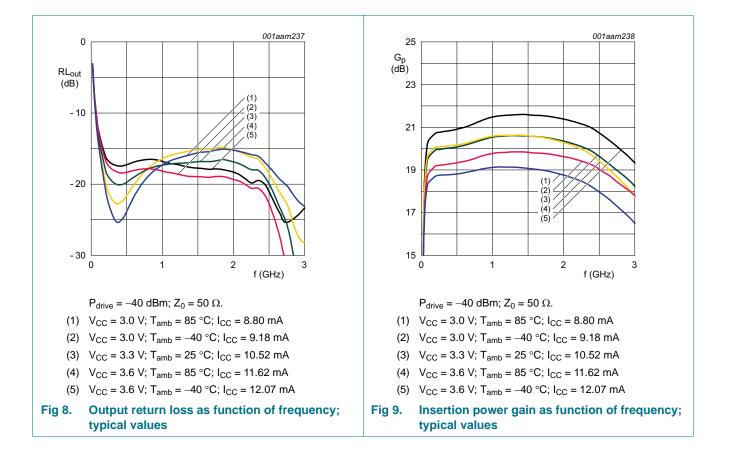


MMIC wideband amplifier



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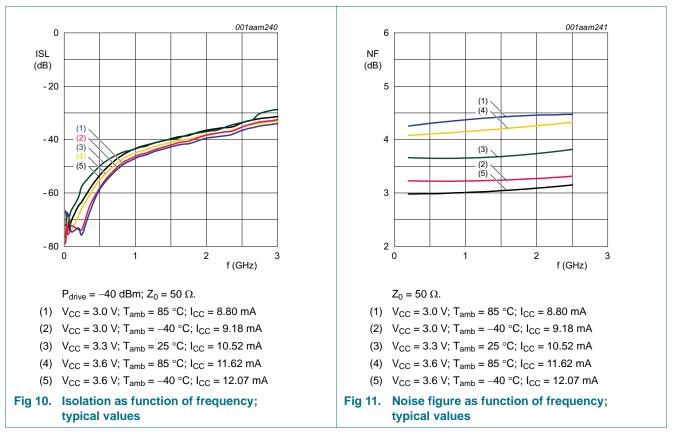
MMIC wideband amplifier



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BGA2800

MMIC wideband amplifier



8.3 Tables

Table 7.Supply current over temperature and supply voltagesTypical values.

Symbol	Parameter	Conditions	T _{amb} (°0	T _{amb} (°C)			
			-40	+25	+85		
I _{CC}	supply current	$V_{CC} = 3.0 V$	9.18	8.96	8.80	mA	
		$V_{CC} = 3.3 V$	11.18	10.52	10.83	mA	
		V _{CC} = 3.6 V	12.07	12.28	11.62	mA	

Table 8.Second harmonic output power over temperature and supply voltagesTypical values.

Symbol	Parameter	Conditions	Tamb	(°C)		Unit
			-40	+25	+85	
P _{L(2H)}	second harmonic output power	f = 250 MHz; P_{drive} = -34 dBm				
		$V_{CC} = 3.0 V$	-53	-58	-63	dBm
		$V_{CC} = 3.3 V$	-57	-60	-63	dBm
		$V_{CC} = 3.6 V$	-58	-61	-62	dBm
		f = 950 MHz; P_{drive} = -34 dBm				
		$V_{CC} = 3.0 V$	-46	-49	-51	dBm
		V _{CC} = 3.3 V	-47	-49	-51	dBm
		V _{CC} = 3.6 V	-47	-49	-51	dBm

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Symbol	Parameter	Conditions	T _{amb}	(°C)		Unit
			-40	+25	+85	
P _{i(1dB)}	input power at 1 dB gain compression	f = 250 MHz				
		$V_{CC} = 3.0 V$	-21	-21	-21	dBm
		V _{CC} = 3.3 V	-20	-20	-20	dBm
		V _{CC} = 3.6 V	-20	-20	-20	dBm
		f = 950 MHz				
		$V_{CC} = 3.0 V$	-21	-21	-21	dBm
		V _{CC} = 3.3 V	-20	-20	-20	dBm
		$V_{CC} = 3.6 V$	-20	-20	-20	dBm
		f = 2150 MHz				
		$V_{CC} = 3.0 V$	-21	-21	-22	dBm
		V _{CC} = 3.3 V	-21	-21	-22	dBm
		V _{CC} = 3.6 V	-20	-21	-22	dBm

Table 9. Input power at 1 dB gain compression over temperature and supply voltages Typical values. Image: Comparison of the second supply voltage of the se

Table 10. Output power at 1 dB gain compression over temperature and supply voltages *Typical values.*

Symbol	Parameter	Conditions	T _{amb}	(°C)		Unit
			-40	+25	+85	
P _{L(1dB)}	output power at 1 dB gain compression	f = 250 MHz				
		$V_{CC} = 3.0 V$	-3	-3	-3	dBm
		$V_{CC} = 3.3 V$	-1	-1	-1	dBm
		$V_{CC} = 3.6 V$	0	0	-1	dBm
		f = 950 MHz				
		$V_{CC} = 3.0 V$	-3	-3	-3	dBm
		$V_{CC} = 3.3 V$	-1	-1	-2	dBm
		$V_{CC} = 3.6 V$	0	0	-1	dBm
		f = 2150 MHz				
		$V_{CC} = 3.0 V$	-3	-3	-4	dBm
		V _{CC} = 3.3 V	-1	-2	-3	dBm
		V _{CC} = 3.6 V	0	-1	-3	dBm

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Symbol	Parameter	Conditions	T _{amb}	(°C)		Unit
			-40	+25	+85	
P _{L(sat)}	saturated output power	f = 250 MHz				
		$V_{CC} = 3.0 V$	-1	-1	-1	dBm
		V _{CC} = 3.3 V	1	1	1	dBm
		V _{CC} = 3.6 V	2	2	2	dBm
		f = 950 MHz				
		V _{CC} = 3.0 V	-1	-1	-1	dBm
		V _{CC} = 3.3 V	2	1	1	dBm
		V _{CC} = 3.6 V	3	2	2	dBm
		f = 2150 MHz				
		$V_{CC} = 3.0 V$	-1	-1	-3	dBm
		V _{CC} = 3.3 V	1	0	-2	dBm
		V _{CC} = 3.6 V	1	0	-1	dBm

Table 11.Saturated output power over temperature and supply voltagesTypical values.

Table 12. Second-order intermodulation distance over temperature and supply voltages Typical values. Values.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit
			-40	+25	+85	
∆IM2 second-order	second-order intermodulation distance					
		$V_{CC} = 3.0 V$	40	51	49	dBc
		$V_{CC} = 3.3 V$	52	53	46	dBc
		V _{CC} = 3.6 V	58	50	45	dBc
		f ₁ = 950 MHz; f ₂ = 951 MHz; P _{drive} = -36 dBm				
		V _{CC} = 3.0 V	38	47	52	dBc
		$V_{CC} = 3.3 V$	48	55	46	dBc
		V _{CC} = 3.6 V	55	51	44	dBc

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Symbol	Parameter	Conditions	T _{amb}	T _{amb} (°C)		
			-40	+25	+85	
IP3 ₀	output third-order intercept point	$f_1 = 250 \text{ MHz};$ $f_2 = 251 \text{ MHz};$ $P_{drive} = -36 \text{ dBm}$				
		V _{CC} = 3.0 V	9	9	8	dBm
		V _{CC} = 3.3 V	12	11	10	dBm
		V _{CC} = 3.6 V	14	12	11	dBm
		f ₁ = 950 MHz; f ₂ = 951 MHz; P _{drive} = -36 dBm				
		V _{CC} = 3.0 V	10	9	8	dBm
		V _{CC} = 3.3 V	12	11	10	dBm
		V _{CC} = 3.6 V	13	12	11	dBm
		$f_1 = 2150 \text{ MHz};$ $f_2 = 2151 \text{ MHz};$ $P_{drive} = -36 \text{ dBm}$				
		V _{CC} = 3.0 V	8	7	5	dBm
		V _{CC} = 3.3 V	10	8	6	dBm
		V _{CC} = 3.6 V	11	9	7	dBm

Table 13. Output third-order intercept point over temperature and supply voltages Typical values. Values.

Table 14. -3 dB bandwidth over temperature and supply voltages Typical values.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit
			-40	+25	+85	
B _{-3dB}	-3 dB bandwidth	$V_{CC} = 3.0 V$	3.287	3.198	3.082	GHz
		$V_{CC} = 3.3 V$	3.240	3.162	3.055	GHz
		$V_{CC} = 3.6 V$	3.226	3.144	3.034	GHz

BGA2800 MMIC wideband amplifier

9. Test information

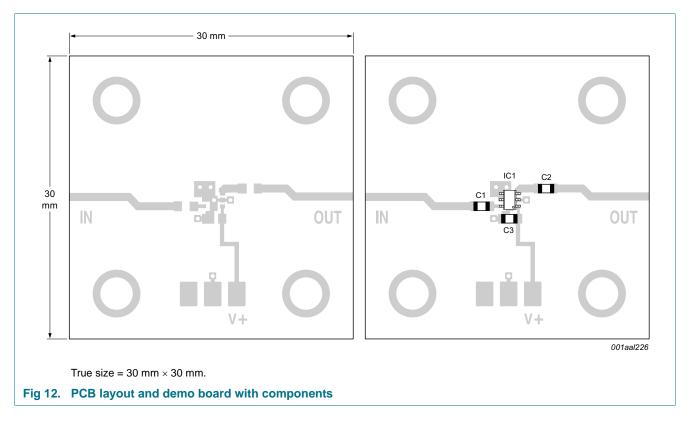


Table 15. List of components used for the typical application

Component	Description	Value	Dimensions
C1, C2	multilayer ceramic chip capacitor	100 pF	0603
C3	multilayer ceramic chip capacitor	22 nF	0603
IC1	BGA2800 MMIC		SOT363

BGA2800 MMIC wideband amplifier

10. Package outline

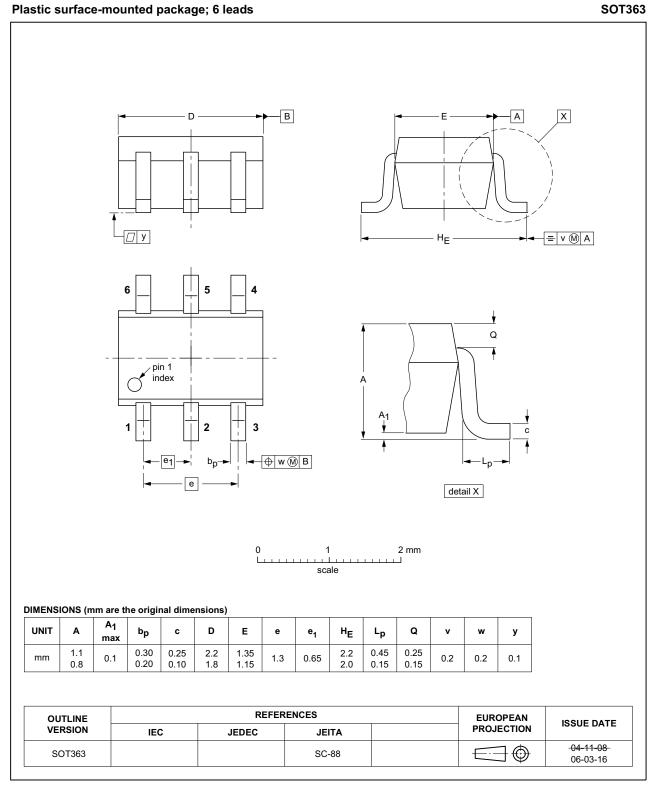


Fig 13. Package outline SOT363

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MMIC wideband amplifier

11. Abbreviations

Table 16. Abbreviations				
Acronym	Description			
IF	Intermediate Frequency			
LNA	Low-Noise Amplifier			
LNB	Low-Noise Block converter			
PCB	Printed-Circuit Board			
SMD	Surface Mounted Device			

12. Revision history

Table 17.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
BGA2800 v.5	20150713	Product data sheet	-	BGA2800 v.4	
Modifications:	of NXP Semi	rmat of this data sheet has been redesigned to comply with the new identity guidelines P Semiconductors. texts have been adapted to the new company name where appropriate.			
BGA2800 v.4	20141208	Product data sheet	-	BGA2800 v.3	
BGA2800 v.3	20121010	Product data sheet	-	BGA2800 v.2	
BGA2800 v.2	20101018	Product data sheet	-	BGA2800 v.1	
BGA2800 v.1	20100803	Product data sheet	-	-	

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

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