

**Product data sheet** 

### 1. General description

Planar passivated four quadrant triac in a SOT82 (SIP3) plastic package intended for use in general purpose bidirectional switching and phase control applications.

#### 2. Features and benefits

- · High blocking voltage capability
- · Planar passivated for voltage ruggedness and reliability
- Less sensitive gate for improved noise immunity
- Triggering in all four quadrants
- Compact package

### **3. Applications**

- General purpose low power motor control
- Home appliances
- Industrial process control

### 4. Quick reference data

Symbol	Parameter	Conditions Values					
Absolute	maximum rating						
$V_{\text{DRM}}$	repetitive peak off-state voltage			8	300		V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 107 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	4			A	
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 20 ms; <u>Fig. 4</u> ; <u>Fig. 5</u>		25			A
Symbol	Parameter	Conditions	Min Typ Max				Unit
Static ch	aracteristics						
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>		-	5	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>		-	8	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>		-	11	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G+; T <sub>i</sub> = 25 °C; <u>Fig. 7</u>		-	30	70	mA

## **5. Pinning information**

Table 2.	Pinning infor	mation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	·	
2	T2	main terminal 2		N
3	G	gate		
mb	T2	mounting base; main terminal 2	·~/	sym051

# 6. Ordering information

Table 3. Ordering information								
Type number	Package							
	Name	Description	Version					
BT134-800	SIP3	plastic single-ended package; 3-leads (in-line)	SOT82					

## 7. Marking

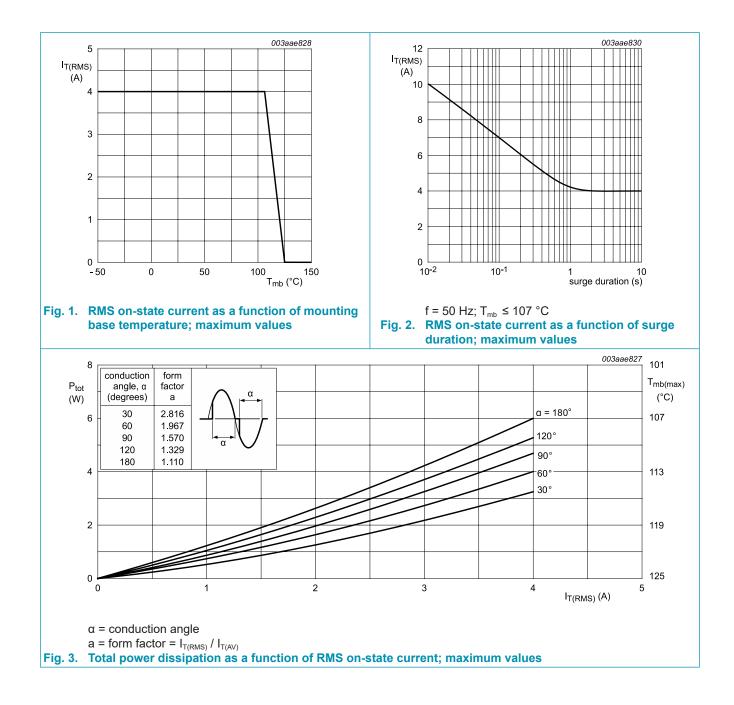
Table 4. Marking codes									
	Type number	Marking codes							
	BT134-800	BT134-800							

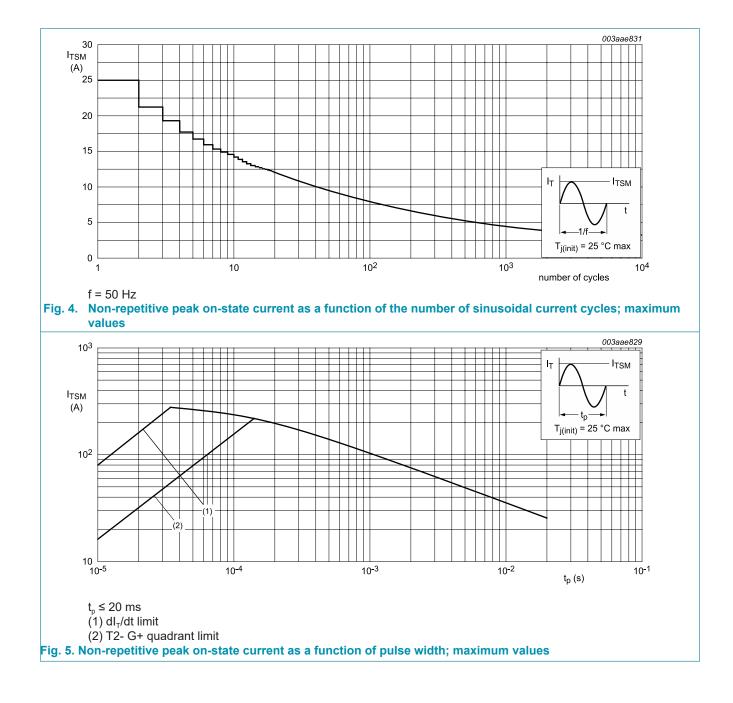
# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

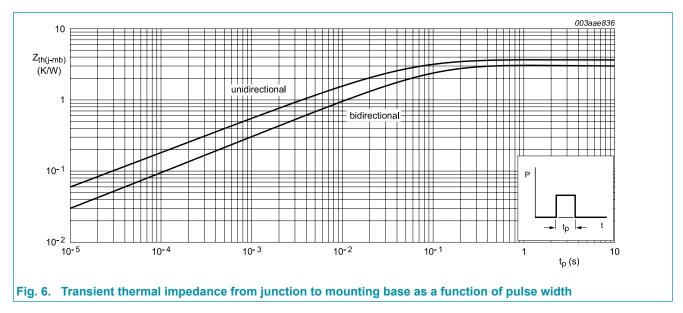
Symbol	Parameter	Conditions	Values	Unit
$V_{\text{DRM}}$	repetitive peak off-state voltage		800	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; $T_{mb} \le 107 \text{ °C}$ ; Fig 1; Fig 2; Fig 3	4	A
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 20 ms; Fig 4; Fig 5	25	A
		full sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 16.7 ms	27	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>P</sub> = 10 ms; SIN	3.1	A <sup>2</sup> s
dI <sub>T</sub> /dt	rate of rise of on-state	I <sub>G</sub> = 70 mA; T2+ G+	50	A/µs
	current	I <sub>G</sub> = 70 mA; T2+ G-	50	A/µs
		I <sub>G</sub> = 70 mA; T2- G-	50	A/µs
		I <sub>G</sub> = 140 mA; T2- G+	10	A/µs
I <sub>GM</sub>	peak gate current		2	А
P <sub>GM</sub>	peak gate power		5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	0.5	W
T <sub>stg</sub>	storage temperature		-40 to 150	°C
T <sub>j</sub>	junction temperature		125	°C





## 9. Thermal characteristics

Table 6. Th	ermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance	half cycle; <u>Fig 6</u>	-	-	3.7	K/W
	from junction to mounting base	full cycle; <u>Fig 6</u>	-	-	3	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	100	-	K/W

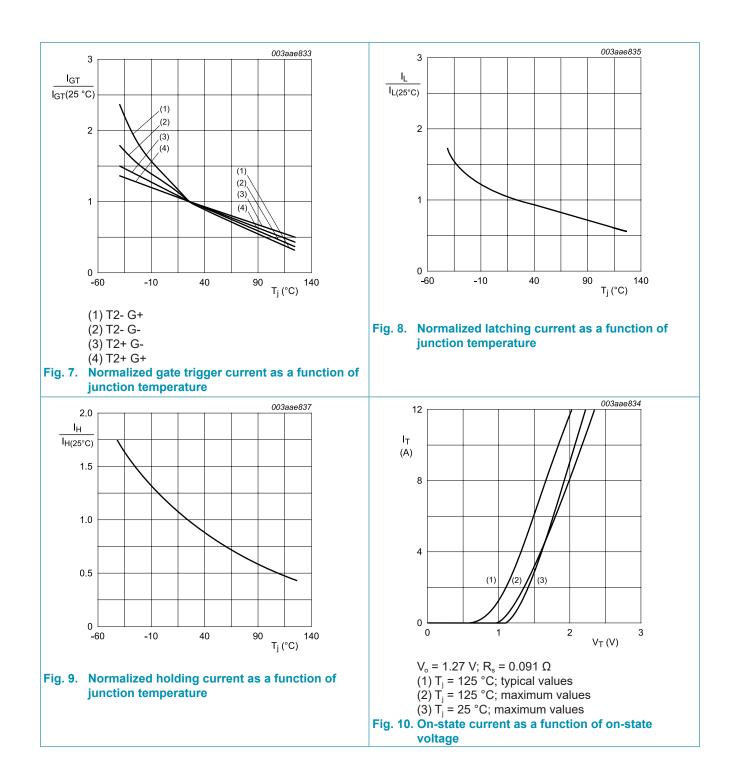


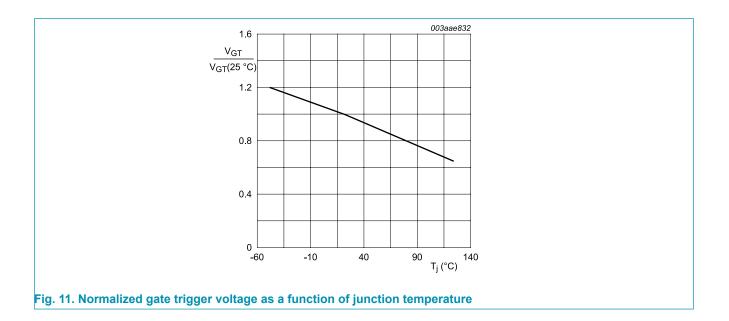
BT134-800 4Q Triac

## **10. Characteristics**

Table 7. Cl	haracteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
I <sub>GT</sub> g	gate trigger current	$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2+ G+};$ $T_{j} = 25 \text{ °C}; \text{ Fig. 7}$	-	5	35	mA
		$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2+ G-};$ T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	8	35	mA
		$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2- G-};$ $T_{j} = 25 \text{ °C}; \text{ Fig. 7}$	-	11	35	mA
		$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2- G+};$ $T_{j} = 25 \text{ °C}; \text{ Fig. 7}$	-	30	70	mA
I <sub>L</sub>	latching current	$V_{D}$ = 12 V; I <sub>G</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	7	20	mA
		$V_{D}$ = 12 V; I <sub>G</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	16	30	mA
		$V_{D}$ = 12 V; I <sub>G</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	5	20	mA
		$V_{D}$ = 12 V; I <sub>G</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 °C; Fig. 8	-	7	30	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	5	15	mA
V <sub>T</sub>	on-state voltage	$I_{T} = 5 \text{ A}; T_{j} = 25 \text{ °C}; Fig. 10$	-	1.4	1.7	V
$V_{\text{GT}}$	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.7	1	V
		$V_{D} = 400 \text{ V}; \text{ I}_{T} = 0.1 \text{ A}; \text{ T}_{j} = 125 \text{ °C};$ Fig. 11	0.25	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 800 V; T <sub>j</sub> = 125 °C	-	0.1	0.5	mA
Dynamic	characteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 536 V; T <sub>j</sub> = 125 °C; (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; gate open circuit	250	-	V/µs	
dV <sub>com</sub> /dt	rate of change of commutating voltage	$V_D = 400 \text{ V}; T_j = 95 \text{ °C}; dI_{com}/dt = 1.8 \text{ A}/ms; I_T = 4 \text{ A}; gate open circuit$	-	50	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$V_{\rm D}$ = 800 V; I <sub>TM</sub> = 6 A; I <sub>G</sub> = 0.1 A; dI <sub>G</sub> /dt = 5 A/µs	-	2	-	μs

**BT134-800** 4Q Triac





# 11. Package outline

astic	singl	le-en	ded p	acka	ge; 3	lead	s (in-	line)							SO <sup>-</sup>
					<b>_</b>										
				-e1-	" _ _ <b>▶</b> 1				0	2.5 	5 mn	n			
		S (mm b	are the c	origin D	al dime E	ensions e	s) e <sub>1</sub>	L	L1 <sup>(1)</sup>	Р	Q	q	w		
mm	2.8	0.88	0.58	11.1	7.8	2.29	<b>-1</b> 4.58	16.5	max.	3.1	1.5	3.9	0.254		
Note	2.3	0.65	0.47	10.5	7.2			15.3		2.5	0.9	3.5			
1. Termi	inal dir	nensio	ns withi	n this z	one are	e uncon					termina	l irregu	larities.		
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	SOT82						52020	-		_170					97-06-11
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Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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4Q Triac

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Телефон: +7 812 627 14 35

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