

# NCV8440, NCV8440A

## Protected Power MOSFET

2.6 A, 52 V, N-Channel, Logic Level,  
Clamped MOSFET w/ ESD Protection

### Features

- Diode Clamp Between Gate and Source
- ESD Protection – Human Body Model 5000 V
- Active Over-Voltage Gate to Drain Clamp
- Scalable to Lower or Higher  $R_{DS(on)}$
- Internal Series Gate Resistance
- These are Pb-Free Devices

### Benefits

- High Energy Capability for Inductive Loads
- Low Switching Noise Generation

### Applications

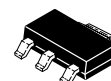
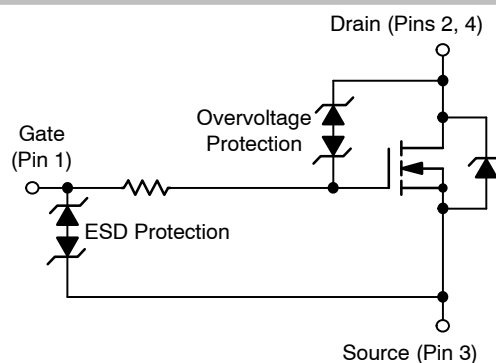
- Automotive and Industrial Markets:  
Solenoid Drivers, Lamp Drivers, Small Motor Drivers
- NCV Prefix for Automotive and Other Applications Requiring Site and Control Changes



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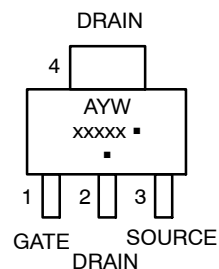
| $V_{DSS}$<br>(Clamped) | $R_{DS(on)}$ TYP     | $I_D$ MAX |
|------------------------|----------------------|-----------|
| 52 V                   | 95 m $\Omega$ @ 10 V | 2.6 A     |



SOT-223  
CASE 318E  
STYLE 3

1 = Gate  
2 = Drain  
3 = Source

### MARKING DIAGRAM



A = Assembly Location  
Y = Year  
W = Work Week  
xxxxx = V8440 or 8440A  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 8 of this data sheet.

# NCV8440, NCV8440A

## MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

| Rating                                                                                                                                                                            | Symbol                             | Value          | Unit               |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------|----------------|--------------------|
| Drain-to-Source Voltage Internally Clamped                                                                                                                                        | $V_{DSS}$                          | 52–59          | V                  |
| Gate-to-Source Voltage – Continuous                                                                                                                                               | $V_{GS}$                           | $\pm 15$       | V                  |
| Drain Current<br>– Continuous @ $T_A = 25^\circ\text{C}$<br>– Single Pulse ( $t_p = 10\ \mu\text{s}$ ) (Note 1)                                                                   | $I_D$<br>$I_{DM}$                  | 2.6<br>10      | A                  |
| Total Power Dissipation @ $T_A = 25^\circ\text{C}$ (Note 1)                                                                                                                       | $P_D$                              | 1.69           | W                  |
| Operating and Storage Temperature Range                                                                                                                                           | $T_J, T_{stg}$                     | $-55$ to $150$ | $^\circ\text{C}$   |
| Single Pulse Drain-to-Source Avalanche Energy<br>( $V_{DD} = 50\ \text{V}$ , $I_{D(pk)} = 1.17\ \text{A}$ , $V_{GS} = 10\ \text{V}$ , $L = 160\ \text{mH}$ , $R_G = 25\ \Omega$ ) | $E_{AS}$                           | 110            | mJ                 |
| Load Dump Voltage ( $V_{GS} = 0$ and $10\ \text{V}$ , $R_I = 2.0\ \Omega$ , $R_L = 9.0\ \Omega$ , $t_d = 400\ \text{ms}$ )                                                        | $V_{LD}$                           | 60             | V                  |
| Thermal Resistance,<br>Junction-to-Ambient (Note 1)<br>Junction-to-Ambient (Note 2)                                                                                               | $R_{\theta JA}$<br>$R_{\theta JA}$ | 74<br>169      | $^\circ\text{C/W}$ |
| Maximum Lead Temperature for Soldering<br>Purposes, 1/8" from Case for 10 Seconds                                                                                                 | $T_L$                              | 260            | $^\circ\text{C}$   |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. When surface mounted to a FR4 board using 1" pad size, (Cu area 1.127 in<sup>2</sup>).
2. When surface mounted to a FR4 board using minimum recommended pad size, (Cu area 0.412 in<sup>2</sup>).

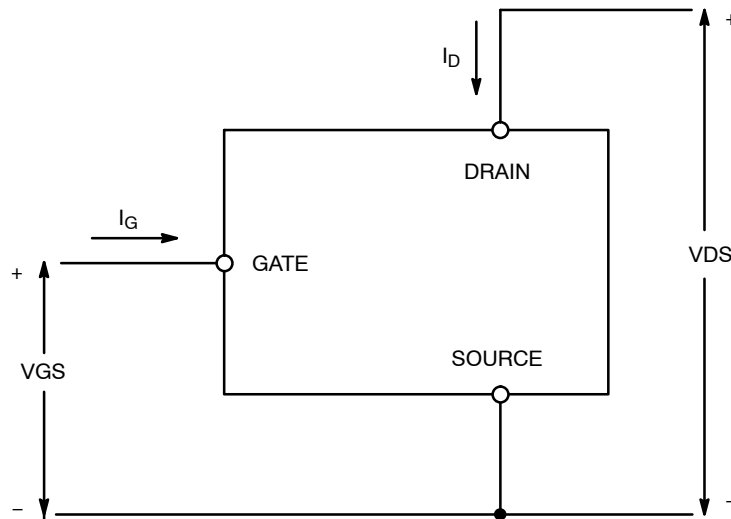


Figure 1. Voltage and Current Convention

# NCV8440, NCV8440A

## MOSFET ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)

| Characteristic | Symbol | Min | Typ | Max | Unit |
|----------------|--------|-----|-----|-----|------|
|----------------|--------|-----|-----|-----|------|

### OFF CHARACTERISTICS

|                                                                                                                                                                                                                                                           |                      |            |                  |            |                 |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------|------------|------------------|------------|-----------------|
| Drain-to-Source Breakdown Voltage (Note 3)<br>(V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1.0 mA, T <sub>J</sub> = 25°C)<br>(V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1.0 mA, T <sub>J</sub> = -40°C to 125°C) (Note 4)<br>Temperature Coefficient (Negative) | V <sub>(BR)DSS</sub> | 52<br>50.8 | 55<br>54<br>-9.3 | 59<br>59.5 | V<br>V<br>mV/°C |
| Zero Gate Voltage Drain Current<br>(V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V)<br>(V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125°C) (Note 4)                                                                                    | I <sub>DSS</sub>     |            |                  | 10<br>25   | μA              |
| Gate-Body Leakage Current<br>(V <sub>GS</sub> = ±8 V, V <sub>DS</sub> = 0 V)<br>(V <sub>GS</sub> = ±14 V, V <sub>DS</sub> = 0 V)                                                                                                                          | I <sub>GSS</sub>     |            | ±35              | ±10        | μA              |

### ON CHARACTERISTICS (Note 3)

|                                                                                                                                                                                                             |                     |     |                  |                   |            |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|-----|------------------|-------------------|------------|
| Gate Threshold Voltage (Note 3)<br>(V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 100 μA)<br>Threshold Temperature Coefficient (Negative)                                                            | V <sub>GS(th)</sub> | 1.1 | 1.5<br>-4.1      | 1.9               | V<br>mV/°C |
| Static Drain-to-Source On-Resistance (Note 3)<br>(V <sub>GS</sub> = 3.5 V, I <sub>D</sub> = 0.6 A)<br>(V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 1.5 A)<br>(V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.6 A) | R <sub>DS(on)</sub> |     | 135<br>150<br>95 | 180<br>160<br>110 | mΩ         |
| Forward Transconductance (Note 3) (V <sub>DS</sub> = 15 V, I <sub>D</sub> = 2.6 A)                                                                                                                          | g <sub>FS</sub>     |     | 3.8              |                   | Mhos       |

### DYNAMIC CHARACTERISTICS

|                      |                                                              |                  |  |     |  |    |
|----------------------|--------------------------------------------------------------|------------------|--|-----|--|----|
| Input Capacitance    | V <sub>DS</sub> = 35 V, V <sub>GS</sub> = 0 V,<br>f = 10 kHz | C <sub>iss</sub> |  | 155 |  | pF |
| Output Capacitance   |                                                              | C <sub>oss</sub> |  | 60  |  |    |
| Transfer Capacitance |                                                              | C <sub>rss</sub> |  | 25  |  |    |
| Input Capacitance    | V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,<br>f = 10 kHz | C <sub>iss</sub> |  | 170 |  | pF |
| Output Capacitance   |                                                              | C <sub>oss</sub> |  | 70  |  |    |
| Transfer Capacitance |                                                              | C <sub>rss</sub> |  | 30  |  |    |

- Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
- Not subject to production testing.
- Switching characteristics are independent of operating junction temperatures.

# NCV8440, NCV8440A

## MOSFET ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)

| Characteristic                            | Symbol                                                                                     | Min          | Typ  | Max | Unit |
|-------------------------------------------|--------------------------------------------------------------------------------------------|--------------|------|-----|------|
| <b>SWITCHING CHARACTERISTICS</b> (Note 5) |                                                                                            |              |      |     |      |
| Turn-On Delay Time                        | $V_{GS} = 4.5\text{ V}, V_{DD} = 40\text{ V},$<br>$I_D = 2.6\text{ A}, R_D = 15.4\ \Omega$ | $t_{d(on)}$  | 375  |     | ns   |
| Rise Time                                 |                                                                                            | $t_r$        | 1525 |     |      |
| Turn-Off Delay Time                       |                                                                                            | $t_{d(off)}$ | 1530 |     |      |
| Fall Time                                 |                                                                                            | $t_f$        | 1160 |     |      |
| Turn-On Delay Time                        | $V_{GS} = 4.5\text{ V}, V_{DD} = 40\text{ V},$<br>$I_D = 1.0\text{ A}, R_D = 40\ \Omega$   | $t_{d(on)}$  | 325  |     | ns   |
| Rise Time                                 |                                                                                            | $t_r$        | 1275 |     |      |
| Turn-Off Delay Time                       |                                                                                            | $t_{d(off)}$ | 1860 |     |      |
| Fall Time                                 |                                                                                            | $t_f$        | 1150 |     |      |
| Turn-On Delay Time                        | $V_{GS} = 10\text{ V}, V_{DD} = 15\text{ V},$<br>$I_D = 2.6\text{ A}, R_D = 5.8\ \Omega$   | $t_{d(on)}$  | 190  |     | ns   |
| Rise Time                                 |                                                                                            | $t_r$        | 710  |     |      |
| Turn-Off Delay Time                       |                                                                                            | $t_{d(off)}$ | 2220 |     |      |
| Fall Time                                 |                                                                                            | $t_f$        | 1180 |     |      |
| Gate Charge                               | $V_{GS} = 4.5\text{ V}, V_{DS} = 40\text{ V},$<br>$I_D = 2.6\text{ A}$ (Note 3)            | $Q_T$        | 4.5  |     | nC   |
|                                           |                                                                                            | $Q_1$        | 0.9  |     |      |
|                                           |                                                                                            | $Q_2$        | 2.6  |     |      |
| Gate Charge                               | $V_{GS} = 4.5\text{ V}, V_{DS} = 15\text{ V},$<br>$I_D = 1.5\text{ A}$ (Note 3)            | $Q_T$        | 3.9  |     | nC   |
|                                           |                                                                                            | $Q_1$        | 1.0  |     |      |
|                                           |                                                                                            | $Q_2$        | 1.7  |     |      |

## SOURCE-DRAIN DIODE CHARACTERISTICS

|                                |                                                                                                                          |          |              |     |               |
|--------------------------------|--------------------------------------------------------------------------------------------------------------------------|----------|--------------|-----|---------------|
| Forward On-Voltage             | $I_S = 2.6\text{ A}, V_{GS} = 0\text{ V}$ (Note 3)<br>$I_S = 2.6\text{ A}, V_{GS} = 0\text{ V}, T_J = 125^\circ\text{C}$ | $V_{SD}$ | 0.81<br>0.66 | 1.5 | V             |
| Reverse Recovery Time          | $I_S = 1.5\text{ A}, V_{GS} = 0\text{ V},$<br>$di_S/dt = 100\text{ A}/\mu\text{s}$ (Note 3)                              | $t_{rr}$ | 730          |     | ns            |
|                                |                                                                                                                          | $t_a$    | 200          |     |               |
|                                |                                                                                                                          | $t_b$    | 530          |     |               |
| Reverse Recovery Stored Charge |                                                                                                                          | $Q_{RR}$ | 6.3          |     | $\mu\text{C}$ |

## ESD CHARACTERISTICS (Note 4)

|                                     |                        |     |      |  |   |
|-------------------------------------|------------------------|-----|------|--|---|
| Electro-Static Discharge Capability | Human Body Model (HBM) | ESD | 5000 |  | V |
|                                     | Machine Model (MM)     |     | 500  |  |   |

- Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
- Not subject to production testing.
- Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES

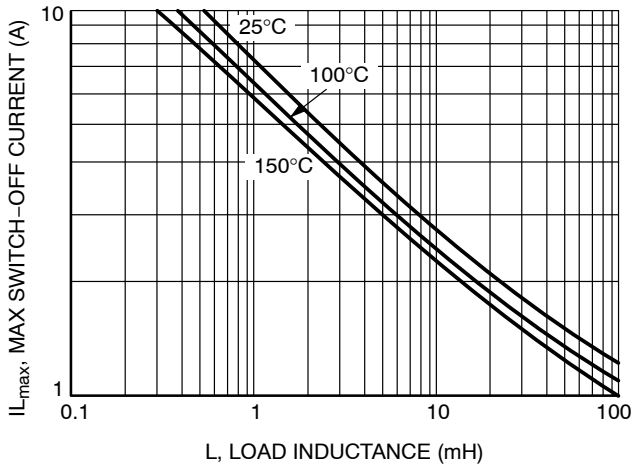


Figure 1. Single Pulse Maximum Switch-off Current vs. Load Inductance

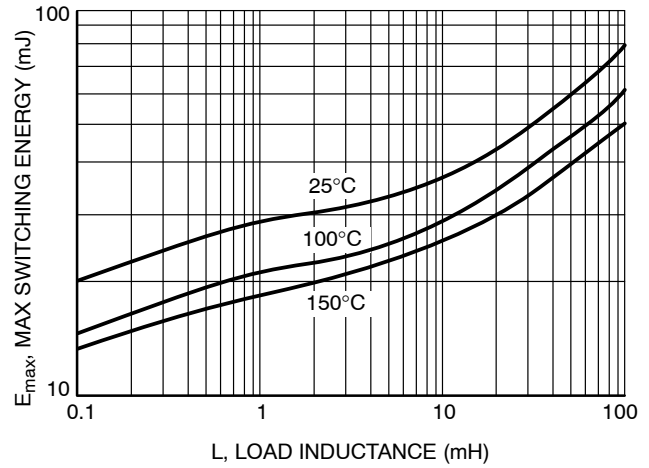


Figure 2. Single Pulse Maximum Switching Energy vs. Load Inductance

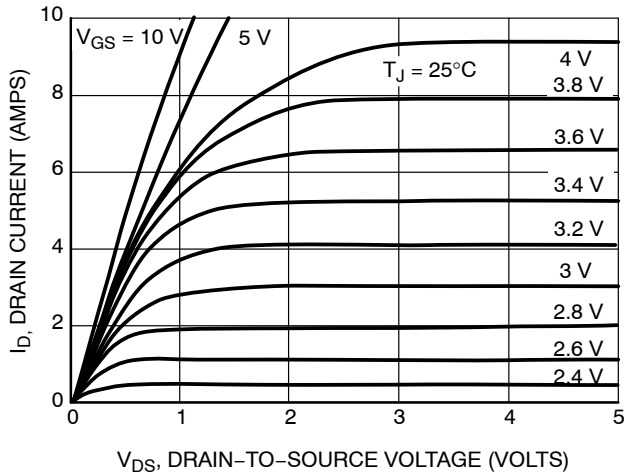


Figure 3. On-State Output Characteristics

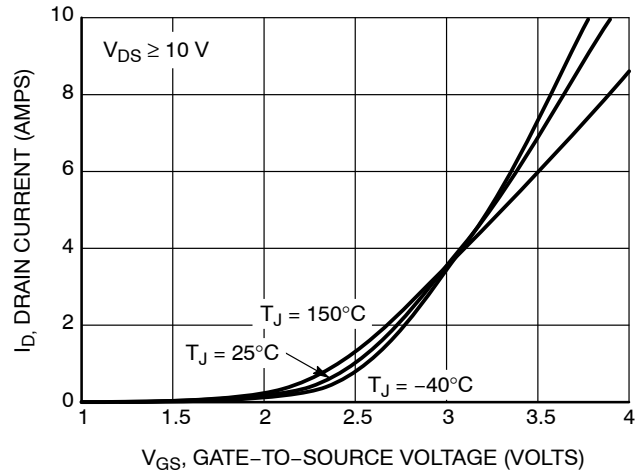


Figure 4. Transfer Characteristics

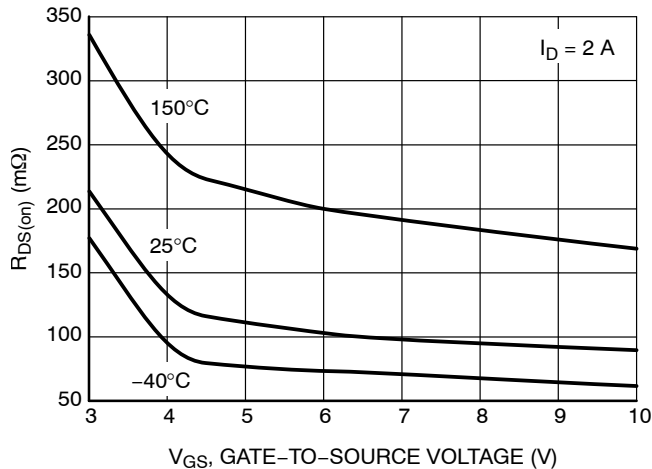


Figure 5.  $R_{DS(on)}$  vs. Gate-Source Voltage

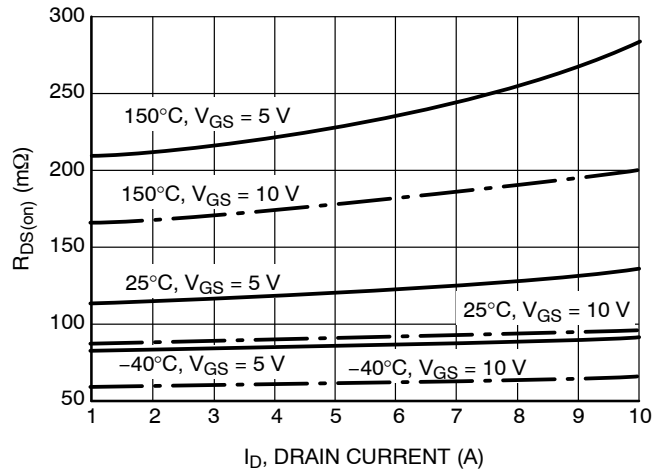


Figure 6.  $R_{DS(on)}$  vs. Drain Current

TYPICAL PERFORMANCE CURVES

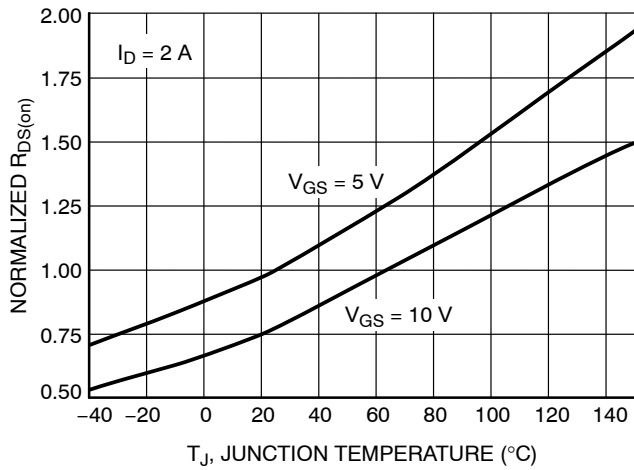


Figure 7. Normalized  $R_{DS(on)}$  vs. Temperature

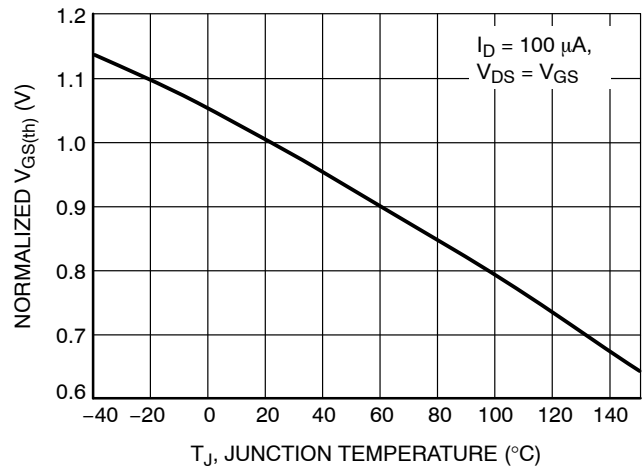


Figure 8. Normalized Threshold Voltage vs. Temperature

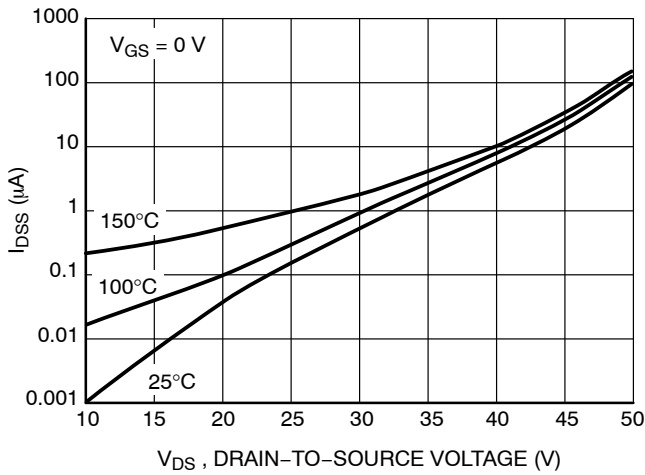


Figure 9. Drain-to-Source Leakage Current

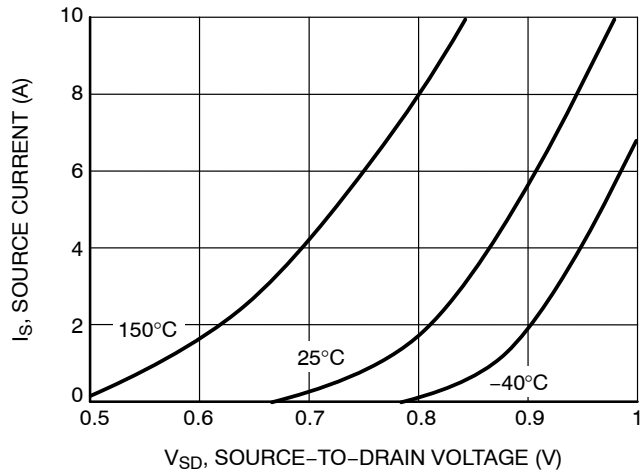


Figure 10. Source-Drain Diode Forward Characteristics

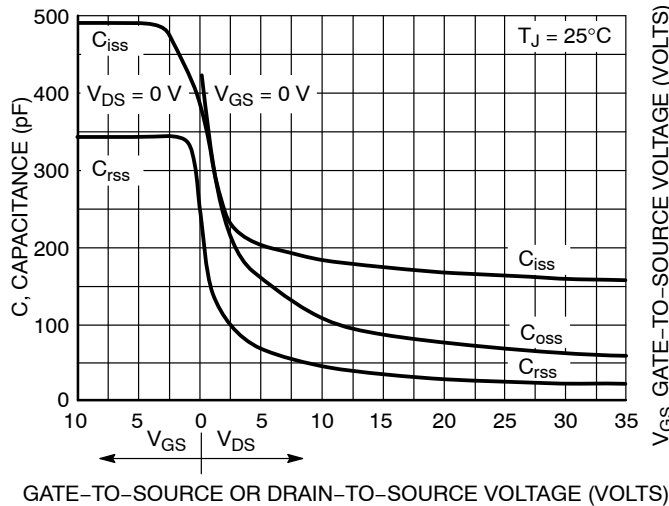


Figure 11. Capacitance Variation

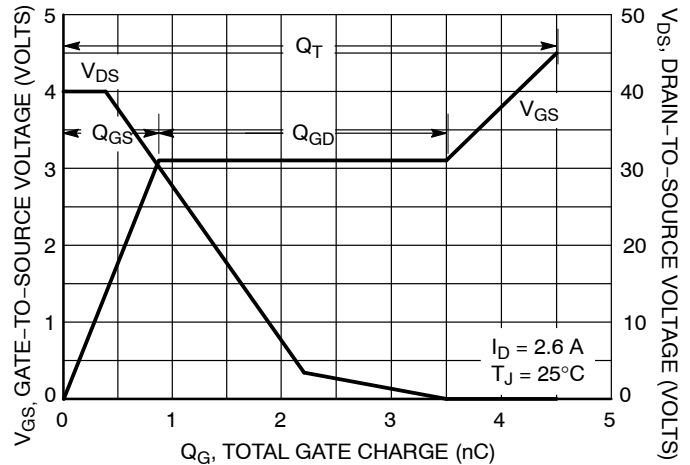


Figure 12. Gate-to-Source Voltage vs. Total Gate Charge

TYPICAL PERFORMANCE CURVES

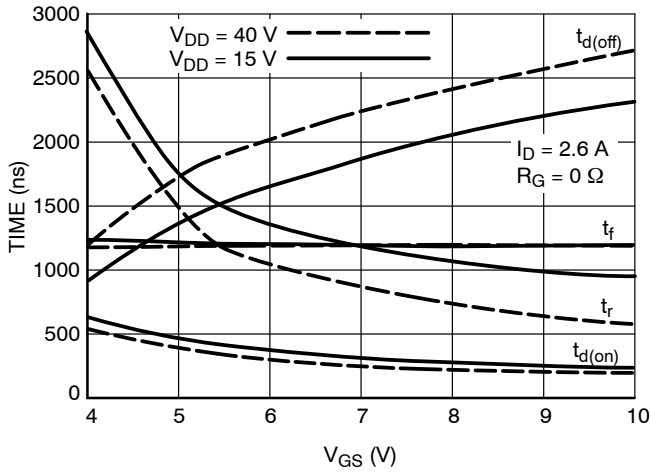


Figure 13. Resistive Load Switching Time vs. Gate-Source Voltage

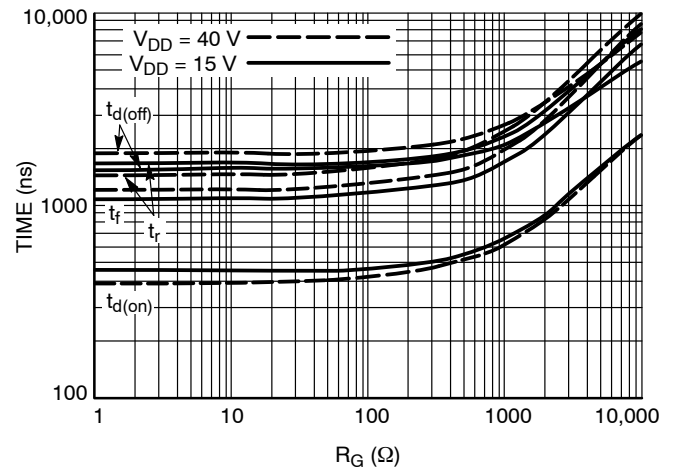


Figure 14. Resistive Load Switching Time vs. Gate Resistance ( $V_{GS} = 5\text{ V}$ ,  $I_D = 2.6\text{ A}$ )

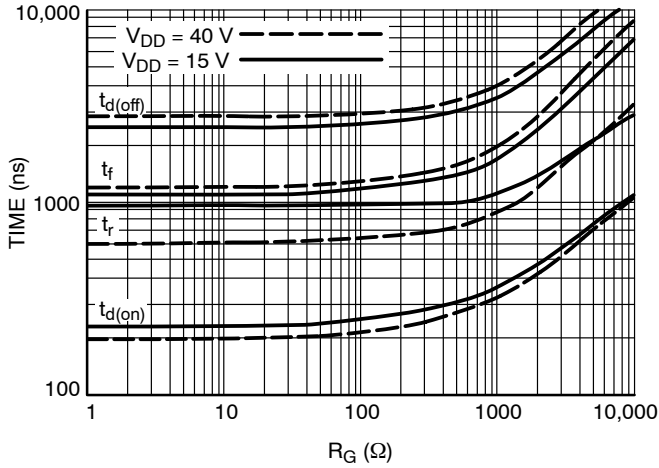


Figure 15. Resistive Load Switching Time vs. Gate Resistance ( $V_{GS} = 10\text{ V}$ ,  $I_D = 2.6\text{ A}$ )

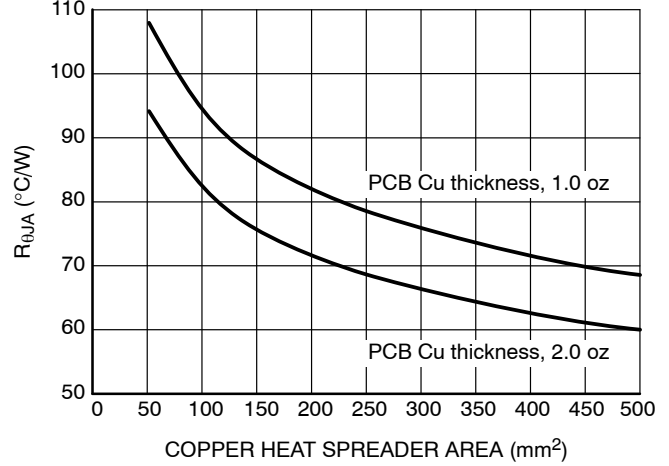


Figure 16.  $R_{\theta JA}$  vs. Copper Area

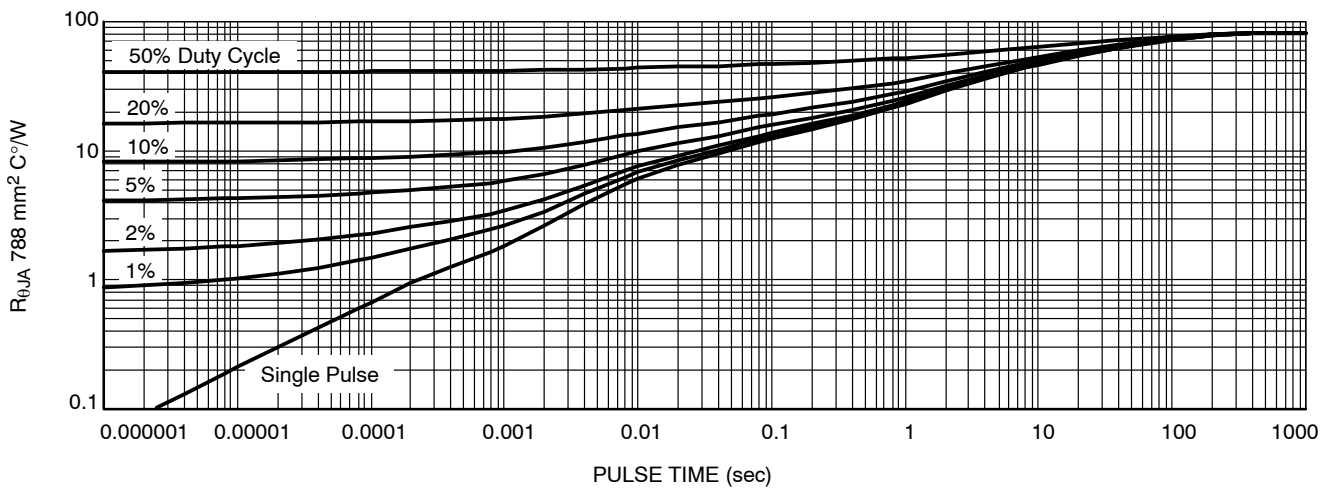


Figure 17. Transient Thermal Resistance

## NCV8440, NCV8440A

### ORDERING INFORMATION

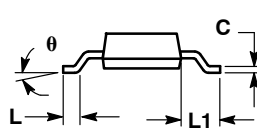
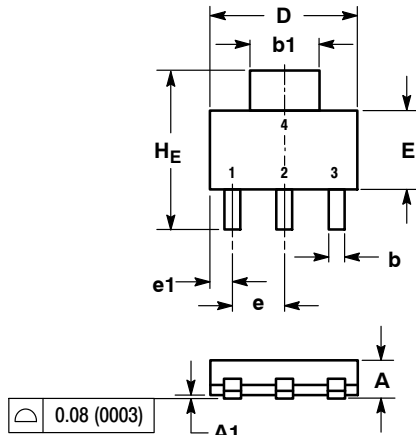
| Device        | Package              | Shipping†          |
|---------------|----------------------|--------------------|
| NCV8440STT1G  | SOT-223<br>(Pb-Free) | 1000 / Tape & Reel |
| NCV8440ASTT1G | SOT-223<br>(Pb-Free) | 1000 / Tape & Reel |
| NCV8440STT3G  | SOT-223<br>(Pb-Free) | 4000 / Tape & Reel |
| NCV8440ASTT3G | SOT-223<br>(Pb-Free) | 4000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NCV8440, NCV8440A

## PACKAGE DIMENSIONS

SOT-223 (TO-261)  
CASE 318E-04  
ISSUE N



### NOTES:

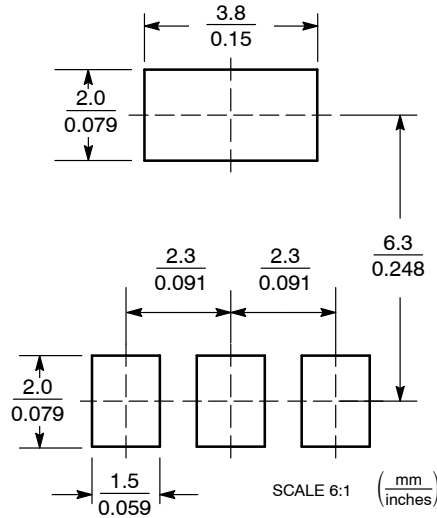
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCH.

| DIM | MILLIMETERS |      |      | INCHES |       |       |
|-----|-------------|------|------|--------|-------|-------|
|     | MIN         | NOM  | MAX  | MIN    | NOM   | MAX   |
| A   | 1.50        | 1.63 | 1.75 | 0.060  | 0.064 | 0.068 |
| A1  | 0.02        | 0.06 | 0.10 | 0.001  | 0.002 | 0.004 |
| b   | 0.60        | 0.75 | 0.89 | 0.024  | 0.030 | 0.035 |
| b1  | 2.90        | 3.06 | 3.20 | 0.115  | 0.121 | 0.126 |
| c   | 0.24        | 0.29 | 0.35 | 0.009  | 0.012 | 0.014 |
| D   | 6.30        | 6.50 | 6.70 | 0.249  | 0.256 | 0.263 |
| E   | 3.30        | 3.50 | 3.70 | 0.130  | 0.138 | 0.145 |
| e   | 2.20        | 2.30 | 2.40 | 0.087  | 0.091 | 0.094 |
| e1  | 0.85        | 0.94 | 1.05 | 0.033  | 0.037 | 0.041 |
| L   | 0.20        | ---  | ---  | 0.008  | ---   | ---   |
| L1  | 1.50        | 1.75 | 2.00 | 0.060  | 0.069 | 0.078 |
| HE  | 6.70        | 7.00 | 7.30 | 0.264  | 0.276 | 0.287 |
| θ   | 0°          | ---  | 10°  | 0°     | ---   | 10°   |

### STYLE 3:

- PIN 1. GATE
- DRAIN
- SOURCE
- DRAIN

## SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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