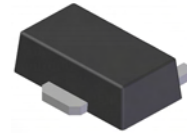


## Features

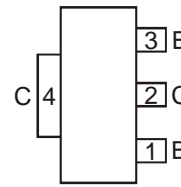
- Epitaxial Planar Die Construction
- Complementary NPN Type Available (DXT2222A)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- **Lead Free by Design/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**



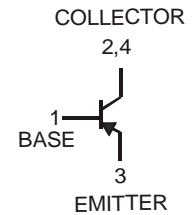
SOT89-3L

## Mechanical Data

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish - Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking & Type Code Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.072 grams



TOP VIEW



Schematic and Pin Configuration

## Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	-60	V
Collector-Base Voltage	$V_{CBO}$	-60	V
Emitter-Base Voltage	$V_{EBO}$	-5	V
Collector Current	$I_C$	-600	mA
Peak Collector Current	$I_{CM}$	-800	mA

## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ $T_A = 25^\circ\text{C}$	$P_D$	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$	125	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_j, T_{STG}$	-55 to +150	$^\circ\text{C}$

- Notes:
1. No purposefully added lead.
  2. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
  3. Device mounted on FR-4 PCB, pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>

## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 4)</b>					
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-60	—	V	I <sub>C</sub> = -10μA, I <sub>E</sub> = 0
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	-60	—	V	I <sub>C</sub> = -10mA, I <sub>B</sub> = 0
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-5.0	—	V	I <sub>E</sub> = -10μA, I <sub>C</sub> = 0
Collector Cutoff Current	I <sub>CBO</sub>	—	-10	nA μA	V <sub>CB</sub> = -50V, I <sub>E</sub> = 0 V <sub>CB</sub> = -50V, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C
Collector Cutoff Current	I <sub>CEX</sub>	—	-50	nA	V <sub>CE</sub> = -30V, V <sub>EB(OFF)</sub> = -0.5V
Emitter Cutoff Current	I <sub>EBO</sub>	—	-50	nA	V <sub>EB</sub> = -5V, I <sub>C</sub> = 0
Base Cutoff Current	I <sub>BL</sub>	—	-50	nA	V <sub>CE</sub> = -30V, V <sub>EB(OFF)</sub> = -0.5V
<b>ON CHARACTERISTICS (Note 4)</b>					
DC Current Gain	h <sub>FE</sub>	75 100 100 100 50	— — — 300 —	—	I <sub>C</sub> = -100μA, V <sub>CE</sub> = -10V I <sub>C</sub> = -1.0mA, V <sub>CE</sub> = -10V I <sub>C</sub> = -10mA, V <sub>CE</sub> = -10V I <sub>C</sub> = -150mA, V <sub>CE</sub> = -10V I <sub>C</sub> = -500mA, V <sub>CE</sub> = -10V
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	—	-0.4 -1.6	V	I <sub>C</sub> = -150mA, I <sub>B</sub> = -15mA I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	—	-1.3 -2.6	V	I <sub>C</sub> = -150mA, I <sub>B</sub> = -15mA I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA
<b>SMALL SIGNAL CHARACTERISTICS</b>					
Output Capacitance	C <sub>obo</sub>	—	8.0	pF	V <sub>CB</sub> = -10V, f = 1.0MHz, I <sub>E</sub> = 0
Input Capacitance	C <sub>ibo</sub>	—	30	pF	V <sub>EB</sub> = -2.0V, f = 1.0MHz, I <sub>C</sub> = 0
Current Gain-Bandwidth Product	f <sub>T</sub>	200	—	MHz	V <sub>CE</sub> = -20V, I <sub>C</sub> = -50mA, f = 100MHz
<b>SWITCHING CHARACTERISTICS</b>					
Turn-On Time	t <sub>on</sub>	—	45	ns	V <sub>CC</sub> = -30V, I <sub>C</sub> = -150mA, I <sub>B1</sub> = -15mA
Delay Time	t <sub>d</sub>	—	10	ns	
Rise Time	t <sub>r</sub>	—	40	ns	
Turn-Off Time	t <sub>off</sub>	—	100	ns	V <sub>CC</sub> = -6.0V, I <sub>C</sub> = -150mA, I <sub>B1</sub> = I <sub>B2</sub> = -15mA
Storage Time	t <sub>s</sub>	—	80	ns	
Fall Time	t <sub>f</sub>	—	30	ns	

Notes: 4. Short duration pulse test used to minimize self-heating effect.

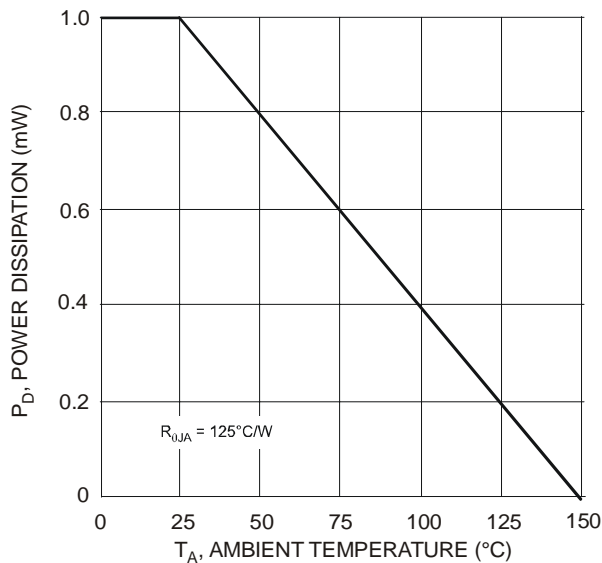


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 3)

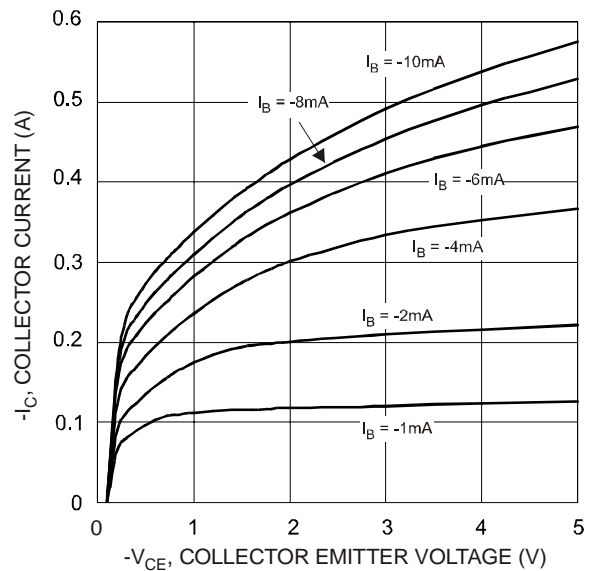


Fig. 2 Typical Collector Current as a Function of Collector Emitter Voltage

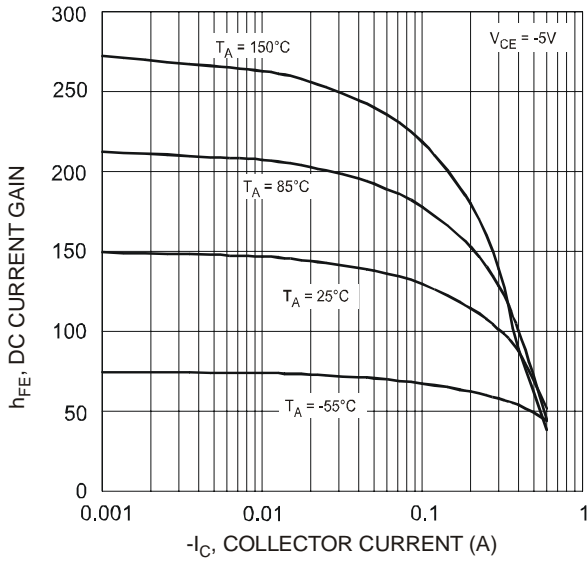


Fig. 3 Typical DC Current Gain vs. Collector Current

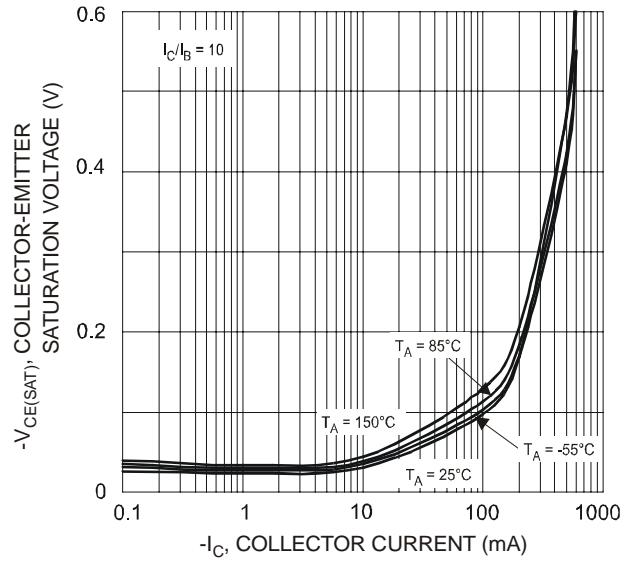


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

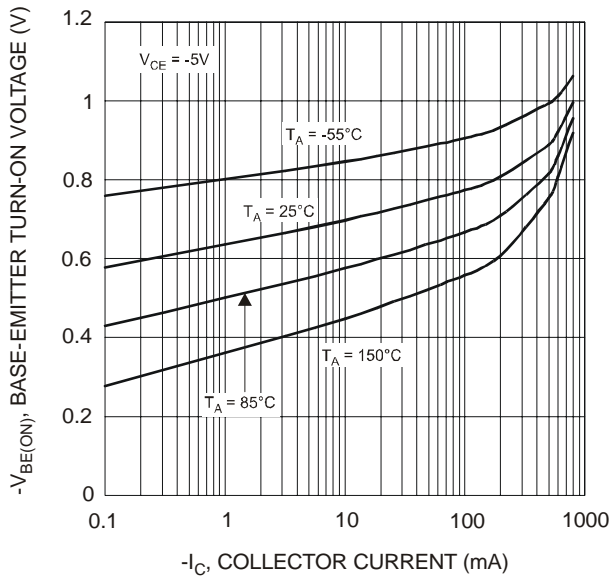


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

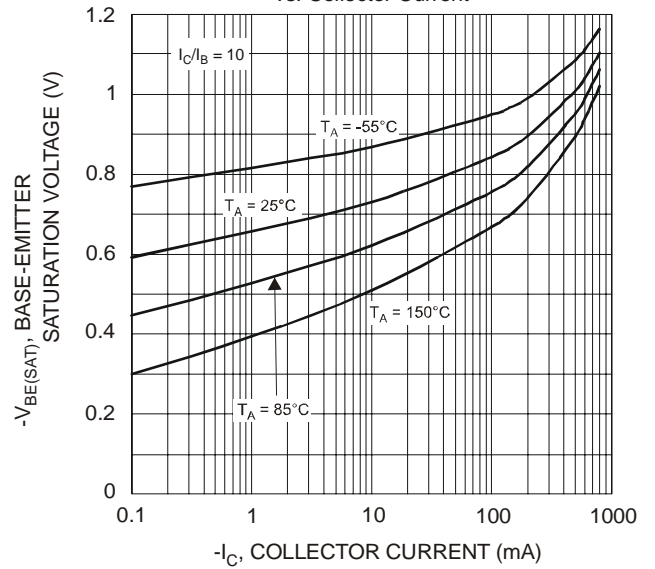


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

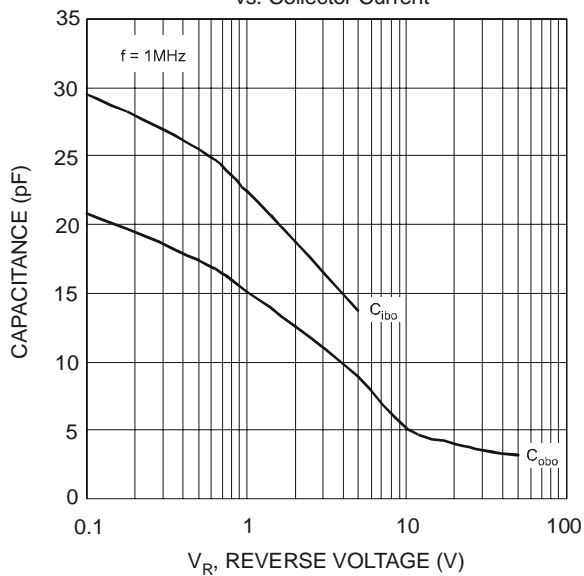


Fig. 7 Typical Capacitance Characteristics

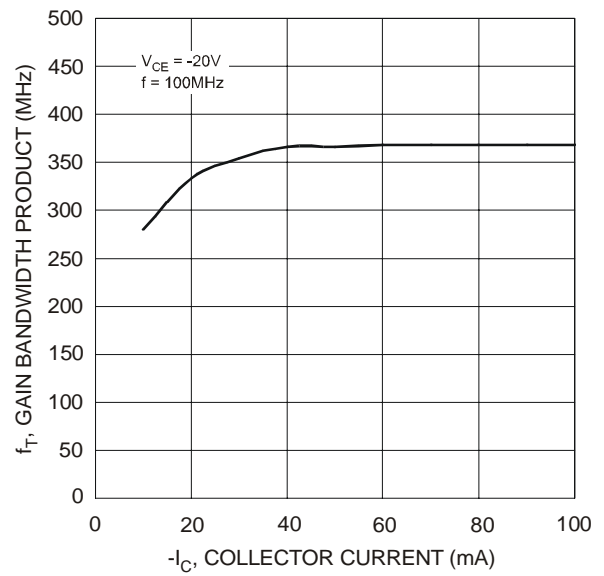


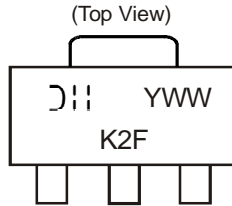
Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

## Ordering Information (Note 5)

Device	Packaging	Shipping
DXT2907A-13	SOT89-3L	2500/Tape & Reel

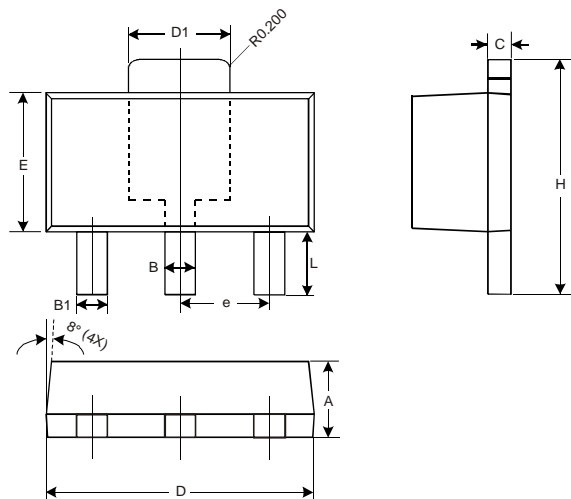
Notes: 5. For packaging details go to our website at <http://www.diodes.com/ap2007.pdf>.

## Marking Information



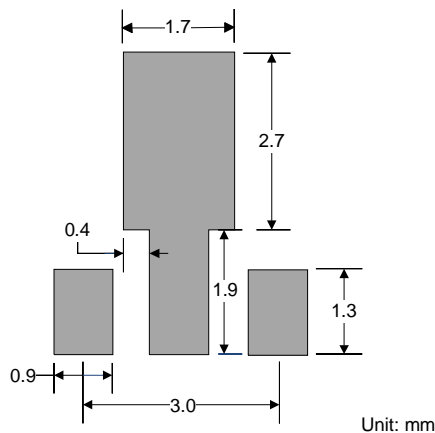
☺ = Manufacturer's code marking  
 K2F = Product Type Marking Code  
 YWW = Date Code Marking  
 Y = Last digit of year ex: 7 = 2007  
 WW = Week code 01 - 52

## Package Outline Dimensions



SOT89-3L			
Dim	Min	Max	Typ
A	1.40	0.160	1.50
B	0.45	0.55	0.50
B1	0.37	0.47	0.42
C	0.35	0.43	0.38
D	4.40	4.60	4.50
D1	1.50	1.70	1.60
E	2.40	2.60	2.50
e	—	—	1.50
H	3.95	4.25	4.10
L	0.90	1.20	1.05
All Dimensions in mm			

## Suggested Pad Layout



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