

General Description

The MAX1482 and MAX1483 are low-power transceivers for RS-485 and RS-422 communication. Both feature slew-rate-limited drivers that minimize EMI and reduce reflections caused by improperly terminated cables. Data rates are guaranteed up to 250kbps.

The MAX1482/MAX1483 draw only 20µA of supply current. Additionally, they have a low-current shutdown mode that consumes only 0.1µA. Both parts operate from a single +5V supply.

Drivers are short-circuit current limited and are protected against excessive power dissipation by thermal shutdown circuitry that places the driver outputs into a high-impedance state. The receiver input has a fail-safe feature that guarantees a logic-high output if the input is open circuit.

The MAX1482 is full duplex and the MAX1483 is half duplex. Both parts have a 1/8-unit-load input impedance that guarantees up to 256 transceivers on the bus.

Applications

Low-Power RS-485/RS-422 Networks Transceivers for EMI-Sensitive Applications Industrial-Control Local Area Networks Large 256-Node LANs

Features

- ♦ Low 20µA Operating Current
- ♦ Slew-Rate Limited for Reduced EMI and **Reduced Reflections**
- ♦ 0.1µA Low-Current Shutdown Mode
- ♦ Designed for RS-485 and RS-422 Applications
- ♦ Operate from a Single +5V Supply
- ◆ -7V to +12V Common-Mode Input Voltage Range
- ♦ Allows up to 256 Transceivers on the Bus— Guaranteed (1/8-unit load)
- **♦** Current Limiting and Thermal Shutdown for **Driver Overload Protection**

Ordering Information

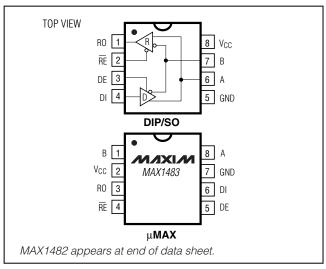
PART	TEMP RANGE	PIN-PACKAGE	PKG CODE
MAX1482CPD	0°C to +70°C	14 PDIP	P14-3
MAX1482CSD	0°C to +70°C	14 SO	S14-4
MAX1482EPD	-40°C to +85°C	14 PDIP	P14-3
MAX1482ESD	-40°C to +85°C	14 SO	S14-4
MAX1483CPA	0°C to +70°C	8 PDIP	P8-1
MAX1483CSA	0°C to +70°C	8 SO	S8-5
MAX1483CUA	0°C to +70°C	8 μMAX®	U8-1
MAX1483EPA	-40°C to +85°C	8 PDIP	P8-1
MAX1483ESA	-40°C to +85°C	8 SO	S8-5

Typical Operating Circuits

MIXIM MAX1483 RF 2 DE 3 NOTE: PIN LABELS Y AND Z ON TIMING, TEST, AND WAVEFORM DIAGRAMS REFER TO PINS A AND B WHEN DE IS HIGH. TYPICAL OPERATING CIRCUIT SHOWN WITH DIP/SO PACKAGE. MAX1482 appears at end of data sheet.

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Pin Configurations



MIXIM

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (VCC)	7V
Control Input Voltages (RE, DE)	
Driver Input Voltage (DI)	-0.5V to $(VCC + 0.5V)$
Driver Output Voltages	7.5V to 12.5V
Receiver Input Voltages (A, B)	7.5V to 12.5V
Receiver Output Voltage (RO)	$-0.5V$ to $(V_{CC} + 0.5V)$
Continuous Power Dissipation (T _A = +70°C	C)
8-Pin Plastic DIP (derate 9.09mW/°C abo	ve +70°C)727mW
14-Pin Plastic DIP (derate 10.00mW/°C a	bove +70°C) .800mW

471mW 667mW C)330mW
,
0°C to +70°C
40°C to +85°C
65°C to +160°C
+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

(V_{CC} = 5V ±5%, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS)	MIN	TYP	MAX	UNITS
Differential Driver Output (no load)	V _{OD1}					5	V
Differential Driver Output	\/op*	R = 50Ω (RS-422), Figure 1		2		5	V
(with load)	V _{OD2}	$R = 27\Omega$ (RS-485), Figure 1		1.5		5	V
Change in Magnitude of Driver Differential Output Voltage for Complementary Output States	ΔV _{OD}	R = 27Ω or 50Ω , Figure 1				0.2	V
Driver Common-Mode Output Voltage	Voc	$R = 27\Omega$ or 50Ω , Figure 1				3	V
Change in Magnitude of Driver Common-Mode Output Voltage for Complementary Output States	ΔV _{OD}	R = 27Ω or 50Ω , Figure 1	R = 27Ω or 50Ω , Figure 1			0.2	V
Three-State (high impedance) Output Current at Driver	lozd	MAX1482 only, -7V < V _{Y and} V _Z < 12V				±50	μΑ
Logic Input High Voltage	VIH	DE, DI, RE		2.0			V
Logic Input Low Voltage	VIL	DE, DI, RE				0.8	V
Logic Input Current	I _{IN1}	DE, DI, RE				±2	μΑ
		MAX1482,	V _{IN} = 12V			150	μA
Input Current	I _{IN2}	DE = 0V, VCC = 0V or 5.25V	$V_{IN} = -7V$			-100	
(A, B)		MAX1483,	VIN = 12V			200	μΑ
		DE = 0V, V _{CC} = 0V or 5.25V	$V_{IN} = -7V$			-150	
Receiver Differential Threshold Voltage	V _{TH}	-7V ≤ V _{CM} ≤ 12V		-0.2		0.2	V
Receiver Input Hysteresis	ΔVTH	VCM = 0V			75		mV
Receiver Output High Voltage	VoH	$I_O = -4mA$, $V_{ID} = 200mV$		3.5			V
Receiver Output Low Voltage	VoL	I _O = 4mA, V _{ID} = -200mV				0.4	V
Three-State (high impedance) Output Current at Receiver	I _{OZR}	$0.4V \le V_{\text{O}} \le 2.4V$				±1	μA
Receiver Input Resistance	RIN	-7V ≤ V _{CM} ≤ 12V		96			kΩ

Note 1: All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to device ground unless otherwise specified.

DC ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = 5V \pm 5\%, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.) \text{ (Note 1)}$

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
No-Load Supply Current	I _{CC} RE	DF 01/ 1/	DE = V _{CC}		25	45	
			DE = 0V		20	35	
		MAX1483, RE = 0V or V _{CC}	DE = Vcc		55	85	μΑ
			DE = 0V		20	35	1
Supply Current in Shutdown	ISHDN	DE = 0V, RE = V _{CC}			0.1	10	μΑ
Driver Short-Circuit Current	IOSD	DI = high or low, $-7V \le V_O \le 12V$ (Note 2)		35		250	mA
Receiver Short-Circuit Current	Iosr	$OV \leq V_O \leq V_{CC}$		±7		±95	mA

SWITCHING CHARACTERISTICS

 $(V_{CC} = 5V \pm 5\%, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.) \text{ (Note 1)}$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Input to Output	tplH	Figures 3 and 5, RDIFF = 54Ω ,			2	LIC
Driver input to Output	tphL	$C_{L1} = C_{L2} = 100pF$			2	μs
Driver Output Skew to Output	tskew	Figures 3 and 5, RDIFF = 54Ω , CL1 = CL2 = 100 pF			800	ns
Driver Rise or Fall Time	t _R , t _F	Figures 3 and 5, $R_{DIFF} = 54\Omega$, $C_{L1} = C_{L2} = 100pF$	0.25		2	μs
Driver Enable to Output High	tzH	Figures 4 and 6, C _L = 100pF, S2 closed	0.2		2	μs
Driver Enable to Output Low	tzL	Figures 4 and 6, C _L = 100pF, S1 closed	0.1		2	μs
Driver Disable Time from Low	tLZ	Figures 4 and 6, C _L = 15pF, S1 closed	0.3		3.0	μs
Driver Disable Time from High	tHZ	Figures 4 and 6, C _L = 15pF, S2 closed	0.3		3.0	μs
Receiver Input to Output	tpLH, tpHL	Figures 3 and 7, RDIFF = 54Ω , CL1 = CL2 = 100 pF	0.25		2.25	μs
I tPLH - tPHL I Differential Receiver Skew	tskd	Figures 3 and 7, $R_{DIFF} = 54\Omega$, $C_{L1} = C_{L2} = 100pF$		160		ns
Receiver Enable to Output Low	tzL	Figures 2 and 8, C _{RL} = 15pF, S1 closed			90	ns
Receiver Enable to Output High	tzH	Figures 2 and 8, CRL = 15pF, S2 closed			90	ns
Receiver Disable Time from Low	tLZ	Figures 2 and 8, C _{RL} = 15pF, S1 closed			90	ns
Receiver Disable Time from High	tHZ	Figures 2 and 8, C _{RL} = 15pF, S2 closed			90	ns
Maximum Data Rate	fMAX		250			kbps
Time to Shutdown	tshdn	(Note 3)	50	200	600	ns
Driver Enable from Shutdown to Output High	tzh(SHDN)	Figures 4 and 6, C _L = 100pF, S2 closed			2	μs
Driver Enable from Shutdown to Output Low	tZL(SHDN)	Figures 4 and 6, C _L = 100pF, S1 closed			2	μs
Receiver Enable from Shutdown to Output High	tzh(SHDN)	Figures 2 and 8, C _L = 15pF, S2 closed, A - B = 2V			3	μs
Receiver Enable from Shutdown to Output Low	tZL(SHDN)	Figures 2 and 8, C _L = 15pF, S1 closed, B - A = 2V			3	μs

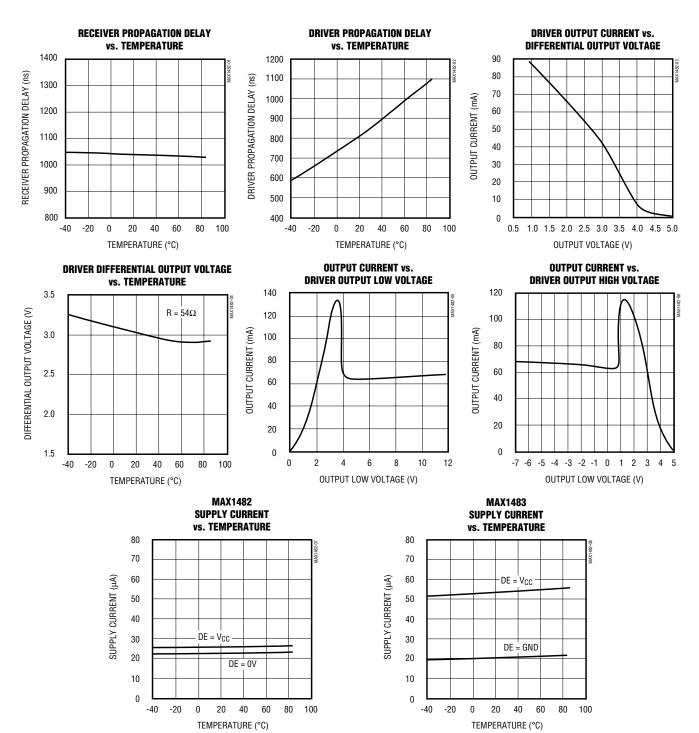
Note 2: Applies to peak current. See *Typical Operating Characteristics*.

Note 3: The MAX1482/MAX1483 are put into shutdown by bringing \overline{RE} high and DE low. If the inputs are in this state for less than 50ns, the parts are guaranteed not to enter shutdown. If the inputs are in this state for at least 600ns, the parts are guaranteed to have entered shutdown. See *Low-Power Shutdown Mode* section.



Typical Operating Characteristics

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$



_Pin Description

PIN MAX1482 MAX1483		PIN			
		NAME	FUNCTION		
DIP/SO	DIP/SO	μМΑХ			
2	1	3	RO	Receiver Output. With the receiver output enabled (RE low), RO is high if A > B by 200mV or when A and B are not connected, and RO is low if A < B by 200mV.	
3	2	4	RE	Receiver Output Enable. When RE is low, RO is enabled. When RE is high, RO is high impedance. If RE is high and DE is low, the MAX1482/MAX1483 enter a low-power (0.1μA) shutdown state.	
4	3	5	DE	Driver Output Enable. The driver outputs, A and B, (Y and Z for the MAX1482) are enabled by bringing DE high. When DE is low, the driver outputs are high impedance, and the devices can function as line receivers if RE is low. If RE is high and DE is low, the parts will enter a low-power (0.1µA) shutdown state. If the driver outputs are enabled, the devices function as line drivers.	
5	4	6	DI	Driver Input. With DE high, a low on DI forces output Y low and output Z high, and a high on DI forces output Y high and output Z low.	
6, 7	5	7	GND	Ground	
9	_	_	Υ	Noninverting Driver Output	
10	_	_	Z	Inverting Driver Output	
_	6	8	А	Noninverting Receiver Input and Noninverting Driver Output	
12	_	_	А	Noninverting Receiver Input	
_	7	1	В	Inverting Receiver Input and Inverting Driver Output	
11	_	_	В	Inverting Receiver Input	
14	8	2	Vcc	Positive Supply: 4.75V to 5.25V	
1, 8, 13	_	_	N.C.	No Connect—not internally connected	

Test Circuits

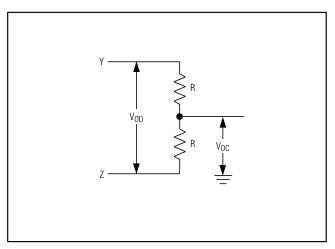


Figure 1. Driver DC Test Load

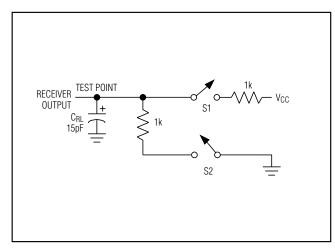


Figure 2. Receiver Timing Test Load

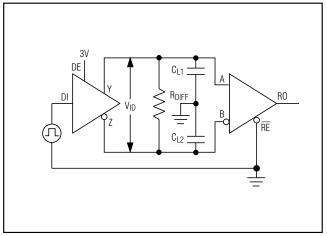


Figure 3. Driver/Receiver Timing Test Circuit

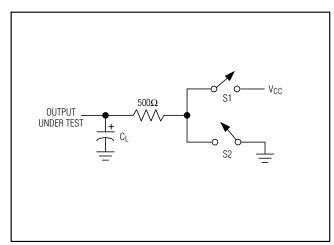


Figure 4. Driver Timing Test Load

Switching Waveforms

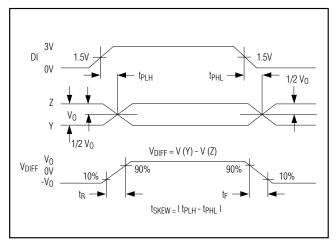


Figure 5. Driver Propagation Delays

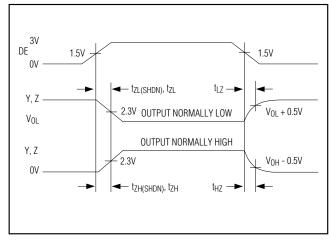


Figure 6. Driver Enable and Disable Times

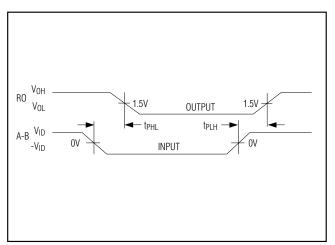


Figure 7. Receiver Propagation Delays

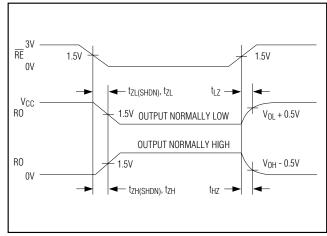


Figure 8. Receiver Enable and Disable Times

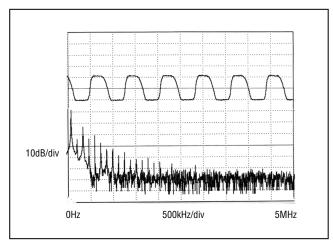


Figure 9. Driver Output Waveform and FFT, Transmitting 250kbps (125kHz) Signal

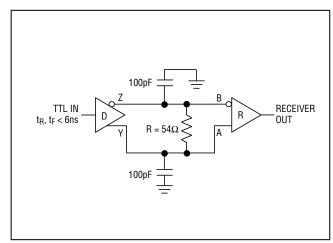


Figure 10. Receiver Propagation-Delay Test Circuit

Table 1. Transmitting

	INPUTS	OUTPUTS		
RE	DE	DI	z	Y
Х	1	1	0	1
Х	1	0	1	0
Х	0	Х	High-Z	High-Z

X = Don't Care High-Z = High Impedance

Table 2. Receiving

	OUTPUT		
RE	DE*	A-B	RO
0	0	≥ +0.2V	1
0	0	≤ -0.2V	0
0	0	Inputs open	1
1	0	Х	High-Z

X = Don't Care

High-Z = High Impedance

_Applications Information

The MAX1482/MAX1483 are low-power transceivers for RS-485 and RS-422 communications. The MAX1482 and MAX1483 are specified for data rates of at least 250kbps. The MAX1482 is a full-duplex transceiver while the MAX1483 is half duplex. When disabled, the driver and receiver outputs are high impedance.

The 96k Ω , 1/8-unit-load receiver input impedance of the MAX1482/MAX1483 allows up to 256 transceivers on a bus, compared to the 1-unit load (12k Ω input impedance) of standard RS-485 drivers (32 transceivers maximum). Any combination of MAX1482/MAX1483 and other RS-485 transceivers with a total of 32 unit loads or less can be put on the bus.

Reduced EMI and Reflections

The MAX1482/MAX1483 are slew-rate limited, minimizing EMI and reducing reflections caused by improperly terminated cables. Figure 9 shows both the driver output waveform of a MAX1482/MAX1483 transmitting a 125kHz signal and the Fourier analysis of that signal.

High-frequency harmonics have much lower amplitudes, and the potential for EMI is significantly reduced.

^{*} DE = 0 for MAX1483 and is a Don't Care for MAX1482.

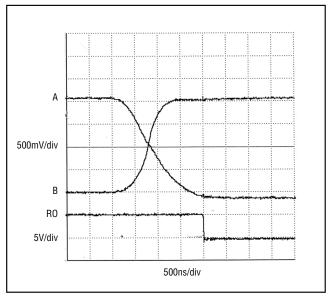


Figure 11. Receiver tPHL

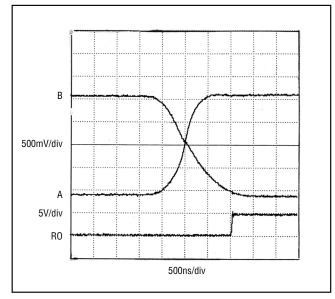


Figure 12. Receiver tPLH

Low-Power Shutdown Mode

A low-power shutdown mode is initiated by bringing RE high and DE low. The devices will not shut down unless **both** the driver and receiver are disabled. In shutdown, the devices typically draw only $0.1\mu A$ of supply current.

RE and DE may be driven simultaneously; the parts are guaranteed not to enter shutdown if RE is high and DE is low for less than 50ns. If the inputs are in this state for at least 600ns, the parts are guaranteed to enter shutdown.

For the receiver, the t_{ZH} and t_{ZL} enable times assume the part was not in the low-power shutdown state. The $t_{ZH(SHDN)}$ and $t_{ZL(SHDN)}$ enable times assume the parts were shut down (see *Electrical Characteristics*).

It takes the receivers longer to become enabled from the low-power shutdown state (tzH(SHDN), tzL(SHDN)) than from the operating mode (tzH, tzL). (The parts are in operating mode if the RE , DE inputs equal a logical 0,1 or 1,1 or 0,0.)

Driver Output Protection

Excessive output current and power dissipation caused by faults or by bus contention are prevented by two mechanisms. A foldback current limit on the output stage provides immediate protection against short circuits over the whole common-mode voltage range (see *Typical Operating Characteristics*). In addition, a thermal shutdown circuit forces the driver outputs into a high-impedance state if the die temperature rises excessively.

Propagation Delay

Digital encoding schemes depend on the driver and receiver skew. Skew is defined as the difference between the rising and falling propagation delay times. Typical propagation delays are shown in Figures 11 and 12 using Figure 10's test circuit.

The difference in receiver delay times, I tpLH - tpHL I, is typically under 160ns.

The driver skew times are typically 160ns (800ns max).

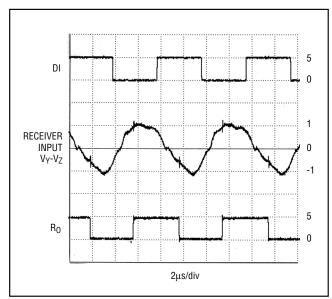


Figure 13. System Differential Voltage at 250kbps (125kHz) Driving 4000 Feet of Cable

Line Length vs. Data Rate

The RS-485/RS-422 standard covers line lengths up to 4000 feet. For line lengths greater than 4000 feet, see Figure 16.

Figure 13 shows the system differential voltage for the parts driving 4000 feet of 26AWG twisted-pair wire at 110kHz into 120 Ω loads. Even after 4000 feet of cable, the MAX1482/MAX1483 output shows virtually no distortion.

Typical Applications

The MAX1482/MAX1483 transceivers are designed for bidirectional data communications on multipoint bus transmission lines. Figures 14 and 15 show typical network applications circuits. These parts can also be used as line repeaters, with cable lengths longer than 4000 feet, as shown in Figure 16.

To minimize reflections, the line should be terminated at both ends in its characteristic impedance, and stub lengths off the main line should be kept as short as possible (although the slew-rate-limited MAX1482 and MAX1483 are more tolerant of imperfect termination than standard RS-485 ICs).

Isolated RS-485

For isolated RS-485 applications, see the MAX253 and MAX1480 data sheets.

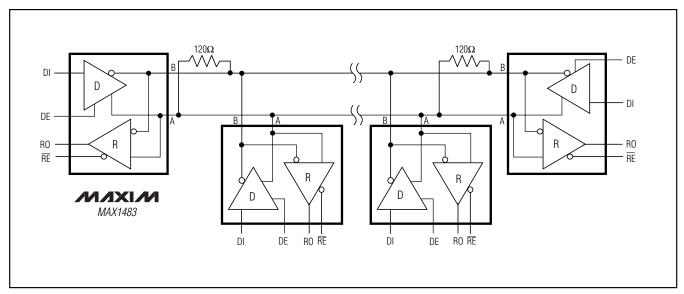


Figure 14. MAX1483 Typical Half-Duplex RS-485 Network

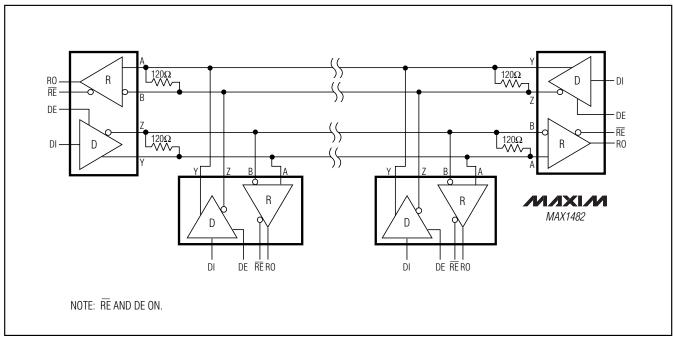
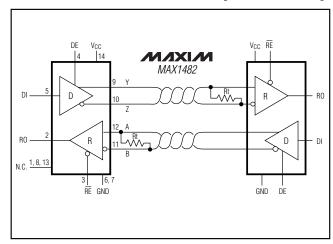


Figure 15. MAX1482 Full-Duplex RS-485 Network

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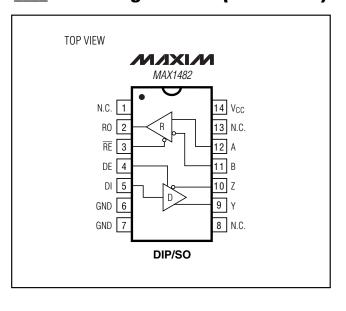
Figure 16. Line Repeater for MAX1482

Typical Operating Circuits (continued)



Pin Configurations (continued)

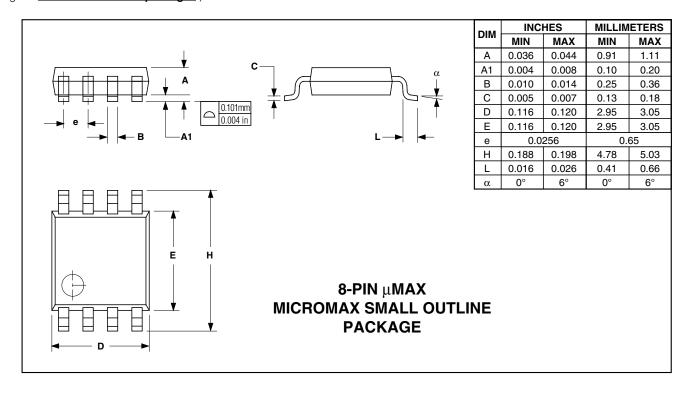




TRANSISTOR COUNT: 294

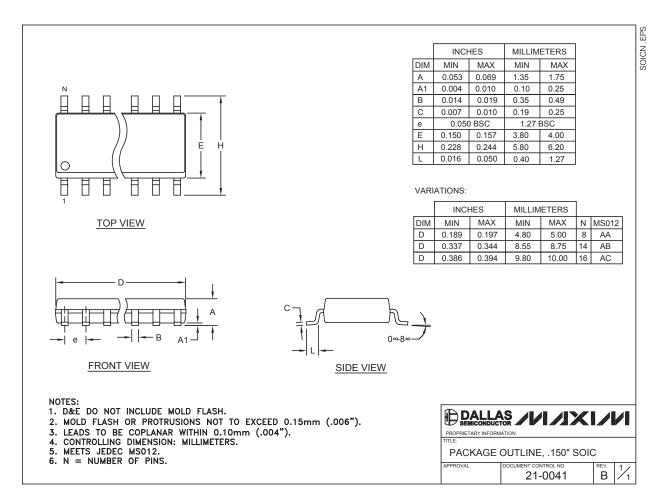
_Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



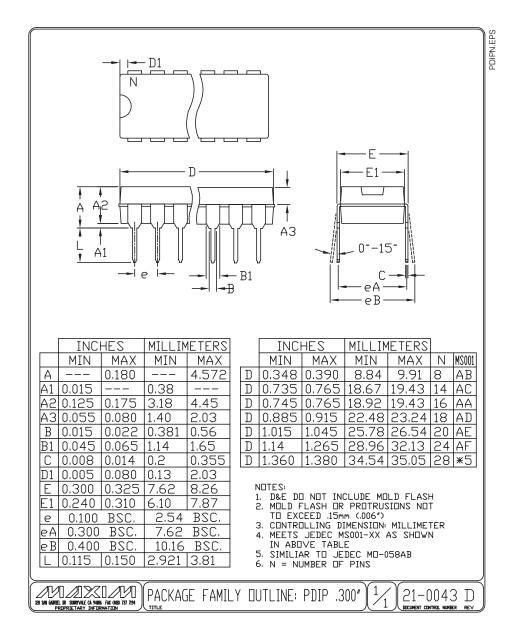
Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



Package Information (continued)

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