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March 1998

### FDN357N N-Channel Logic Level Enhancement Mode Field Effect Transistor

#### **General Description**

SuperSOT<sup>TM</sup>-3 N-Channel logic level enhancement mode power field effect transistors are produced using Fairchild's proprietary, high cell density, DMOS technology. This very high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage applications in notebook computers, portable phones, PCMCIA cards, and other battery powered circuits where fast switching, and low in-line power loss are needed in a very small outline surface mount package.

#### Features

- 1.9 A, 30 V,  $R_{DS(ON)} = 0.090 \ \Omega \ @ V_{GS} = 4.5 \ V$  $R_{DS(ON)} = 0.060 \ \Omega \ @ V_{GS} = 10 \ V.$
- Industry standard outline SOT-23 surface mount package using proprietary SuperSOT<sup>™</sup>-3 design for superior thermal and electrical capabilities.
- High density cell design for extremely low R<sub>DS(ON)</sub>.
- Exceptional on-resistance and maximum DC current capability.

so	OT-23	SuperSOT <sup>™</sup> -6	SuperSOT <sup>™</sup> -8	SO-8	SOT-223	SOIC-16
		D 351	s			
	-	<sub>SOT<sup>™</sup>3</sub> G			G	
Absol Symbol	-	<sub>SOT<sup>™</sup>3</sub> G	= 25°C unless other wise not	ied	G S	Units
Symbol	ute Maximu	$\mathbf{SOT}^{T}_{3}  \mathbf{G}$		ed		
<b>Symbol</b> / DSS	ute Maximu Parameter Drain-Source	$\mathbf{SOT}^{T}_{3}  \mathbf{G}$	= 25°C unless other wise not	ed	FDN357N	Units
	ute Maximu Parameter Drain-Source Gate-Source	SOT <sup>™</sup> 3 G Im Ratings T <sub>A</sub> =	= 25°C unless other wise not	ed	<b>FDN357N</b> 30	Units
bymbol DSS GSS	ute Maximu Parameter Drain-Source Gate-Source	SOT <sup>™</sup> -3 G Im Ratings T <sub>A</sub> = e Voltage Voltage - Continuous	= 25°C unless other wise not	ied	FDN357N 30 ±20	Units V V V
Symbol DSS GSS	ute Maximu Parameter Drain-Source Gate-Source Drain/Output	SOT <sup>™</sup> -3 G m Ratings T <sub>A</sub> = Poltage Voltage - Continuous Current - Continuous	= 25°C unless other wise not	ied	FDN357N 30 ±20 1.9	Units V V
Cymbol DSS GSS	ute Maximu Parameter Drain-Source Gate-Source Drain/Output	SOT <sup>™</sup> -3 G MR Ratings T <sub>A</sub> = 2 Voltage Voltage - Continuous Current - Continuou - Pulsed	= 25°C unless other wise not s	ed	FDN357N 30 ±20 1.9 10	Units V V A
ymbol DSS GSS D	ute Maximu Parameter Drain-Source Gate-Source Drain/Output Maximum Po	SOT <sup>™</sup> -3 G MR Ratings T <sub>A</sub> = 2 Voltage Voltage - Continuous Current - Continuou - Pulsed	= 25°C unless other wise not S S (Note 1a) (Note 1b)	ied	FDN357N 30 ±20 1.9 10 0.5	Units V V A
joss joss joss jo joss joss joss joss j	ute Maximu Parameter Drain-Source Gate-Source Drain/Output Maximum Po	SOT <sup>™</sup> -3 G Im Ratings T <sub>A</sub> = Poltage Voltage - Continuous Current - Continuous - Pulsed wer Dissipation d Storage Temperatu	= 25°C unless other wise not S S (Note 1a) (Note 1b)		FDN357N 30 ±20 1.9 10 0.5 0.46	Units V V V A W W
Coss Coss Coss Coss Co Co Co Co Co Co Co Co Co Co Co Co Co	Ute Maximu       Parameter       Drain-Source       Gate-Source       Drain/Output       Maximum Po       Operating ar       AL CHARACTE	SOT <sup>™</sup> -3 G Im Ratings T <sub>A</sub> = Poltage Voltage - Continuous Current - Continuous - Pulsed wer Dissipation d Storage Temperatu	= 25°C unless other wise not s s (Note 1a) (Note 1b) ure Range		FDN357N 30 ±20 1.9 10 0.5 0.46	Units V V V A W W

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Symbol	Parameter	Conditions	Min	Тур	Max	Units
OFF CHAR	ACTERISTICS					•
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 V, I_{D} = 250 \mu A$	30			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient	$I_{D}$ = 250 µA, Referenced to 25 °C		36		mV/ °C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{\rm DS} = 24  \text{V},   \text{V}_{\rm GS} = 0  \text{V}$			1	μA
		$T_{J} = 55^{\circ}C$	;		10	μA
I <sub>GSSF</sub>	Gate - Body Leakage, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate - Body Leakage, Reverse	$V_{gs} = -20 \text{ V}, \text{ V}_{Ds} = 0 \text{ V}$			-100	nA
ON CHARA	CTERISTICS (Note)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{\rm DS} = V_{\rm GS}, \ I_{\rm D} = 250 \ \mu {\rm A}$	1	1.6	2	V
$\Delta V_{GS(th)} / \Delta T_J$	Gate Threshold Voltage Temp. Coefficient	$I_{D}$ = 250 µA, Referenced to 25 °C		-3.6		mV/ °C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS} = 4.5 \text{ V}, I_{D} = 1.9 \text{ A}$		0.081	0.09	Ω
		T <sub>J</sub> =125°	C	0.11	0.14	
		$V_{GS} = 10 \text{ V}, I_{D} = 2.2 \text{ A}$		0.053	0.06	
I <sub>D(ON)</sub>	On-State Drain Current	$V_{GS} = 4.5 V, V_{DS} = 5 V$	5			Α
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 5 V, I_{D} = 1.9 A$		5		S
DYNAMIC (	CHARACTERISTICS					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz		235		pF
C <sub>oss</sub>	Output Capacitance			145		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			50		pF
SWITCHING	CHARACTERISTICS (Note)					
t <sub>D(on)</sub>	Turn - On Delay Time	$V_{DD} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ A},$ $V_{GS} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		5	10	ns
t,	Turn - On Rise Time			12	22	ns
t <sub>D(off)</sub>	Turn - Off Delay Time			12	22	ns
t,	Turn - Off Fall Time			3	8	ns
Q <sub>g</sub>	Total Gate Charge	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1.9 \text{ A},$ $V_{GS} = 5 \text{ V}$		4.2	5.9	nC
Q <sub>gs</sub>	Gate-Source Charge			1.3		nC
$Q_{gd}$	Gate-Drain Charge			1.7		nC
DRAIN-SO	JRCE DIODE CHARACTERISTICS AND M	AXIMUM RATINGS		T		
l <sub>s</sub>	Maximum Continuous Drain-Source Diode For	rward Current			0.42	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	Forward Voltage $V_{GS} = 0 \text{ V}, I_{S} = 0.42 \text{ A} (Note)$		0.71	1.2	V

1. R<sub>BM</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>BUC</sub> is guaranteed by design while R<sub>BCA</sub> is determined by the user's board design.

Typical  $R_{_{\theta,h}}$  using the board layouts shown below on 4.5"x5" FR-4 PCB in a still air environment :

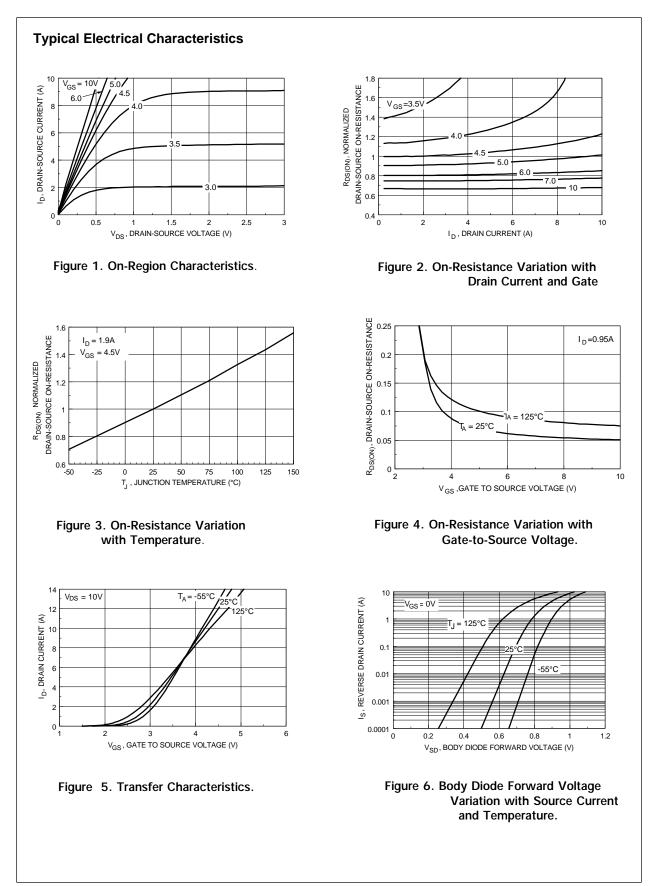


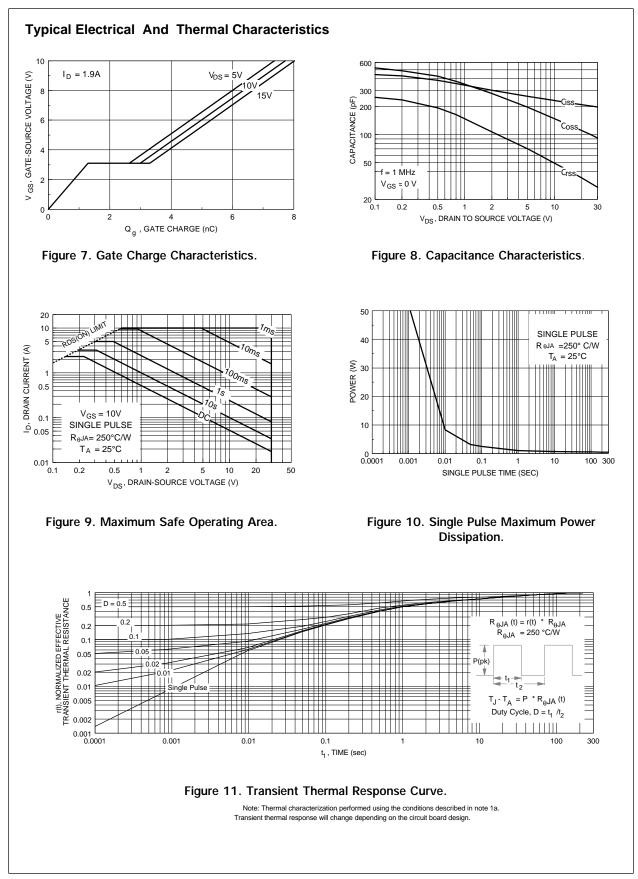
1 75

a. 250°C/W when mounted on a 0.02 in<sup>2</sup> pad of 2oz Cu.  b. 270°C/W when mounted on a 0.001 in<sup>2</sup> pad of 2oz Cu.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2.0%.





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