

DC Brushless Fan Motor Driver 5V Single-phase Full-wave Fan Motor Driver

BU6904GF

General Description

The BU6904GF is a 5V single-phase full-wave FAN motor driver with built in HALL element. This is a DC brushless FAN motor driver series.

That has compact package, auto gain control function (Henceforth, abbreviated to AGC), silent drive by soft switching, and saving motor start up current by soft start function.

This is the best lineup for note PC cooling FANs.

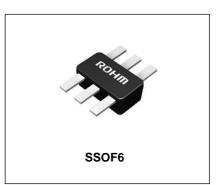
Features

- Built in HALL element.
- A GC function.
- Soft switching drive (PWM type).
- Soft start function
- Incorporating lock protection and automatic restart circuit.
- Compact package (GF:SSOF6 flat lead package) When SSOF6 is drop mounting, PKG thickness is 0.3mm.
- Rotating speed pulse signal (FG) output
- Vcc speed control (1.8V to 5.5V).

Package

SSOF6

W(Typ.) x D(Typ.) x H(Max.) 2.90mm x 3.80mm x 1.15mm



Appl	lications

For compact 5V FAN such as notebook PC cooling FAN

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Supply voltage	Vcc	7	V
Power dissipation	Pd	540*	mW
Operating temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +125	°C
Output voltage	Vomax	7	V
Output current	lomax	800**	mA
FG signal output voltage	Vfg	7	V
FG signal output current	lfg	10	mA
Junction temperature	Tjmax	125	C°

Reduce by 5.4mW/°C over 25°C. (70.0mm×70.0mm×1.6mm glass epoxy board)

** This value is not to exceed Pd.

Operating Ratings

TSZ22111 • 14 • 001

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Parameter	Symbol	Limit	Unit
Operating supply voltage range	Vcc	1.8 to 5.5	V

RoHS

Block Diagram

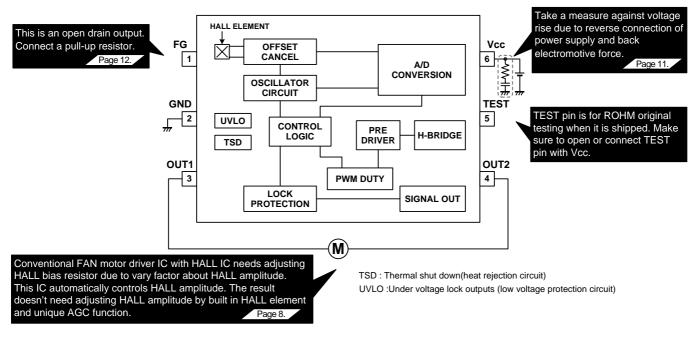


Figure 1. Block diagram and application circuit

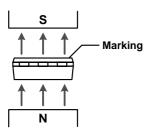
Pin Description(s)

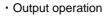
P/No.	T/Name	Function
1	FG	FG signal output terminal
2	GND	GND terminal
3	OUT1	Motor output terminal1
4	OUT2	Motor output terminal2
5	TEST	TEST terminal*
6 Vcc		Power supply terminal

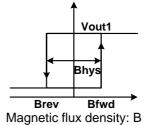
* TEST pin is for ROHM original testing when it is shipped. Make sure to open or connect TEST pin with Vcc.

I/O truth table

· Supply magnetic direction (positive)







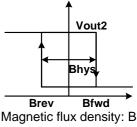


Figure 2. Output operation

Supply magnetic direction	OUT1	OUT2	FG
S	S H		L (Output Tr : ON)
N	L	Н	H (Output Tr : OFF)

● Electrical Characteristics (Unless otherwise specified Ta=25°C, Vcc=5V)

Deverseter	Symbol	Limit		11-14			
Parameter		MIN	TYP	MAX	Unit	Conditions	Characteristics
Circuit current	lcc	-	2	4	mA		Figure 3.
Magnetic switch-point for forward rotation	Bfwd	-	1.5	-	mT		Figure 4.
Magnetic switch-point for reverse rotation	Brev	-	-1.5	-	mT		Figure 5.
Magnetic hysteresis	Bhys	-	3.0	5.0	mT		Figure 6.
Output voltage	Vo	-	0.16	0.24	V	Io=200mA Upper and Lower total	Figure 7 to 12.
FG low voltage	Vfgl	-	-	0.4	V	lfg=5mA	Figure 13,14.
FG leak current	lfgl	-	-	5	μA	Vfg=7V	Figure 15.
Lock detection ON time	Ton	0.35	0.50	0.65	S		Figure 16.
Lock detection OFF time	Toff	3.5	5.0	6.5	S		Figure 17.
The maximum time of soft start 50% DUTY	T50	114	164	214	ms		

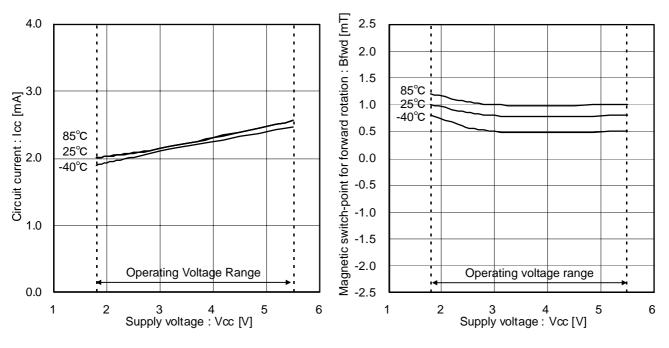


Figure 3. Circuit current 1

Figure 4. Magnetic switch-point for forward rotation

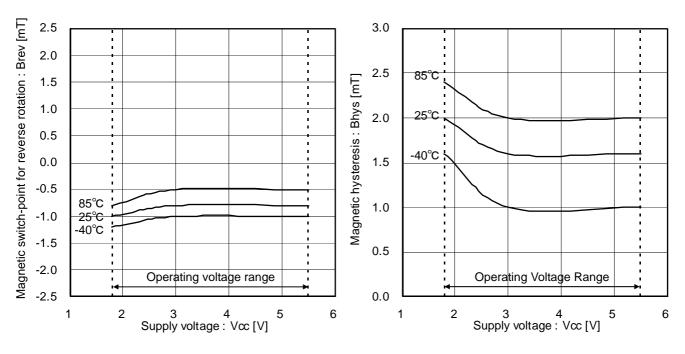
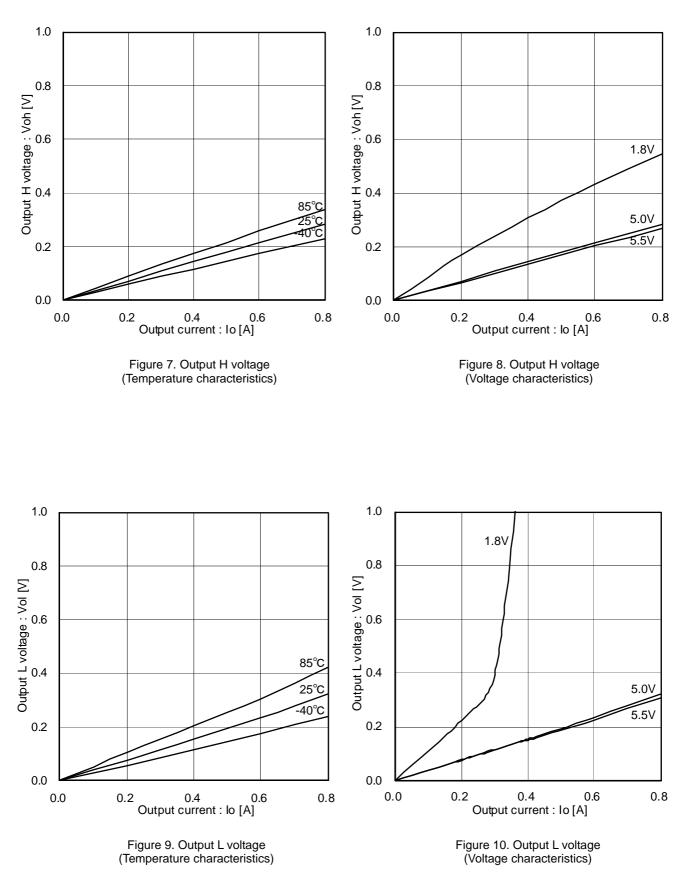


Figure 5. Magnetic switch-point for reverse rotation

Figure 6. Magnetic hysteresis



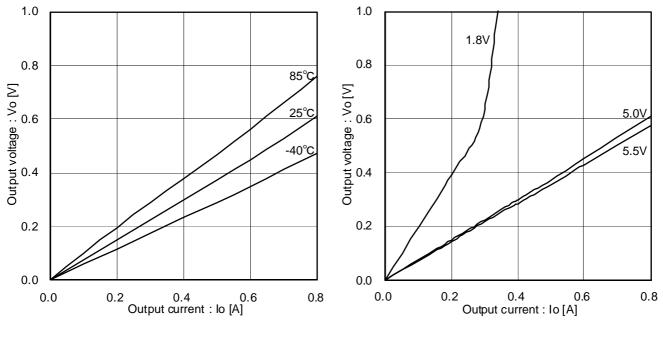
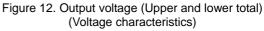
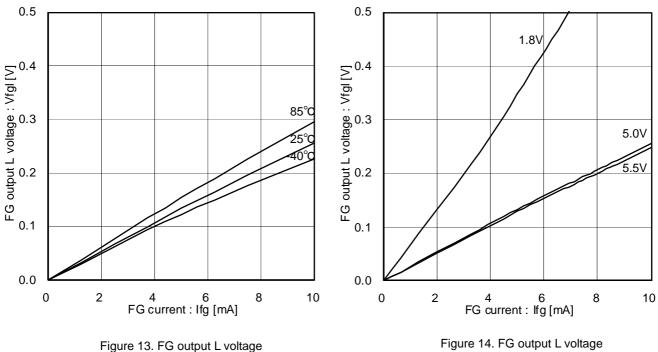


Figure 11. Output voltage (Upper and lower total) (Temperature characteristics)





(Temperature characteristics)

Figure 14. FG output L voltage (Voltage characteristics)

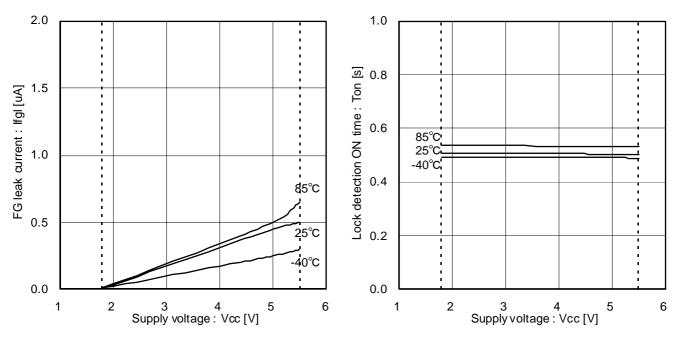


Figure 15. FG Output leak current

Figure 16. Lock detection ON time

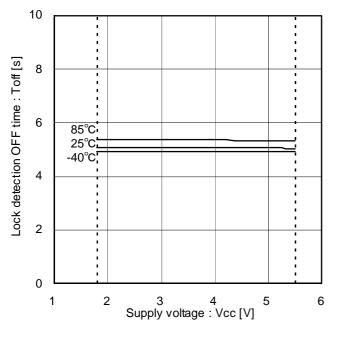


Figure 17. Lock detection OFF time

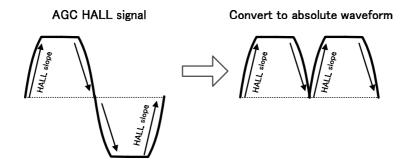
Auto gain control

Conventional FAN motor driver IC with HALL IC needs adjusting HALL bias resistor due to vary factor about HALL amplitude. This IC automatically controls HALL amplitude generated by built in HALL element and motor magnet. The result doesn't need adjusting HALL amplitude by built in HALL element and unique AGC function. AGC function needs selecting 30ms time for HALL amp gain when turning on the power, recovering from lock protection.

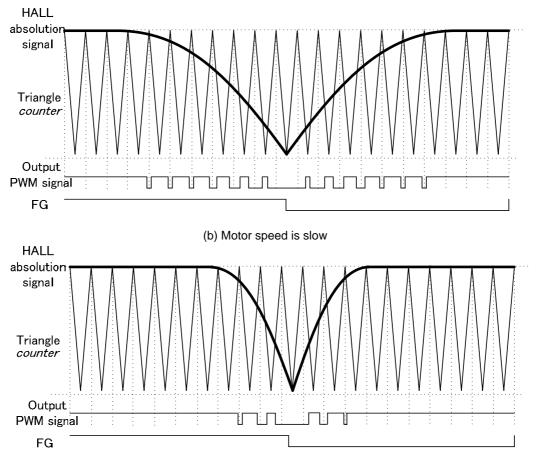
•Soft switching (PWM type)

Soft switching is operated using output PWM switching. The output PWM signal is generated by the slope of AGC HALL signal. The first, the AGC HALL signal is converted to absolute waveform. Next, synthesize the absolute waveform and the triangular waveform by inside IC generated. The synthesized waveform has determined PWM soft switching duty and the ratio of time.

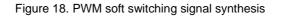
PWM soft switching time depends on motor speed. In case of HALL signal a slower, PWM soft switching time is long due to the obtuse angle of the AGC HALL signal. (PWM soft switching time is about 2ms to 4ms.) The other, in case of HALL signal a faster, PWM soft switching time is short due to the sharp slope of the AGC HALL signal. (PWM soft switching time is about 200µs to 1ms.) And, PWM soft switching frequency is 50 kHz (typical) for making triangle oscillator IC inside.



(a) Convert to absolute waveform from the AGC HALL signal

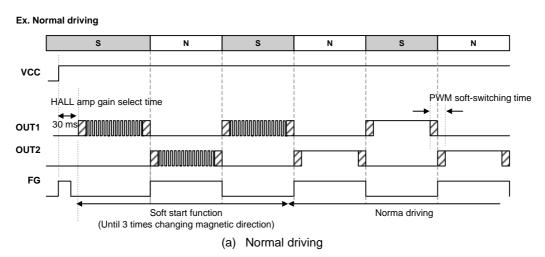


(c) Motor speed is fast



Soft start function

The soft start function can save output current for motor start-up. When motor starts up from stop condition, outputs are driven in about PWM 50% duty until 3 times changing magnetic direction. If the motor can't start up using soft start function at low Vcc voltage or insufficiency motor torque, it is automatically avoided to changing full torque (Output duty 100%). (Refer to the maximum time of soft start 50% DUTY : 164ms (typical) in electrical characteristic). Figure 19. shows soft start timing chart at normal driving and insufficiency motor torque driving.



Ex. Insufficiency motor torque driving

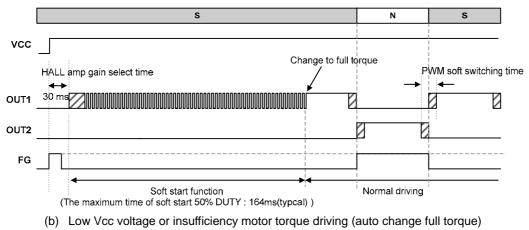
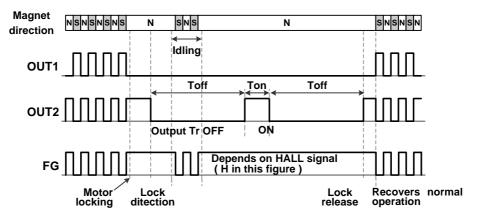


Figure 19. Soft start function

●Lock protection and automatic restart

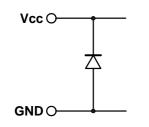
Motor rotation is detected by HALL signal, and lock detection ON time (Ton) and lock detection OFF time (Toff) are set by IC internal counter. External part (C or R) is not required. Timing chart is shown in Figure 20.



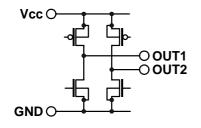


●Equivalent circuit

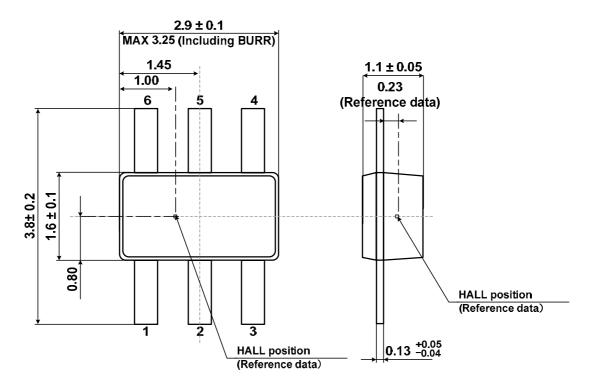
1) Supply voltage terminal



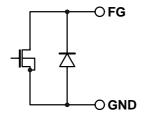
3) Motor output terminal



HALL position (Reference data)



2) FG output terminal



Safety measure

1) Reverse connection protection diode

Reverse connection of power results in IC destruction as shown in Figure 21. When reverse connection is possible, reverse connection protection diode must be added between power supply and Vcc.

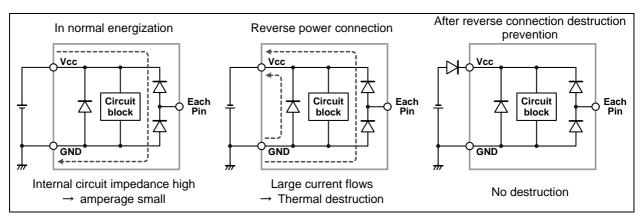


Figure 21. Flow of current when power is connected reversely

- 2) Measure against Vcc voltage rise by back electromotive force
 - Back electromotive force (Back EMF) generates regenerative current to power supply. However, when reverse connection protection diode is connected, Vcc voltage rises because the diode prevents current flow to power supply.

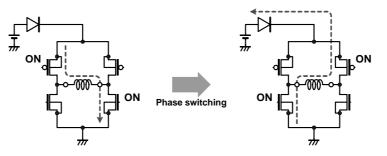
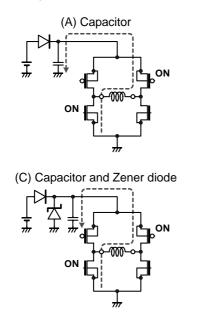
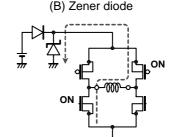


Figure 22. Vcc voltage rise by back electromotive force

When the absolute maximum rated voltage may be exceeded due to voltage rise by back electromotive force, place (A) Capacitor or (B) Zener diode between Vcc and GND. If necessary, add both (C). (D) Capacitor and resister are improved ESD surge destruction.





(D) Capacitor and resister

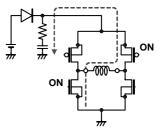


Figure 23. Measure against Vcc voltage rise

3) Problem of GND line PWM switching

Do not perform PWM switching of GND line because GND terminal potential cannot be kept to a minimum.

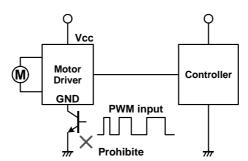


Figure 24. GND Line PWM switching prohibited

4) FG output

FG output is an open collector and requires pull-up resistor. The IC can be protected by adding resistor R1. An excess of absolute maximum rating, when FG output terminal is directly connected to power supply, could damage the IC.

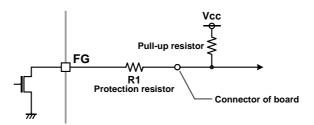
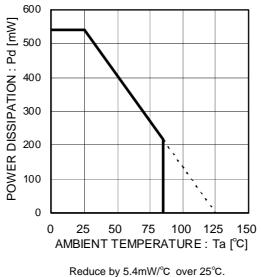


Figure 25. Protection of FG terminal

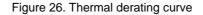
Thermal derating curve

Thermal derating curve indicates power that can be consumed by IC with reference to ambient temperature. Power that can be consumed by IC begins to attenuate at certain ambient temperature. This gradient is determined by thermal resistance θ_{ja} .

Thermal resistance θ depends on chip size, power consumption, package ambient temperature, packaging condition, wind velocity, etc., even when the same package is used. Thermal derating curve indicates a reference value measured at a specified condition. Figure 26. shows a thermal derating curve.



(70.0mm × 70.0mm × 1.6mm FR4 glass epoxy board)



Operational Notes

1) Absolute maximum ratings

Devices may be destroyed when supply voltage or operating temperature exceeds the absolute maximum ratings.
Because the cause of this damage cannot be identified as a short circuit or an open circuit, if any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.
2) Connecting the power supply connector backward

2) Connecting the power supply connector backward Connecting of the power supply in reverse polarity can damage IC. Take precautions when connecting the power supply lines. An external direction diode can be added.

3) Power supply line

Back electromotive force causes regenerated current to power supply line, therefore take a measure such as placing a capacitor between power supply and GND for routing regenerated current. And fully ensure that the capacitor characteristics have no problem before determine a capacitor value. (when applying electrolytic capacitors, capacitance characteristic values are reduced at low temperatures)

4) GND potential

It is possible that the motor output terminal may deflect below GND terminal because of influence by back electromotive force of motor. The potential of GND terminal must be minimum potential in all operating conditions, except that the levels of the motor outputs terminals are under GND level by the back electromotive force of the motor coil. Also ensure that all terminals except GND and motor output terminals do not fall below GND voltage including transient characteristics. Malfunction may possibly occur depending on use condition, environment, and property of individual motor. Please make fully confirmation that no problem is found on operation of IC.

- Thermal design Use a thermal design that allows for a sufficient margin in light of the power dissipation(Pd) in actual operating conditions.
- 6) Inter-pin shorts and mounting errors Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error or if pins are shorted together.
- Actions in strong electromagnetic field Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.
- 8) ASO

When using the IC, set the output transistor so that it does not exceed absolute maximum rations or ASO. 9) Thermal shut down circuit

The IC incorporates a built-in thermal shutdown circuit (TSD circuit). Operation temperature is 150°C(typ.) and has a hysteresis width of 25°C(typ.). When IC chip temperature rises and TSD circuit works, the output terminal becomes an open state. TSD circuit is designed only to shut the IC off to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operation this circuit or use the IC in an environment where the operation of this circuit is assumed.

10) Testing on application boards

When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.

11) GND wiring pattern

When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.

- 12) Capacitor between output and GND When a large capacitor is connected between output and GND, if Vcc is shorted with 0V or GND for some cause, it is possible that the current charged in the capacitor may flow into the output resulting in destruction. Keep the capacitor between output and GND below 100uF.
- 13) IC terminal input

When Vcc voltage is not applied to IC, do not apply voltage to each input terminal. When voltage above Vcc or below GND is applied to the input terminal, parasitic element is actuated due to the structure of IC. Operation of parasitic element causes mutual interference between circuits, resulting in malfunction as well as destruction in the last. Do not use in a manner where parasitic element is actuated.

14) In use

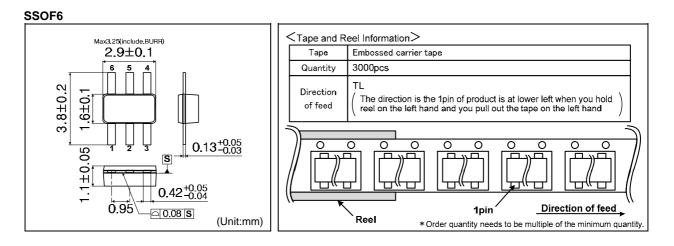
We are sure that the example of application circuit is preferable, but please check the character further more in application to a part which requires high precision. In using the unit with external circuit constant changed, consider the variation of externally equipped parts and our IC including not only static character but also transient character and allow sufficient margin in determining.

Status of this document

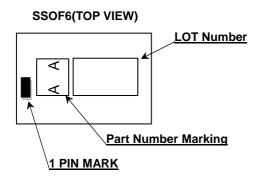
The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority

Physical Dimension Tape and Reel Information



Marking Diagram(s)(TOP VIEW)



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CLASSⅢ		CLASS II b	
CLASSⅣ	CLASSⅢ	CLASSⅢ	CLASSII

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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For details, please refer to ROHM Mounting specification

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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