



INA2132

Dual, Low Power, Single-Supply DIFFERENCE AMPLIFIER

FEATURES

- DESIGNED FOR LOW COST
- LOW QUIESCENT CURRENT:
160 μ A per Amplifier
- WIDE POWER SUPPLY RANGE:
Single Supply: 2.7V to 36V
Dual Supplies: ± 1.35 V to ± 18 V
- LOW GAIN ERROR: $\pm 0.05\%$ max
- LOW NONLINEARITY: 0.001% max
- HIGH CMRR: 90dB
- HIGHLY VERSATILE CIRCUIT
- EASY TO USE
- SO-14 PACKAGE

APPLICATIONS

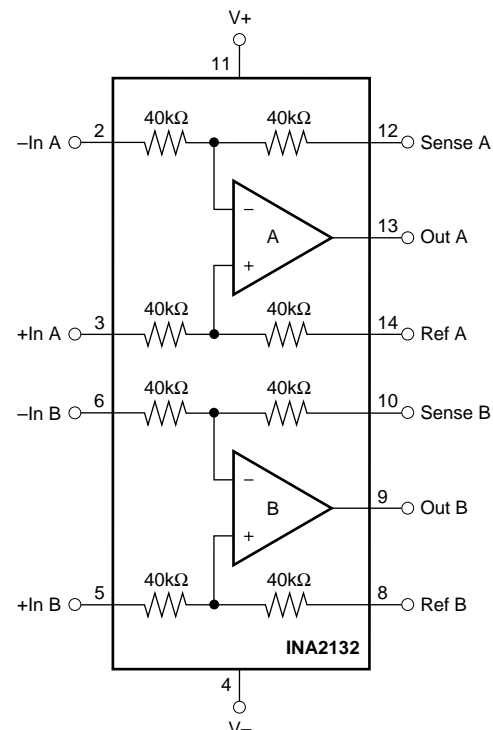
- DIFFERENTIAL INPUT AMPLIFIER
- INSTRUMENTATION AMPLIFIER
BUILDING BLOCK
- UNITY-GAIN INVERTING AMPLIFIER
- G = 1/2 AMPLIFIER
- G = 2 AMPLIFIER
- SUMMING AMPLIFIER
- DIFFERENTIAL CURRENT RECEIVER
- VOLTAGE-CONTROLLED CURRENT SOURCE
- BATTERY-POWERED SYSTEMS
- GROUND LOOP ELIMINATOR

DESCRIPTION

The INA2132 is a dual low power, unity-gain difference amplifier offering excellent value at very low cost. Each channel consists of a precision op amp with a laser-trimmed precision resistor network, providing accurate gain and high common-mode rejection. Excellent TCR tracking of the resistors maintains gain accuracy and common-mode rejection over temperature. The internal op amp's common-mode range extends to the negative supply—ideal for single-supply applications.

The difference amplifier is the foundation of many commonly used circuits. The INA2132 provides this circuit function without using an expensive precision resistor network. The INA2132 is available in the SO-14 surface-mount package and is specified for operation over the extended industrial temperature range, -40°C to $+85^{\circ}\text{C}$.

A single version of this product with similar specifications is also available. See the INA132 data sheet for details.



SPECIFICATIONS: $V_S = \pm 15V$

At $T_A = +25^\circ C$, $R_L = 10k\Omega$ connected to ground, and reference pins connected to ground unless otherwise noted.

PARAMETER	CONDITIONS	INA2132U			INA2132UA			UNITS
		MIN	TYP	MAX	MIN	TYP	MAX	
OFFSET VOLTAGE⁽¹⁾	RTO							
Initial V_{OS}			± 75	± 250		*	± 500	μV
vs Temperature dV_{OS}/dT			± 1	± 5		*	± 10	$\mu V/^\circ C$
vs Power Supply PSRR	$V_S = \pm 1.35V$ to $\pm 18V$		± 5	± 30		*	*	$\mu V/V$
vs Time			0.3			*		$\mu V/mo$
Channel Separation ⁽²⁾	dc		0.04			*		$\mu V/V$
INPUT IMPEDANCE⁽³⁾								
Differential			80			*		$k\Omega$
Common-Mode			40			*		$k\Omega$
INPUT VOLTAGE RANGE								
Common-Mode Voltage Range ⁽⁴⁾	$V_O = 0V$	2 (V-)		2 (V+) -2	*		*	V
Common-Mode Rejection Ratio CMRR	$V_{CM} = -30V$ to $28V$, $R_S = 0\Omega$	80	90		74	*		dB
OUTPUT VOLTAGE NOISE⁽⁵⁾	RTO							
$f = 0.1Hz$ to $10Hz$			1.6			*		$\mu Vp-p$
$f = 1kHz$			65			*		nV/\sqrt{Hz}
GAIN								
Initial			1			*		V/V
Error	$V_O = -14V$ to $13.5V$		± 0.01	± 0.05		*	± 0.1	%
vs Temperature			± 1	± 10		*	*	$ppm/^\circ C$
Nonlinearity	$V_O = -14V$ to $13.5V$		± 0.0001	± 0.001		*	± 0.002	% of FS
OUTPUT								
Voltage, Positive	$R_L = 100k\Omega$ to Ground	(V+) -1	(V+) -0.8		*	*		V
Negative	$R_L = 100k\Omega$ to Ground	(V-) +0.5	(V-) +0.15		*	*		V
Positive	$R_L = 10k\Omega$ to Ground	(V+) -1.5	(V+) -0.8		*	*		V
Negative	$R_L = 10k\Omega$ to Ground	(V-) +1	(V-) +0.25		*	*		V
Current Limit, per Amplifier	Continuous to Common		± 12			*		mA
Capacitive Load (stable operation)			10			*		nF
FREQUENCY RESPONSE								
Small-Signal Bandwidth	-3dB		300			*		kHz
Slew Rate	SR		0.1			*		V/ μs
Settling Time: 0.1%	$V_O = 10V$ Step		85			*		μs
0.01%	$V_O = 10V$ Step		88			*		μs
Overload Recovery Time	50% Overdrive		7			*		μs
POWER SUPPLY								
Rated Voltage V_S			± 15			*		V
Voltage Range		± 1.35		± 18	*		*	V
Quiescent Current (per amplifier) I_Q	$I_O = 0mA$		± 160	± 185		*	*	μA
TEMPERATURE RANGE								
Specification		-40		+85	*		*	$^\circ C$
Operation		-55		+125	*		*	$^\circ C$
Storage		-55		+125	*		*	$^\circ C$
Thermal Resistance θ_{JA}			100			*		$^\circ C/W$

* Specifications the same as INA2132U.

NOTES: (1) Includes effects of amplifier's input bias and offset currents. (2) Measured output offset change of one channel for a full-scale swing ($V_O = -14V$ to $13.5V$) on the opposite channel. (3) $40k\Omega$ resistors are ratio matched but have $\pm 20\%$ absolute value. (4) $2 (V-) -V_{REF} < V_{CM} < 2 ((V+) -1) -V_{REF}$. For more detail, see Applications Information section. (5) Includes effects of amplifier's input current noise and thermal noise contribution of resistor network.

The information provided herein is believed to be reliable; however, BURR-BROWN assumes no responsibility for inaccuracies or omissions. BURR-BROWN assumes no responsibility for the use of this information, and all use of such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. BURR-BROWN does not authorize or warrant any BURR-BROWN product for use in life support devices and/or systems.

SPECIFICATIONS: $V_S = +5V$ Single Supply

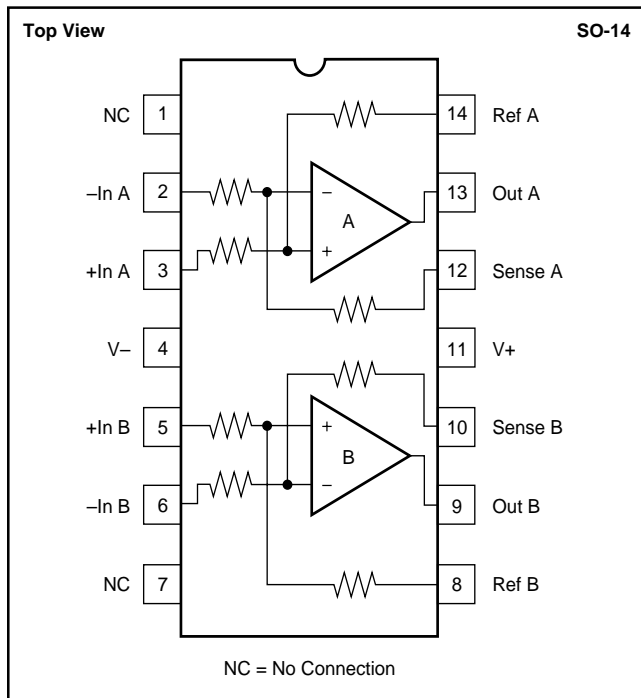
At $T_A = +25^\circ C$, $R_L = 10k\Omega$ connected to $V_S/2$, and reference pin connected to $V_S/2$, unless otherwise noted.

PARAMETER	CONDITIONS	INA2132U			INA2132UA			UNITS
		MIN	TYP	MAX	MIN	TYP	MAX	
OFFSET VOLTAGE⁽¹⁾	RTO							
Initial V_{OS} vs Temperature dV_{OS}/dT			± 150 ± 2	± 500		*	± 750	μV $\mu V/^\circ C$
INPUT VOLTAGE RANGE								
Common-Mode Voltage Range ⁽²⁾ Common-Mode Rejection CMRR	$V_{CM} = -2.5V$ to $+5.5V$, $R_S = 0\Omega$	-2.5 80	90	+5.5	*	*	*	V dB
OUTPUT								
Voltage, Positive Negative Positive Negative	$R_L = 100k\Omega$ to Ground $R_L = 100k\Omega$ to Ground $R_L = 10k\Omega$ to Ground $R_L = 10k\Omega$ to Ground	(V+) -1 +0.25 (V+) -1 +0.25	(V+) -0.75 +0.06 (V+) -0.8 +0.12		*	*	*	V V V V
POWER SUPPLY								
Rated Voltage V_S Voltage Range Quiescent Current I_Q	$I_O = 0mA$	+2.7	+5 ± 155	+36 ± 185	*	*	*	V V μA

* Specifications the same as INA2132U.

NOTE: (1) Includes effects of amplifier's input bias and offset currents. (2) $2(V-) - V_{REF} < V_{CM} < 2(V+) - V_{REF}$. For more detail, see Applications Information section.

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS

Supply Voltage, $V+$ to $V-$	36V
Input Voltage Range	$\pm 80V$
Output Short-Circuit (to ground)	Continuous
Operating Temperature	$-55^\circ C$ to $+125^\circ C$
Storage Temperature	$-55^\circ C$ to $+125^\circ C$
Junction Temperature	$+150^\circ C$
Lead Temperature (soldering, 10s)	$+300^\circ C$



ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

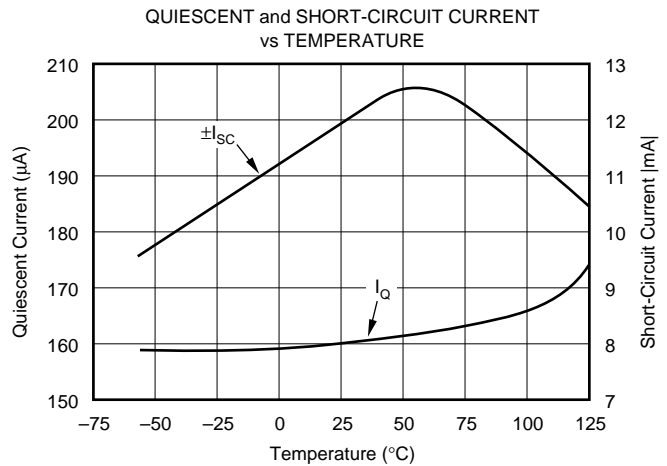
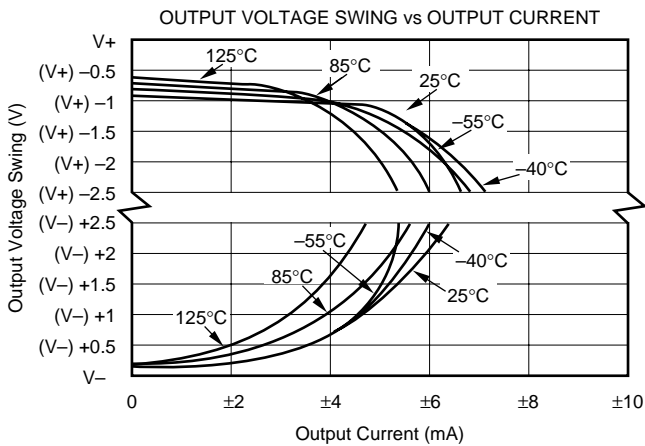
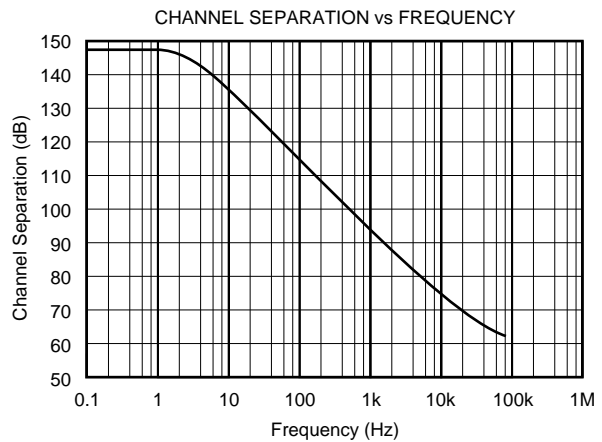
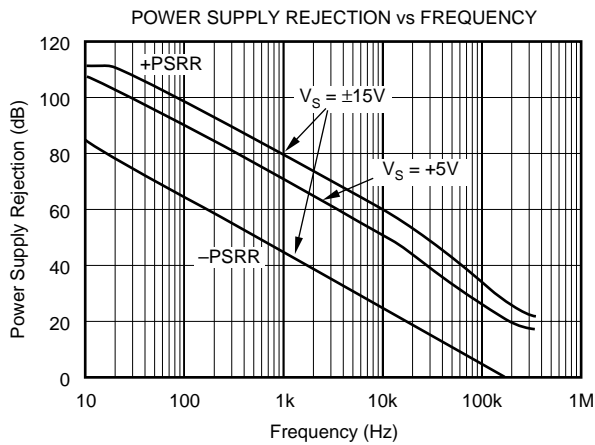
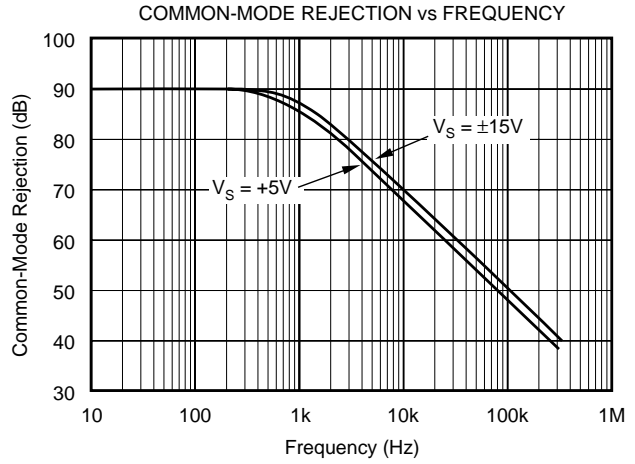
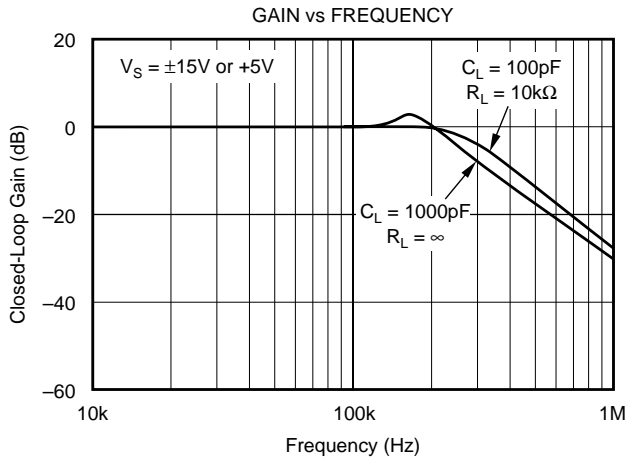
PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER ⁽¹⁾	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER ⁽²⁾	TRANSPORT MEDIA
INA2132U	SO-14 Surface-Mount	235	$-40^\circ C$ to $+85^\circ C$	INA2132U	INA2132U	Rails
"	"	"	"	"	INA2132U/2K5	Tape and Reel
INA2132UA	SO-14 Surface-Mount	235	$-40^\circ C$ to $+85^\circ C$	INA2132UA	INA2132UA	Rails
"	"	"	"	"	INA2132UA/2K5	Tape and Reel

NOTES: (1) For detailed drawing and dimension table, please see end of data sheet, or Appendix C of Burr-Brown IC Data Book. (2) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /2K5 indicates 2500 devices per reel). Ordering 2500 pieces of "INA2132U/2K5" will get a single 2500-piece Tape and Reel. For detailed Tape and Reel mechanical information, refer to Appendix B of Burr-Brown IC Data Book.

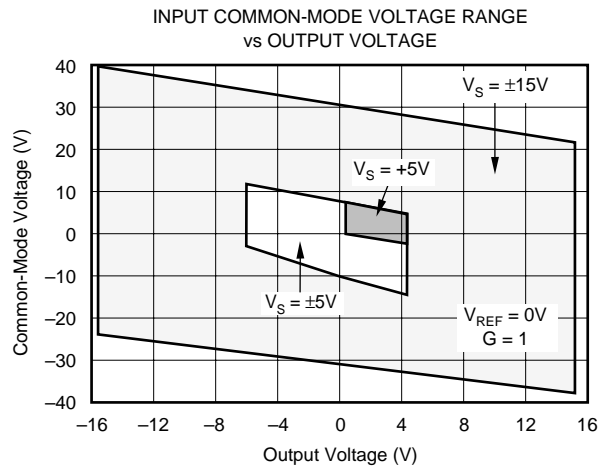
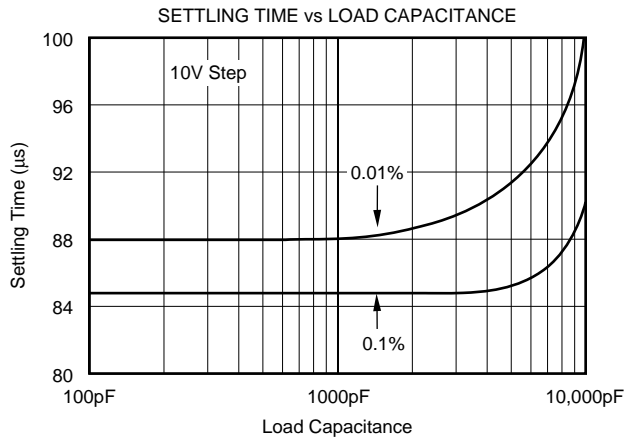
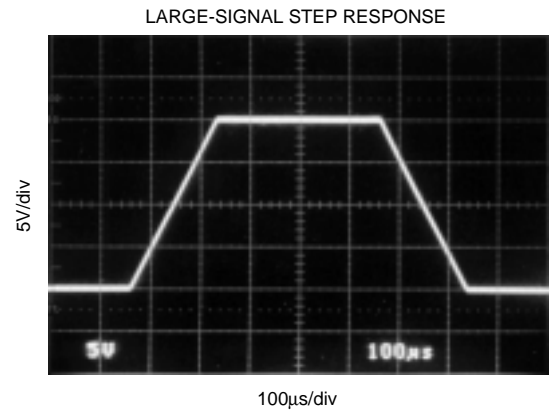
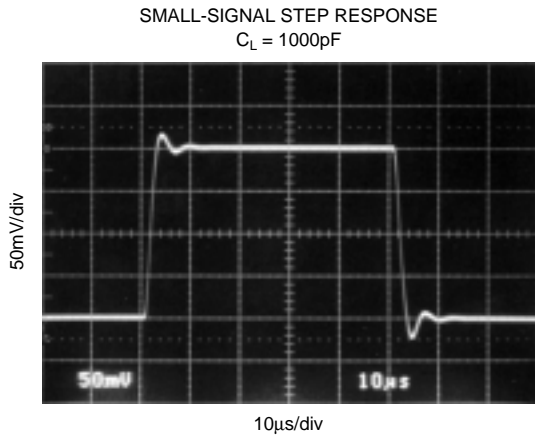
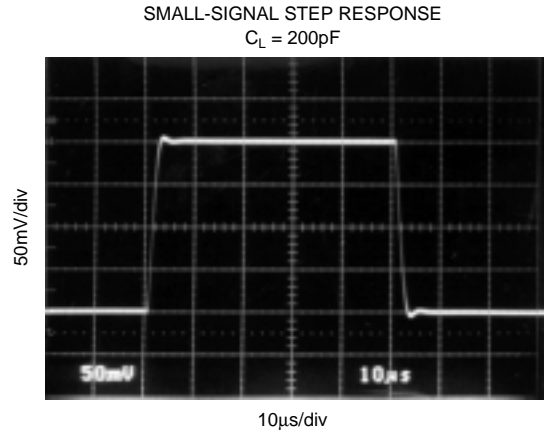
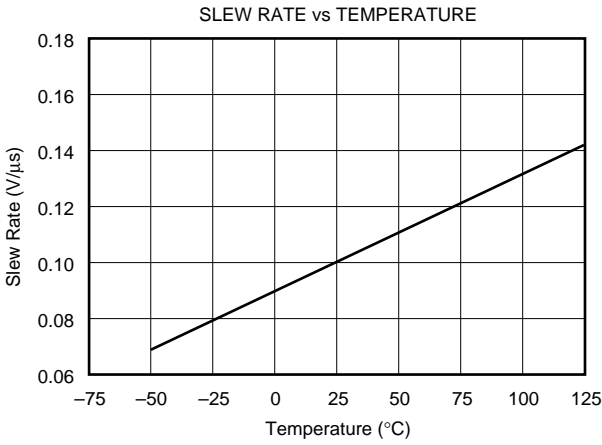
TYPICAL PERFORMANCE CURVES

At $T_A = +25^\circ\text{C}$ and $V_S = \pm 15\text{V}$, unless otherwise noted.



TYPICAL PERFORMANCE CURVES (CONT)

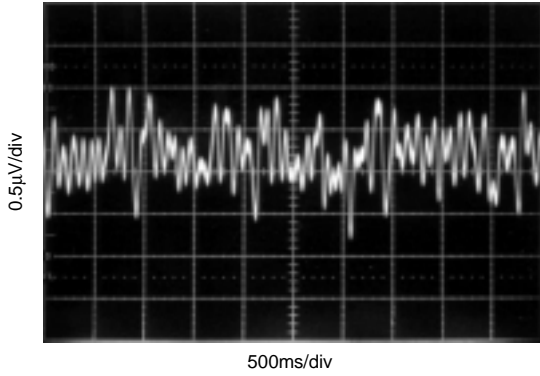
At $T_A = +25^\circ\text{C}$ and $V_S = \pm 15\text{V}$, unless otherwise noted.



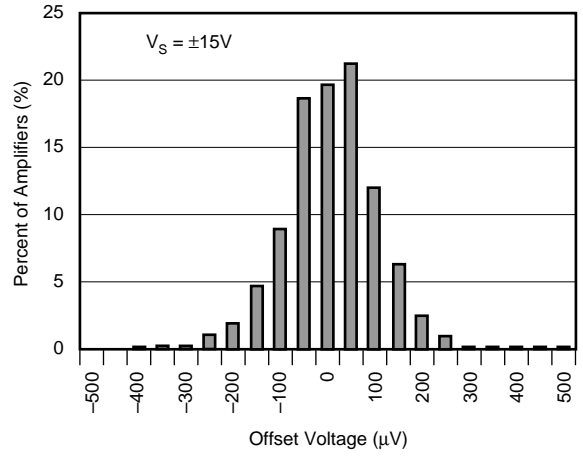
TYPICAL PERFORMANCE CURVES (CONT)

At $T_A = +25^\circ\text{C}$ and $V_S = \pm 15\text{V}$, unless otherwise noted.

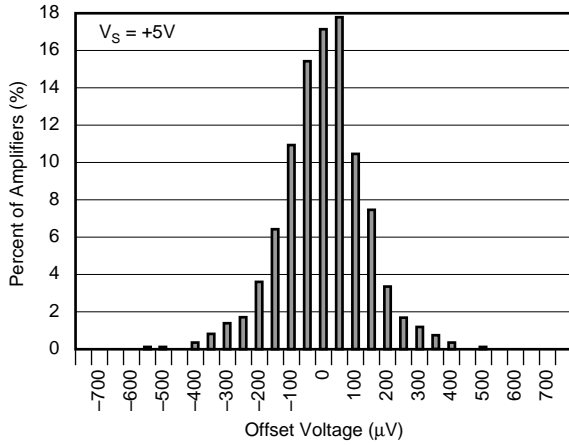
0.1Hz to 10Hz PEAK-TO-PEAK
VOLTAGE NOISE



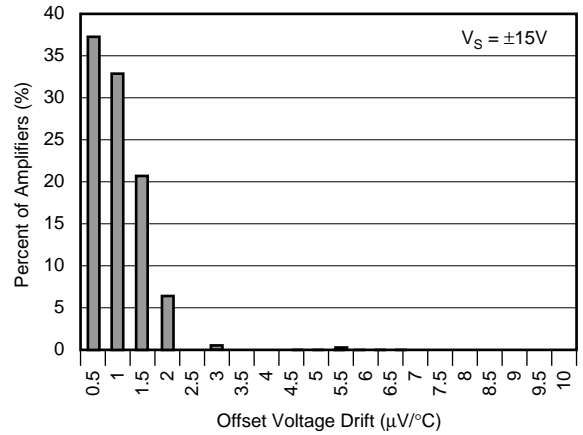
OFFSET VOLTAGE
PRODUCTION DISTRIBUTION



OFFSET VOLTAGE
PRODUCTION DISTRIBUTION



OFFSET VOLTAGE DRIFT
PRODUCTION DISTRIBUTION



APPLICATIONS INFORMATION

Figure 1 shows the basic connections required for operation of the INA2132. Power supply bypass capacitors should be connected close to the device pins.

The differential input signal is connected to pins 2 and 3 (or pins 6 and 5) as shown. The source impedances connected to the inputs must be nearly equal to assure good common-mode rejection. An 8Ω mismatch in source impedance will degrade the common-mode rejection of a typical device to approximately 80dB. Gain accuracy will also be slightly affected. If the source has a known impedance mismatch, an additional resistor in series with one input can be used to preserve good common-mode rejection.

Do not interchange pins 3 and 14 (or pins 5 and 8) or pins 2 and 12 (or pins 6 and 10), even though nominal resistor values are equal. These resistors are laser-trimmed for precise resistor ratios to achieve accurate gain and highest CMRR. Interchanging these pins may not provide specified performance. As shown in Figure 1, sense line should be connected as close to the load as possible.

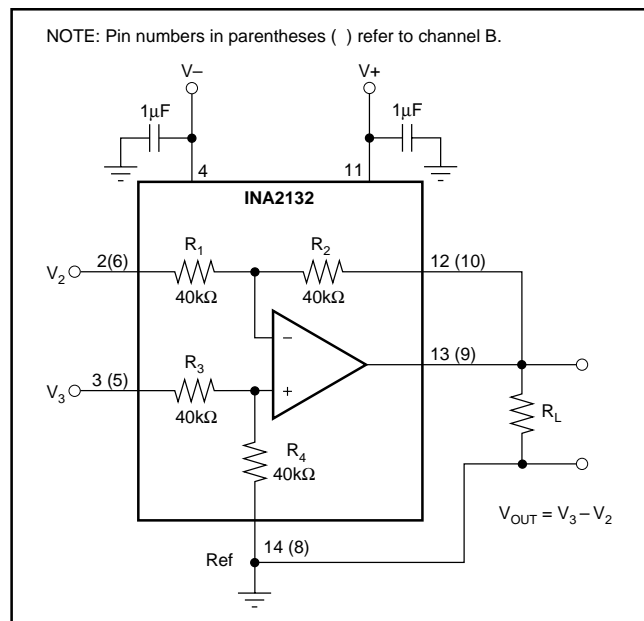


FIGURE 1. Basic Power Supply and Signal Connections.

To ensure valid operation of the differential amplifier, please note the following points:

- 1) $V_{OUT} = V_3 - V_2 + V_{REF}$
- 2) V_{OUT} must be within the specified linear range. For example, with $\pm 15V$ supplies and a $100k\Omega$ load, the output will be defined by:

$$(V-) + 0.15V < V_{OUT} < (V+) - 0.8V$$

- 3) Input common-mode range at the nodes of the op amp must be $V- \leq V_{CM} \leq (V+) - 1$. To ensure that the inputs to the differential amp (+In and -In) meet this criteria, limit the common-mode voltage inputs to:

$$2 \cdot (V-) - V_{REF} < V_{CM} < 2 \cdot ((V+) - 1) - V_{REF}$$

In the case where V_{REF} is grounded, the equation simplifies to:

$$2 \cdot (V-) < V_{CM} < 2 \cdot ((V+) - 1)$$

For more information, see the typical performance curve titled “Input Common-Mode Voltage Range vs Output Voltage.”

OPERATING VOLTAGE

The INA2132 operates from single ($+2.7V$ to $+36V$) or dual ($\pm 1.35V$ to $\pm 18V$) supplies with excellent performance. Specifications are production tested with $+5V$ and $\pm 15V$ supplies. Most behavior remains unchanged throughout the full operating voltage range. Parameters which vary significantly with operating voltage are shown in the Typical Performance Curves.

The INA2132 can accurately measure differential signals that are beyond the power supply rails. Linear common-mode range extends to twice the negative power supply voltage and nearly twice the positive power supply voltage. Output phase reversal does not occur when the inputs to the internal operational amplifier are overloaded to either rail. See typical performance curve, “Common-Mode Range vs Output Voltage.”

OFFSET VOLTAGE TRIM

The INA2132 is laser-trimmed for low offset voltage and drift. Most applications require no external offset adjustment. Figure 2 shows an optional circuit for trimming the output offset voltage. The output is referred to the output reference terminal (pin 14 or pin 8), which is normally grounded. A voltage applied to the Ref terminal will be summed with the output signal. This can be used to null offset voltage. The source impedance of a signal applied to the Ref terminal should be less than 8Ω to maintain good common-mode rejection. To assure low impedance at the Ref terminal, the trim voltage can be buffered with an op amp, such as the OPA277.

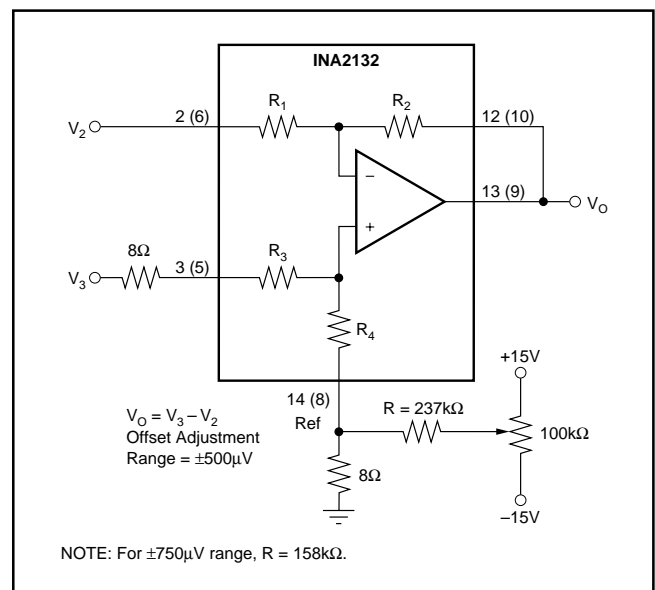


FIGURE 2. Offset Adjustment.

CAPACITIVE LOAD DRIVE CAPABILITY

The INA2132 can drive large capacitive loads, even at low supplies. It is stable with a 10nF load. Refer to the “Small-Signal Step Response” and “Settling Time vs Load Capacitance” typical performance curves.

CHANNEL CROSSTALK

The two channels of the INA2132 are completely independent, including all bias circuitry. At dc and low frequency, there is virtually no signal coupling between channels. Crosstalk increases with frequency and is dependent on source impedance and signal characteristics. See the typical performance curve “Channel Separation vs Frequency” for more information.

Most crosstalk is produced by capacitive coupling of signals from one channel to the input section of the other channel. To minimize coupling, separate the input traces as far as practical from any signals associated with the opposite channel. A grounded guard trace surrounding the inputs helps reduce stray coupling between channels. Run the differential inputs of each channel parallel to each other or

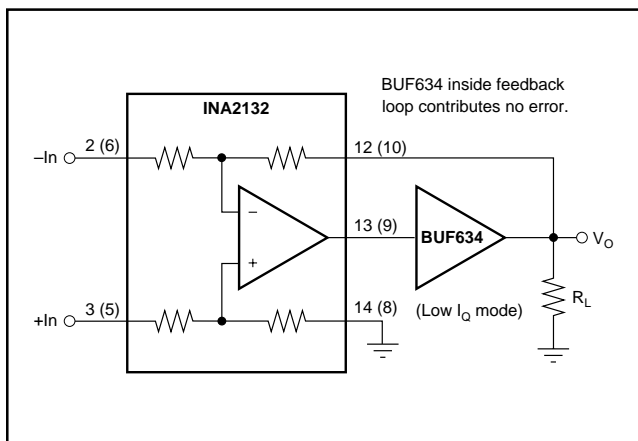


FIGURE 3. Low Power, High Output Current Precision Difference Amplifier.

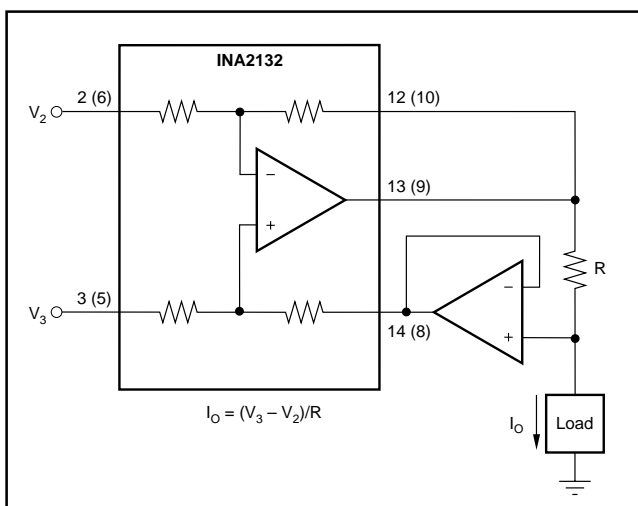


FIGURE 4. Differential Input Voltage-to-Current Converter for Low I_{OUT} .

directly adjacent on the top and bottom sides of a circuit board. Stray coupling then produces a common-mode signal which is rejected by the INA2132’s input.

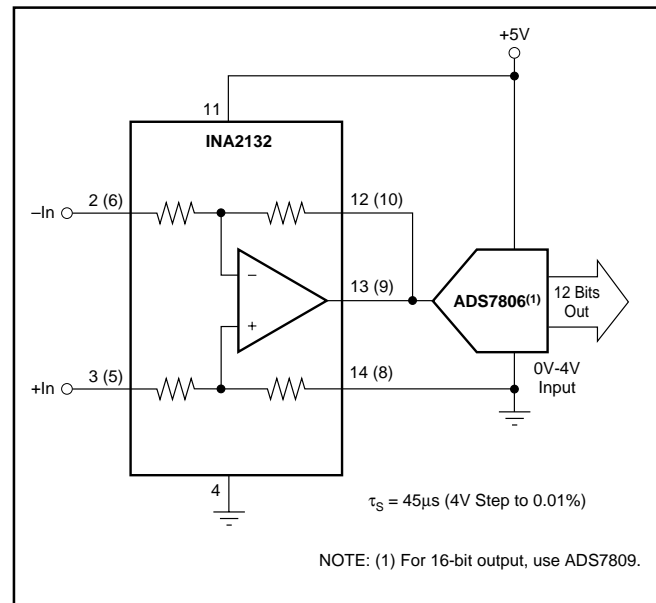


FIGURE 5. Differential Input Data Acquisition.

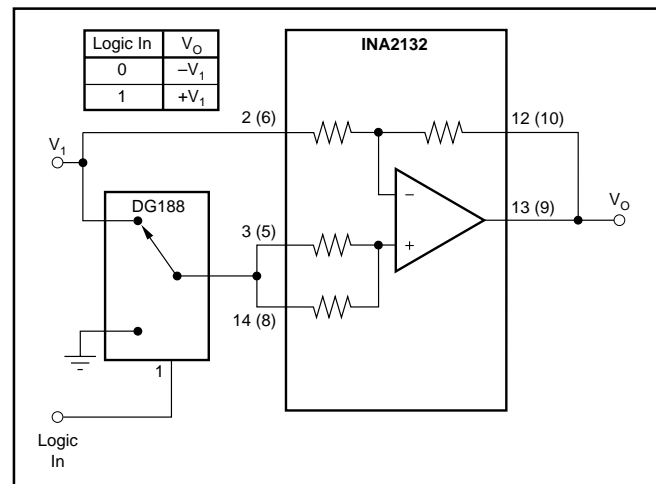


FIGURE 6. Digitally Controlled Gain of ± 1 Amplifier.

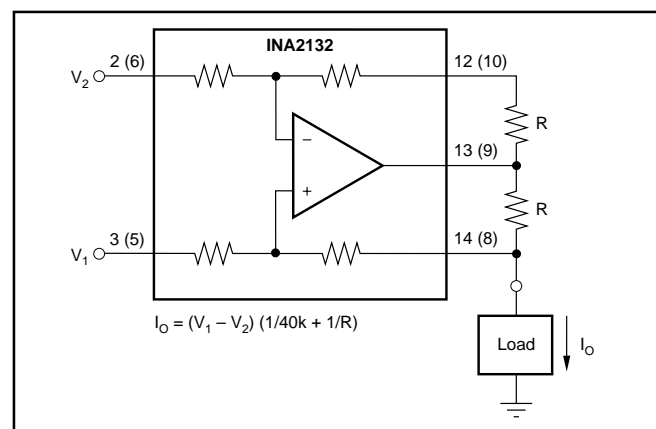


FIGURE 7. Precision Voltage-to-Current Converter with Differential Inputs.

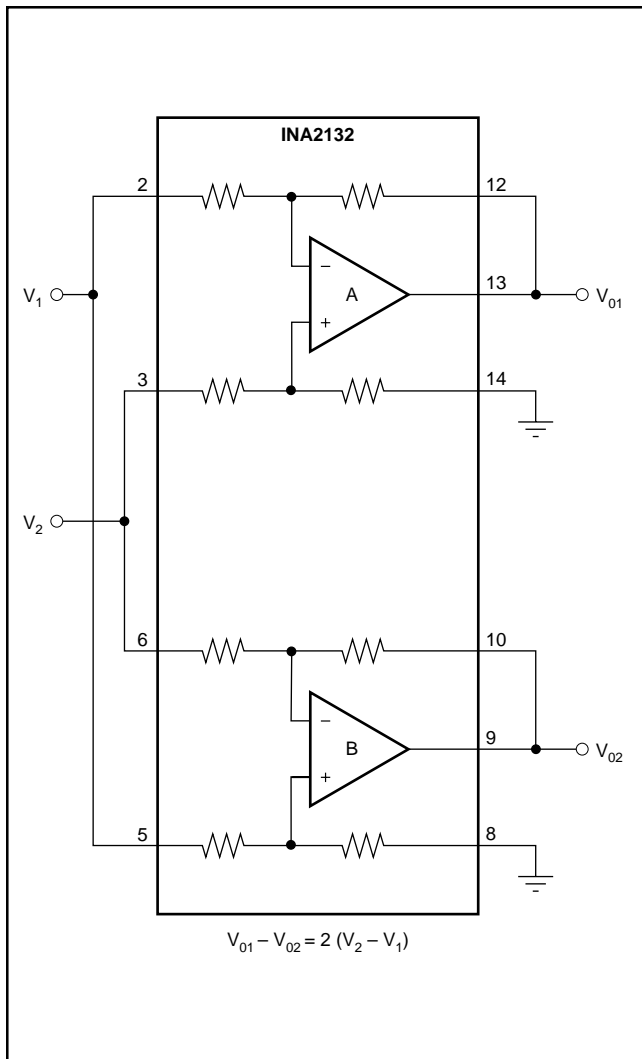


FIGURE 8. Differential Output Difference Amplifier.

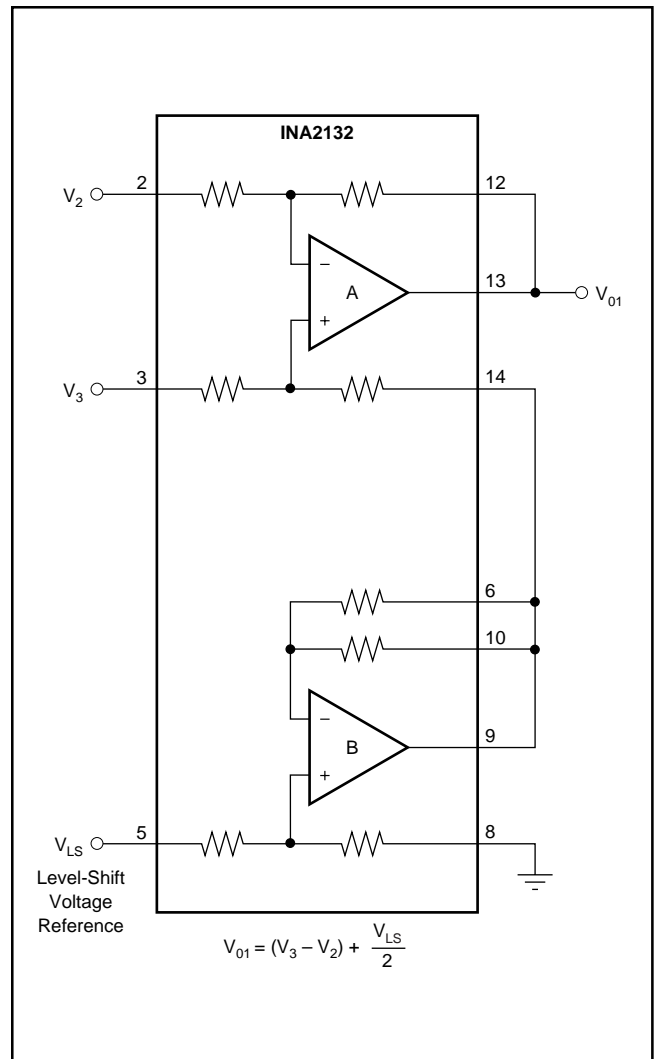


FIGURE 9. Precision Level Shifter.

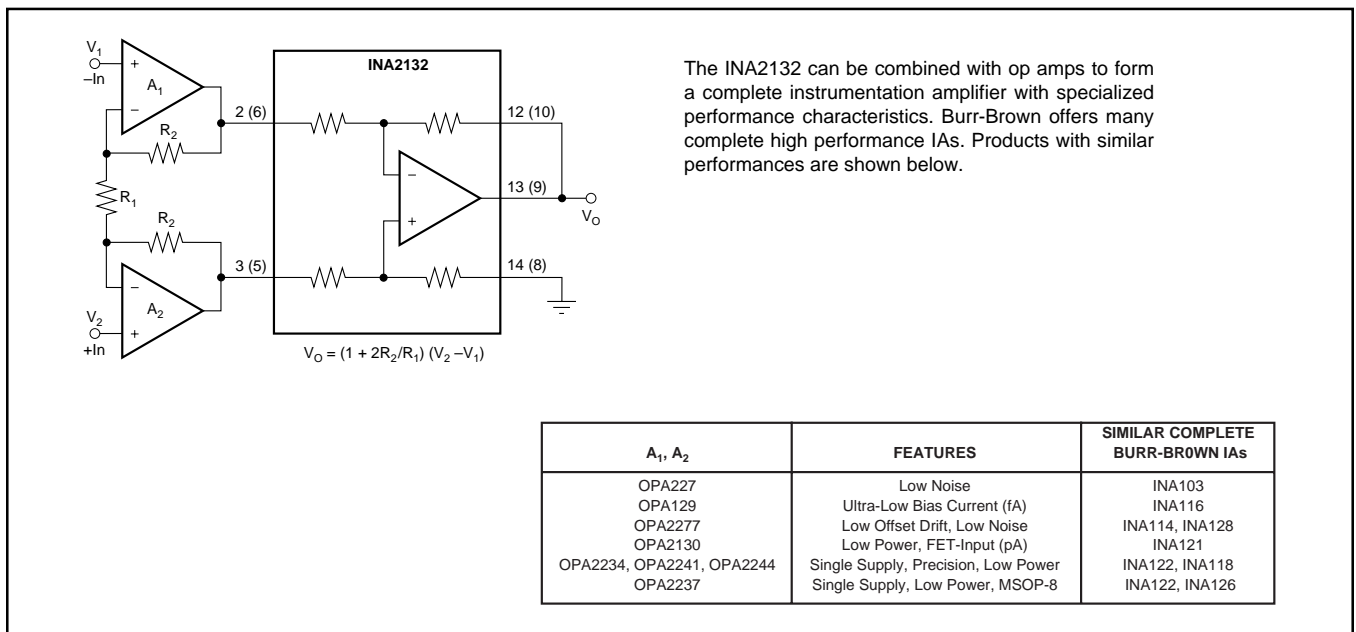


FIGURE 10. Precision Instrumentation Amplifier.

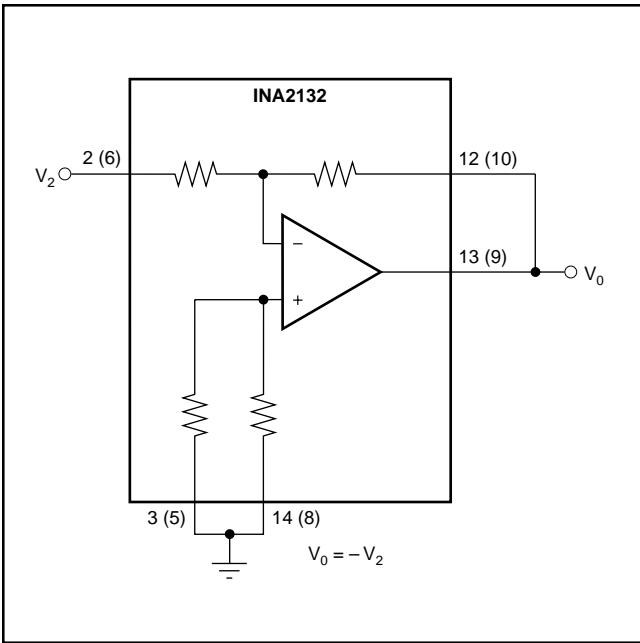


FIGURE 11. Precision Inverting Unity-Gain Amplifier.

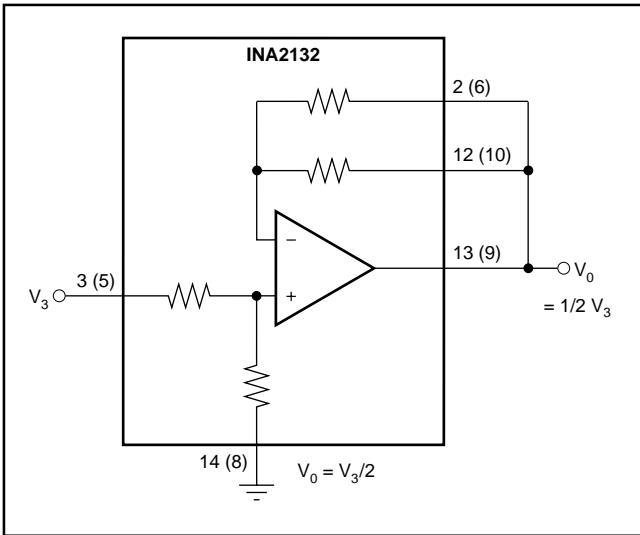


FIGURE 12. Precision Gain = 1/2 Amplifier.

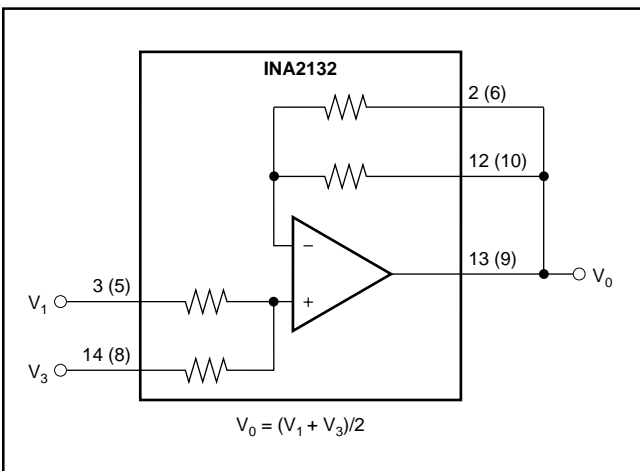


FIGURE 13. Precision Average Value Amplifier.

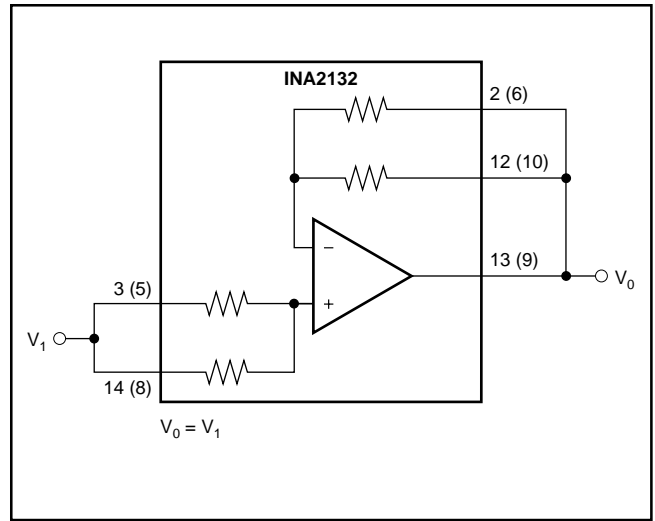


FIGURE 14. Precision Unity-Gain Buffer.

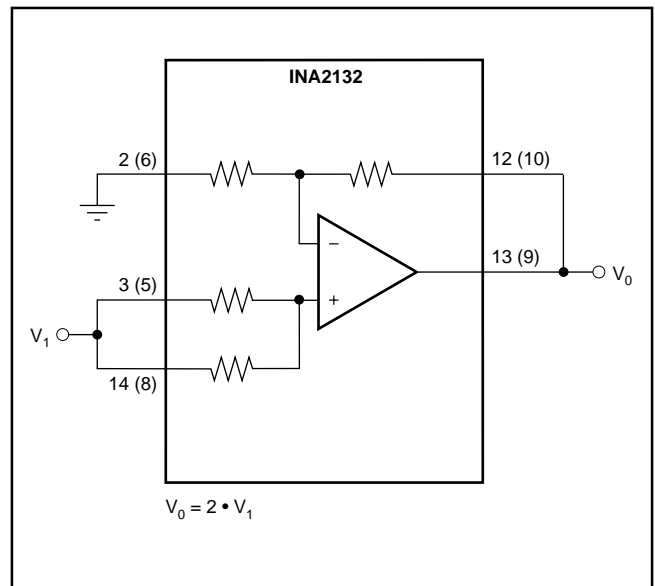


FIGURE 15. Precision Gain = 2 Amplifier.

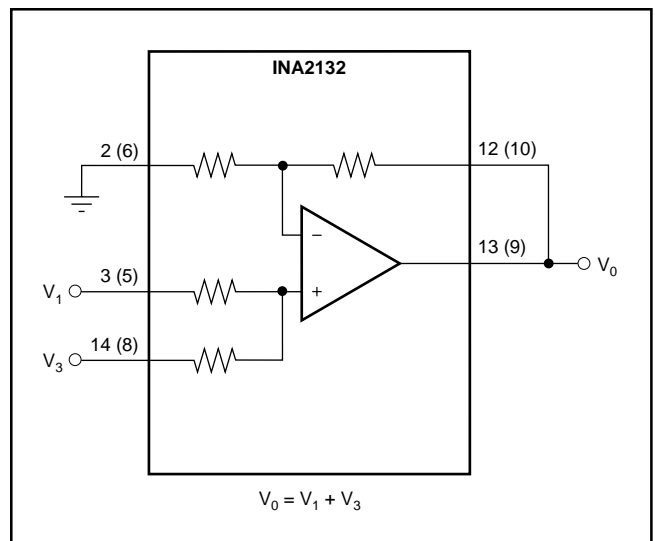


FIGURE 16. Precision Summing Amplifier.

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
INA2132U	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples
INA2132U/2K5	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples
INA2132UA	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples
INA2132UA/2K5	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples
INA2132UA/2K5E4	OBSOLETE	SOIC	D	14		TBD	Call TI	Call TI	-40 to 85		
INA2132UA/2K5G4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples
INA2132UAE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples
INA2132UAG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples
INA2132UE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples
INA2132UG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	-40 to 85	INA2132U A	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ Only one of markings shown within the brackets will appear on the physical device.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

TAPE AND REEL INFORMATION



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
INA2132U/2K5	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1
INA2132UA/2K5	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
INA2132U/2K5	SOIC	D	14	2500	367.0	367.0	38.0
INA2132UA/2K5	SOIC	D	14	2500	367.0	367.0	38.0

D (R-PDSO-G14)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 -  Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
 -  Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
 - E. Reference JEDEC MS-012 variation AB.

D (R-PDSO-G14)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Publication IPC-7351 is recommended for alternate designs.
 - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
 - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Products

Audio	www.ti.com/audio
Amplifiers	amplifier.ti.com
Data Converters	dataconverter.ti.com
DLP® Products	www.dlp.com
DSP	dsp.ti.com
Clocks and Timers	www.ti.com/clocks
Interface	interface.ti.com
Logic	logic.ti.com
Power Mgmt	power.ti.com
Microcontrollers	microcontroller.ti.com
RFID	www.ti-rfid.com
OMAP Applications Processors	www.ti.com/omap
Wireless Connectivity	www.ti.com/wirelessconnectivity

Applications

Automotive and Transportation	www.ti.com/automotive
Communications and Telecom	www.ti.com/communications
Computers and Peripherals	www.ti.com/computers
Consumer Electronics	www.ti.com/consumer-apps
Energy and Lighting	www.ti.com/energy
Industrial	www.ti.com/industrial
Medical	www.ti.com/medical
Security	www.ti.com/security
Space, Avionics and Defense	www.ti.com/space-avionics-defense
Video and Imaging	www.ti.com/video

TI E2E Community

e2e.ti.com



Стандарт Электрон Связь

Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

Благодаря сотрудничеству с мировыми поставщиками мы осуществляем комплексные и плановые поставки широчайшего спектра электронных компонентов.

Собственная эффективная логистика и склад в обеспечивает надежную поставку продукции в точно указанные сроки по всей России.

Мы осуществляем техническую поддержку нашим клиентам и предпродажную проверку качества продукции. На все поставляемые продукты мы предоставляем гарантию .

Осуществляем поставки продукции под контролем ВП МО РФ на предприятия военно-промышленного комплекса России , а также работаем в рамках 275 ФЗ с открытием отдельных счетов в уполномоченном банке. Система менеджмента качества компании соответствует требованиям ГОСТ ISO 9001.

Минимальные сроки поставки, гибкие цены, неограниченный ассортимент и индивидуальный подход к клиентам являются основой для выстраивания долгосрочного и эффективного сотрудничества с предприятиями радиоэлектронной промышленности, предприятиями ВПК и научно-исследовательскими институтами России.

С нами вы становитесь еще успешнее!

Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург,
Промышленная ул, дом № 19, литера Н,
помещение 100-Н Офис 331