Wireless 2 Wireless

GPS Module W2SG0084i

Product Datasheet

ALT-PDT-DOC Revision 1.61
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Revision History

Rev.	Revision Date	Originator	Changes
1.0	June 12, 2010	Will Lumpkins	Initial draft
1.1	June 23, 2010	Will Lumpkins	Added current source for 1.8V Output
1.2	June 29, 2010	Will Lumpkins	Added missing figures
1.21	Sept 30, 2010	Will Lumpkins	Added missing Block diagrams
1.3	Oct 10, 2010	Will Lumpkins	Added Wakeup, timing diagram, Receiver Sensitivity data
1.4	Oct 20, 2010	Will Lumpkins	Added SRESET Pin label & WAAS-SBAS
1.45	Dec 2, 2010	Will Lumpkins	Added SRESET description
1.6	Aug 29, 2011	WJL, SK, EK	Added Ordering Options, Updated Pin List, SRESET, Time Mark
1.61	Nov 11, 2011	WJL	Removed SRESET

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1 General Description

This specification provides a general guideline on the performance and the integration of the Wi2Wi, Inc. NAVSTAR L1C/A Band, 48 Channel GPS Receiver Module Solution. The solder-down module, W2SG0084i which is a drop in replacement for the W2SG0004, is targeted to assist companies to easily integrate GPS functionally into their products. This is accomplished by reducing their development times and cost by using a complete, small form factor, low power, ready to integrate GPS Receiver System Solution.

The specification maximum and minimum limits presented herein are those guaranteed when the unit is integrated into the Wi2Wi, Inc. Development System. These limits are to serve as the representative performance characteristics of the when properly designed into a customer's product. Wi2Wi makes no warranty, implied or otherwise specified, with respect to the customer's design and the performance characteristics presented in this specification.

2 Features

The W2SG0084i is a 48 Verification Channel Global Position System (GPS) receiver surface mount device (SMD) solution. Based upon the SiRFstar IV™ technology, the SMD features fast acquisition times, high receiver sensitivity and low power consumption in a small, compact form factor. The W2SG0084i is a flexible design that supports a broad range of applications where GPS functionality and location based services is required.

Key features are as follows:

- GPS technology based upon by the CSR/SiRF™ SiRFStar IV™
- Compact design for easy integration: 11.2mm x 12mm x 2.5mm
- Fast acquisition time and high sensitivity GPS Receiver
- High sensitivity navigation engine (PVT) tracks as low as -163dBm
- Ultra-low power consumption (Only 50 to 500μA maintains hot start capability)
- Surface Mount Design (SMD)
- 50Ω Antenna Launch
- 48 verification channel GPS receiver
- Active Jammer Remover:
    - Removes in-band jammers up to 80 dB-Hz
    - Tracks up to 8 CW jammers
- Uses NAVSTAR GPS L1 C/A signal
- Single 3.3 VDC supply input
- UART Interface (4800 BAUD Default)
- Auto Start Option: Immediate tracking after power up (see part order options)
- Format Selectable Output Data: NMEA and OSP™
- RoHS Compliant

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2.1 Pin Definition

The functional pin definition for the W2SG0084i is presented below in Table 1.

Table 1: Pin Definition

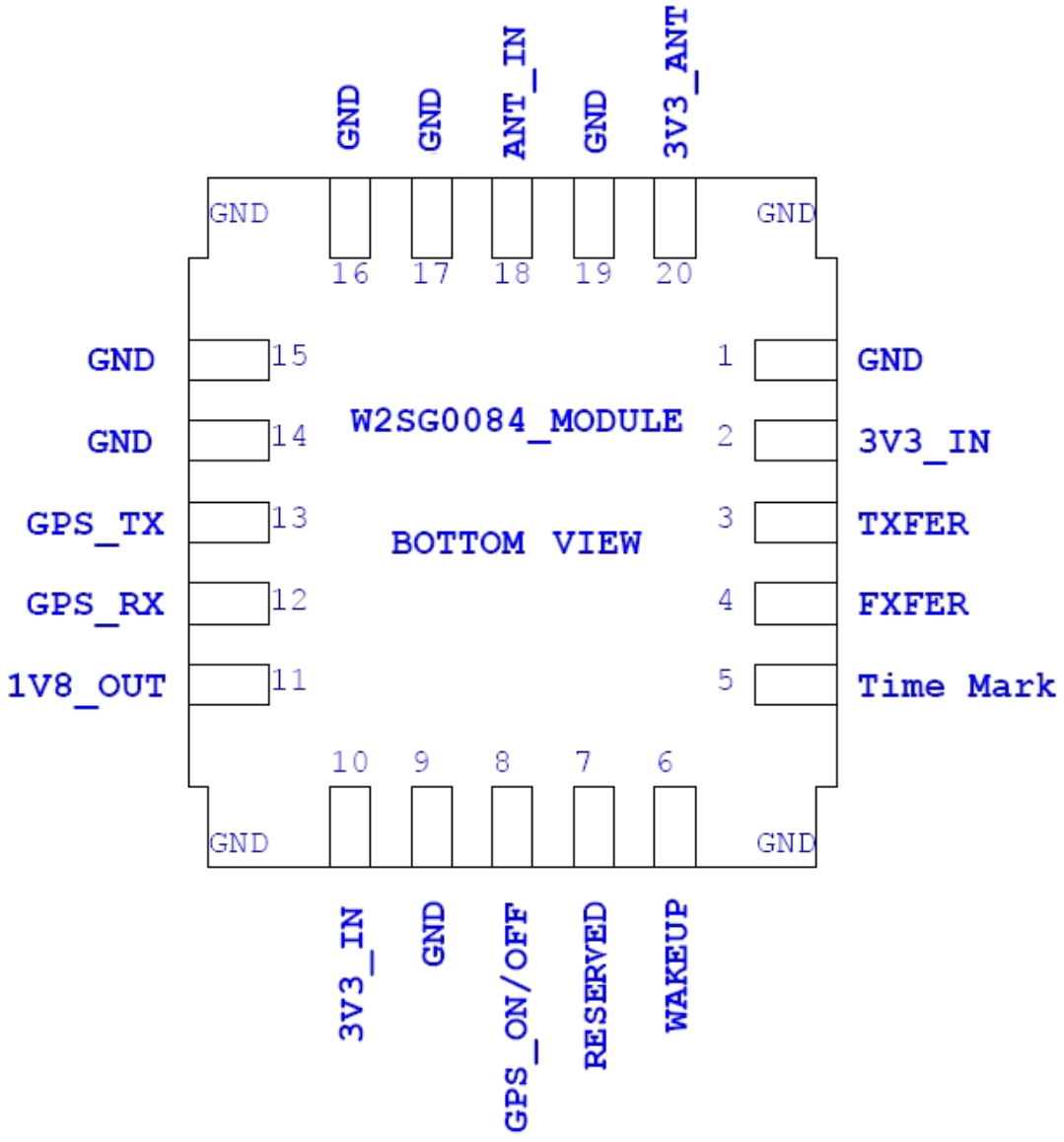
1	GND	PWR	Ground
2	3V3_IN	PWR	3.3V Power supply
3	TXFER	I	Time Aiding input, 1.8V; Leave unconnected if it is not used
4	FXFER	I	Frequency Aiding input, 1.8V; Leave unconnected if it is not used
5	Time Mark	O	Time Mark output; Leave unconnected if it is not used
6	WAKEUP	O	GPS is ready for full power mode
7	Reserved	NC	Reserved No Connect
8	GPS_ON/OFF	I	Power On enabled by a rising edge or by interrupt, Power Off through software command 1.8V level
9	GND	PWR	Ground
10	3V3_IN	PWR	3.3V Power supply
11	1V8_OUT	O	Regulator Output, 1.8V; Can source 10mA
12	GPS_RX	I	UART Receive input port, 1.8V (9600 BAUD)
13	GPS_TX	O	UART Transmit output port, 1.8V (9600 BAUD)
14	GND	PWR	Ground
15	GND	PWR	Ground
16	GND	PWR	Ground
17	GND	PWR	Ground
18	ANT_IN	I	Antenna RF input (50 ohm impedance) 10dB max
19	GND	PWR	Ground
20	3V3_ANT	PWR	Active antenna bias input; Connect to 3.3V if using active antenna; Leave it unconnected if using passive antenna

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2.2 Pin Configuration

The W2SG0084i is a 20 pin SMD device with a board down antenna connection. The pin configuration is presented below in
Figure 1.

Figure 1: Pin Configuration (Bottom View)

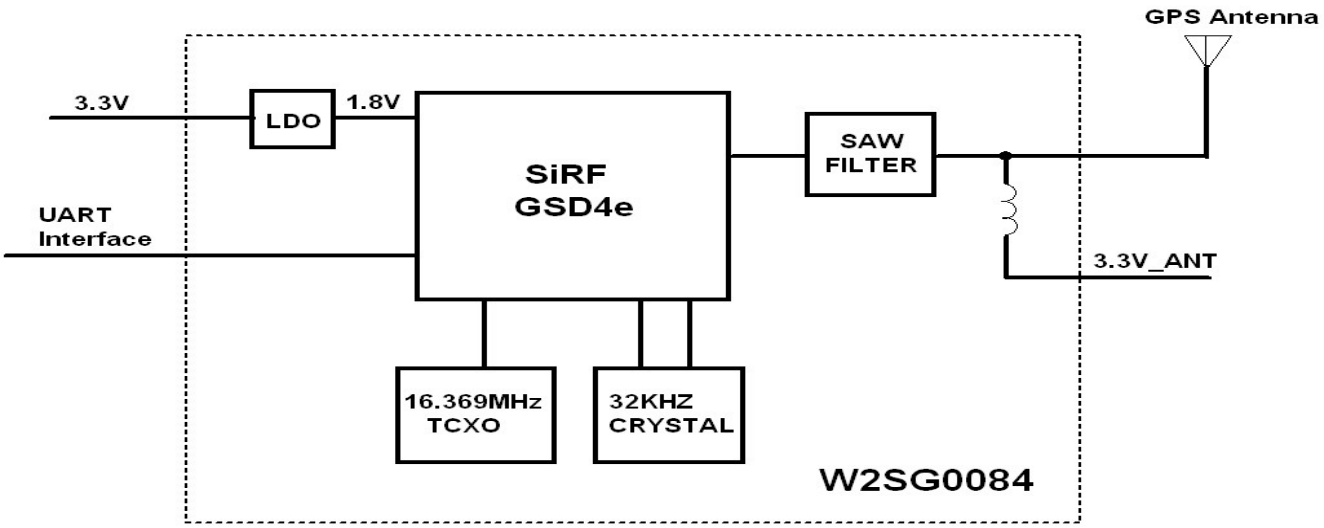


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2.3 System Block Diagram

The System Block for the W2SG0084i is presented below in Figure 2.

Figure 2: System Block Diagram



The W2SG0084i provides an internal LNA, all required power regulation, and clocking. The TTL UART Interface is accessed via Pin 12 and 13.

3 Specifications

3.1 Clock Frequency

The W2SG0084i features an internal clock and crystal and requires no external clock sources.

Absolute Maximums Ratings

The values presented below in Table 2 are those parameters beyond which permanent damage could result. These values *do not* imply functional operation and should be considered as stress ratings only.

Table 2: Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Power Supply			
Input Voltage	V_{DD}	5.5	V
RF Input	RF_{IN}	10	dBm
Case Temperature	T_{CASE}		C°
Lead Temperature (Soldering, 10sec)	T_{MFG}	260	C°
Operating Temperature Range	T_A	-40 to +85	C°
Storage Temperature Range	T_S	-40 to +150	C°

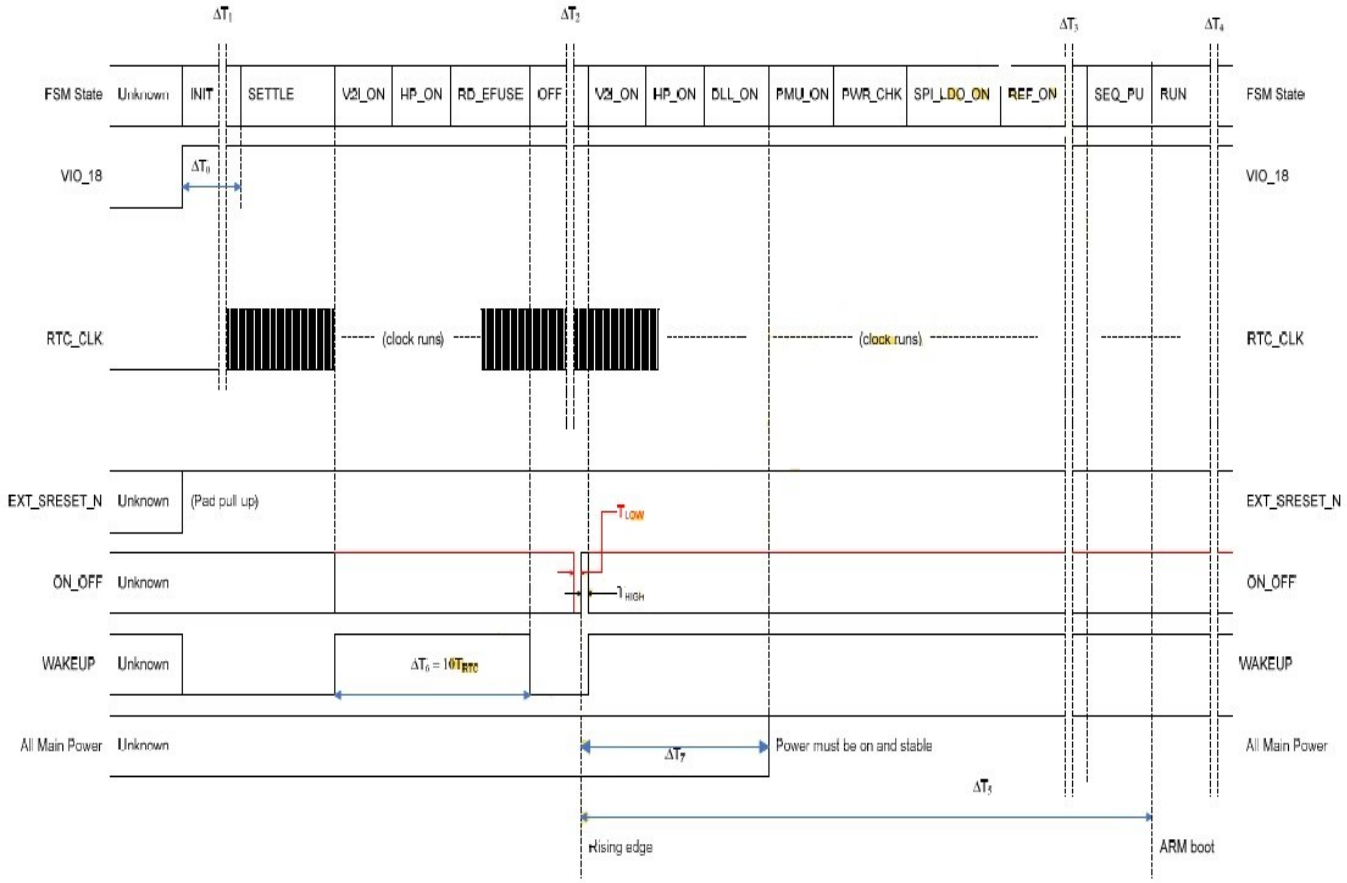
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3.2 Interfaces

3.2.1 Host UART Interface

The TTL UART Interface (Pins 12, 13) has a bit rate range of 1.2kbps to 115.2 kbps, though defaulted at 4800 BAUD. Protocol options for the W2SG0084i are NMEA and SiRFBINARY™ (OSP). Default setting for the UART Interface is 4800 BAUD/NMEA Protocol. Initial Power-On sequence

Figure 3: Timing Diagram of initial application of power supply



3.2.2 Wake Up Pin

The Wake up pin functions as follows: A short pulse on the WAKEUP output line indicates to a host that the W2SG0084i is ready to accept an ON_OFF pulse to start normal operation. When the module is in standby or hibernate mode, the WAKEUP output goes low and when the module is in Full power mode the WAKEUP output goes high.

3.2.3 Time Mark

The Time Mark pin [5] functions as follows: the Time Mark output provides a one pulse-per-second (1pps) signal to the customer's application processor. When the receiver provides a

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valid navigation solution which consists of five satellite vehicles, the rising edge of each TMARK pulse is synchronized with the UTC one second epochs to within ±1 microsecond. The receiver software produces a binary format data message containing the UTC time associated with each time mark pulse. The relationship between the UTC Time Mark Pulse Output message and the TMARK pulse is shown in Figure x. When the receiver's serial data communication port is set to 9600 bps, the UTC Time Mark Pulse Output message precedes the TMARK pulse by xxx milliseconds (typically). The TMARK pulse waveform is shown in Figure x. This signal is a positive logic, buffered CMOS level output pulse that transitions from a logic "low" condition to a logic "high" at a 1 Hz rate. The TMARK output pulse rise time is typically less than 2 nanoseconds and the pulse duration is typically 200 milliseconds.

Patch Option:

A patch exists that allows the Time Mark to output the 1 PPS with only four satellite vehicles instead of the default of five satellites. This patch can be downloaded from the Wi2Wi Extranet and implemented with the GPS patch manager that will run on Windows 7^{tm} and Linux operating systems. Please register at www.wi2iw.com .

3.3 GPS Power On/Off

To enable GPS or to turn off GPS a positive pulse for two RTC ticks (62uS) after the Receiver has stabilized after a Fix. This can be verified with a message ID 18, called "OK to Send", that comes out when the receiver is receptive to commands.

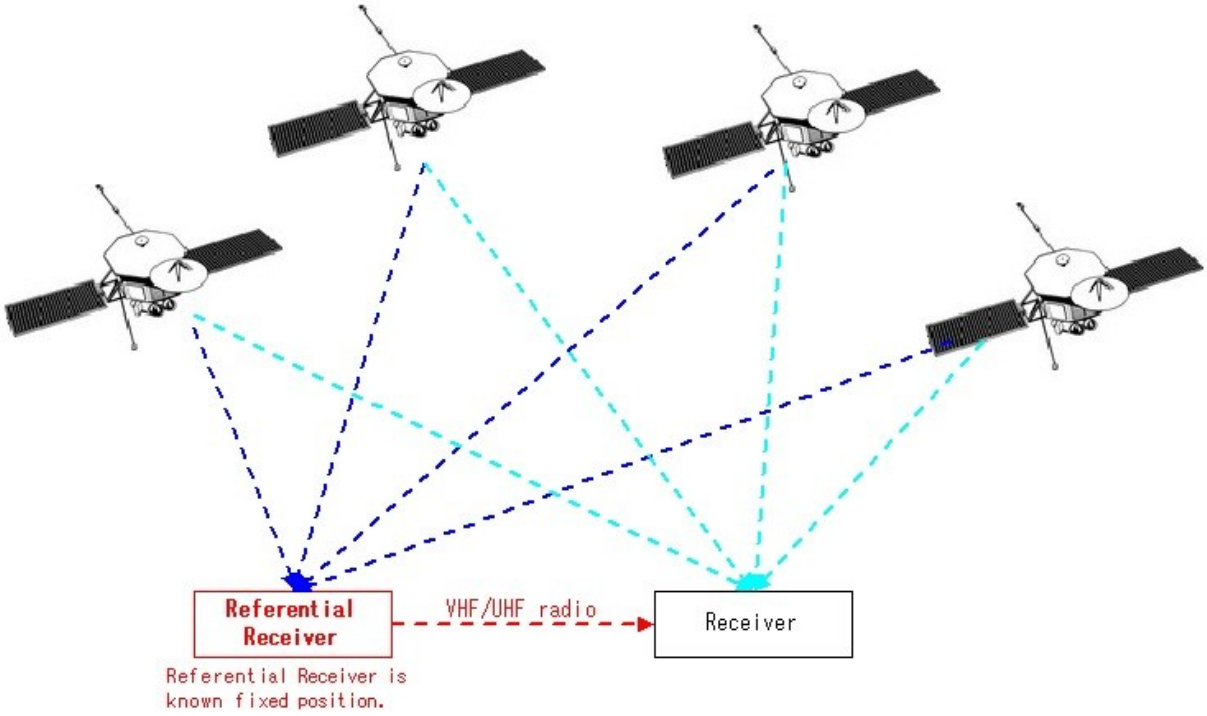
4 WAAS with SBAS or Wide Area Differential GPS

4.1 Differential GPS

Differential GPS (DGPS) is traditionally used with the fix of three to four satellites and the secondary fix from ground based GPS receiver based stations that retransmit the secondary fix via VHF/UHF and occasionally FM. DGPS can achieve positional accuracies of between 60 cm ~ 10 cm's. See Figure 4

Figure 4 Traditional DGPS System

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DGPS requires at least two antennas, one for the NavStar L1 CA signal from the satellite and one antenna for the secondary fix from the VHF/UHF/FM transmitter. Also a secondary application processor is used to perform "Mixing" calculations between the two fixes. This increases the size and the expense of the system.

4.2 Wide Area DGPS or WAAS

Wide Area DGPS or WAAS (Wide Area Augmentation System) uses the same concept but eliminates the need for the secondary RF signal. It accomplishes this by reusing the already orbiting satellites to re-broadcast other secondary fixes that have been established by ground based master stations in North America and Hawaii, to measure small variations in the GPS satellites' signals in the western Hemisphere. Measurements from the reference stations are routed to master stations, which queue the received Deviation Correction (DC) and send the correction messages to geostationary WAAS satellites in a timely manner (every 5 seconds or better). These master stations rebroadcast the secondary fix to the NavStar satellites which broadcast the secondary fix on unused channel space. This enables a GPS receiver to utilize the same antenna to reprocess both fixes internal to it also eliminating the need for a secondary application processor. WAAS enables a GPS receiver to provide positional accuracy to 300 cm ~ 200 cm. Of course a GPS receiver utilizing WAAS will need an expanded offering of GPS receiver channels to effective use this feature like the W2SG0084i which has 48 channels available for this function.

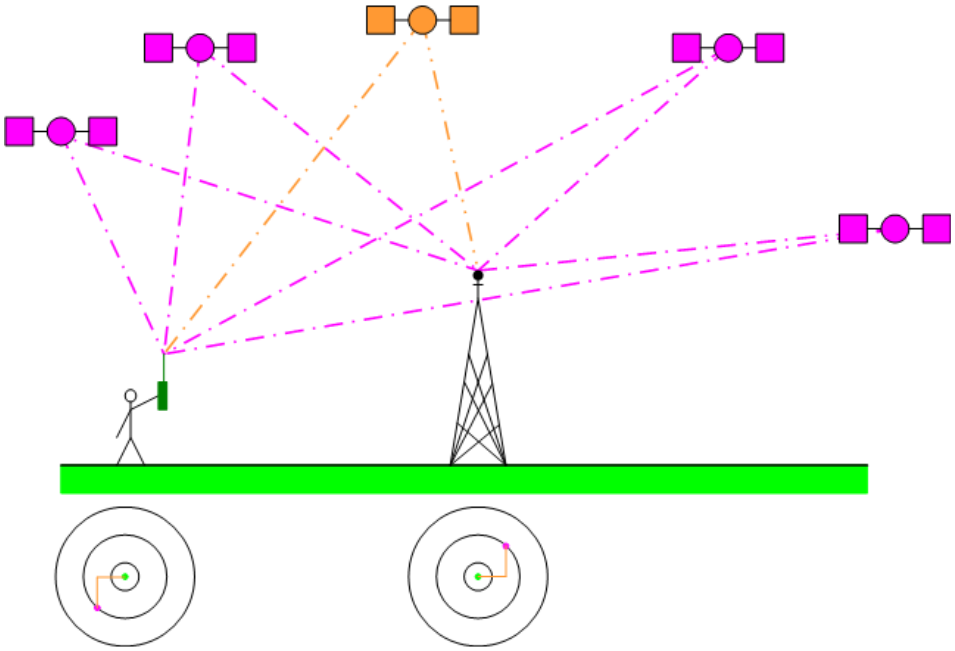
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As noted this system was originally developed for the U.S. and Northern Hemisphere Geography. As GPS became ubiquitous in technology use throughout the world, similar systems called generically as Satellite-Based Augmentation System (SBAS). A SBAS is a system that supports wide-area or regional augmentation through the use of additional satellite-broadcast messages. Such systems are commonly composed of multiple ground stations, located at accurately-surveyed points. The ground stations take measurements of one or more of the GNSS satellites, the satellite signals, or other environmental factors which may impact the signal received by the users. Using these measurements, information messages are created and sent to one or more satellites for broadcast to the end users.

While SBAS designs and implementations may vary widely, with SBAS being a general term referring to any such satellite-based augmentation system, under the International Civil Aviation Organization (ICAO) rules a SBAS must transmit a specific message format and frequency which matches the design of the United State's Wide Area Augmentation System. See Figure 5

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Figure 5 SBAS Example System



Some examples of these are (GPS Aided GEO Augmented Navigation) GAGAN developed for the Indian Sub-Continent, European Geostationary Navigation Overlay Service (EGNOS) developed for the European Union, and the Japanese Multi-functional Satellite Augmentation System (MSAS), respectively.

4.3 How do we enable WAAS-SBAS on the W2SG0084i?

WAAS-SBAS is enabled by issuing Message ID 133, (MID 133) is supported in One Socket Protocol (Formerly Sirf Binary Protocol mode), and then the accuracy improves to 300 cm ~ 100 cm with it enabled, depending on Open Sky Conditions (Multi-Path interference). With MID 133 not enabled, the accuracy is near 1500cm.

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5 NMEA Input Messages

The NMEA messages used to control the W2SG00084i modules are listed in Table 3. Messages 100 to 106 are OSP NMEA messages. The MSK NMEA string is as defined by the NMEA 0183 standard. See NMEA Reference Manual (CS-129435-MA-1) for further detail.

Table 3: NMEA Input Messages

Message	Message ID	Description
SetSerialPort	100	Set PORT A parameters and protocol
NavigationInitialization	101	Parameters required for start using X/Y/Z
SetDGPSPort	102	Set PORT B parameters for DGPS input
Query/Rate Control	103	Query standard NMEA message and/or set output rate
LLANavigationInitialization	104	Parameters required for start using Lat/Lon/Alt
Development Data On/Off	105	Development Data messages On/Off
Select Datum	106	Selection of datum to be used for coordinate transformations
Extended Ephemeris Proprietary 1	107	Extended Ephemeris Proprietary message
Extended Ephemeris Proprietary 2	108	Extended Ephemeris Proprietary message
Extended Ephemeris Debug	110	Extended Ephemeris Debug
MSK Receiver Interface	MSK	Command message to a MSK radio-beacon receiver

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6 One Socket Protocol (OSP) Messages (Formerly SiRF Binary I/O)

6.1 OSP Output Messages

OSP Output Messages generated by the W2SG0084i are listed in Table 4. Please note that the support of individual commands is dependent upon the firmware loaded in the module.

Table 4: OSP Output Messages

Hex	Decimal	Name	Description
01	1	Reference Navigation Data	Not Implemented
02	2	Measured Navigation Data	Position, velocity, and time
03	3	True Tracker Data	Not Implemented
04	4	Measured Tracking Data	Satellite and C/No information
05	5	Raw Track Data	Not supported by SiRFstarII
06	6	SW Version	Receiver software
07	7	Clock Status	Current clock status
08	8	50 BPS Subframe Data	Standard ICD format
09	9	Throughput	Navigation complete data
0A	10	Error ID	Error coding for message failure
0B	11	Command Acknowledgment	Successful request
0C	12	Command NAcknowledgment	Unsuccessful request
0D	13	Visible List	Auto Output
0E	14	Almanac Data	Response to poll
0F	15	Ephemeris Data	Response to poll
10	16	Test Mode 1	For use with SiRFtest (Test Mode 1)
11	17	Differential Corrections	Received from DGPS broadcast
12	18	OkToSend	CPU ON / OFF (TricklePower)
13	19	Navigation Parameters	Response to Poll
14	20	Test Mode 2/3/4	Test Mode 2, 3, or 4 test data
1B	27	DGPS Status	Differential GPS status information
1C	28	Nav. Lib. Measurement Data	Measurement data
1D	29	Nav. Lib. DGPS Data	Differential GPS data
1E	30	Nav. Lib. SV State Data	Satellite state data
1F	31	Nav. Lib. Initialization Data	Initialization data
29	41	Geodetic Navigation Data	Geodetic navigation information
2B	43	Queue Command Parameters	Command parameters
2D	45	Raw DR Data	Raw DR data from ADC
2E	46	Test Mode 3	Additional test data (Test Mode 3)
30	48	Test Mode 4 for SiRFLoc v2.x only	Additional test data (Test Mode 4)
30	48	SiRFDRive-specific Class of Output Messages	The MID is partitioned into messages identified by Sub IDs
31	49	Test Mode 4 for SiRFLoc v2.x only	Additional test data (Test Mode 4)
32	50	SBAS Parameters	SBAS operating parameters
34	52	1 PPS Time Message	Time message for 1 PPS
37	55	Test Mode 4	Track Data
38	56	Extended Ephemeris Data	Extended Ephemeris Mask and Integrity Information
E1	225	SiRF internal message	Reserved
FF	255	Development Data	Various status messages

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6.2 OSP Input Messages

OSP input commands for the W2SG0084i are listed in
Table 5. Please note that the support of individual commands is dependent upon the firmware loaded in the module.

Table 5: OSP Input Messages

Hex	Decimal	Name	Description
35	53	Advanced Power Management	Power management scheme for SiRFLoc and SiRFXTrac
80	128	Initialize Data Source	Receiver initialization and associated parameters
81	129	Switch to NMEA Protocol	Enable NMEA messages, output rate and baud rate
82	130	Set Almanac (upload)	Sends an existing almanac file to the receiver
83	131	Handle Formatted Dump Data	Outputs formatted data
84	132	Poll Software Version	Polls for the loaded software version
85	133	DGPS Source Control	DGPS correction source and beacon receiver information
86	134	Set Binary Serial Port	Baud rate, data bits, stop bits, and parity
88	136	Mode Control	Navigation mode configuration
89	137	DOP Mask Control	DOP mask selection and parameters
8A	138	DGPS Mode	DGPS mode selection and timeout value
8B	139	Elevation Mask	Elevation tracking and navigation masks
8C	140	Power Mask	Power tracking and navigation masks
8F	143	Static Navigation	Configuration for static operation
90	144	Poll Clock Status	Polls the clock status
91	145	Set DGPS Serial Port	DGPS port baud rate, data bits, stop bits, and parity
92	146	Poll Almanac	Polls for almanac data
93	147	Poll Ephemeris	Polls for ephemeris data
94	148	Flash Update	On the fly software update
95	149	Set Ephemeris (upload)	Sends an existing ephemeris to the receiver
96	150	Switch Operating Mode	Test mode selection, SV ID, and period.
97	151	Set TricklePower Parameters	Push to fix mode, duty cycle, and on time
98	152	Poll Navigation Parameters	Polls for the current navigation parameters
A5	165	Set UART Configuration	Protocol selection, baud rate, data bits, stop bits, and parity
A6	166	Set Message Rate	SiRF Binary message output rate
A7	167	Set Low Power Acquisition Parameters	Low power configuration parameters
A8	168	Poll Command Parameters	Poll for parameters:
			0x80: Receiver initialized & associated params
			0x85: DGPS source and beacon receiver info
			0x88: Navigation mode configuration
			0x89: DOP mask selection and parameters
			0x8A: DGPS mode selection and timeout values
			0x8B: Elevation tracking and navigation masks
			0x8C: Power tracking and navigation masks
			0x8F: Static navigation configuration
			0x97: Low power parameters
AA	170	Set SBAS Parameters	SBAS configuration parameters
AC	172	SiRFDRive-specific Class of Input Messages	The MID is partitioned into messages identified by Sub IDs.
B4-C7	180-199	MID_UserInputBegin - MID_UserInputEnd	SDK user input messages only.
B4	180	Marketing Software Configuration	Selection of the Marketing Software Configurations as defined in bits [3:2] of the GSC2xr chip configuration register
B6	182	Set UART Configuration	Obsolete.
E4	228	SiRF internal message	Reserved
E8	232	Extended Ephemeris Proprietary	Extended Ephemeris and Debug Flag

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7 Electrical/RF Characteristics: Operating

The Table 6 below presents the normal limits of operation for the W2SG0084i. Operation of the W2SG0084i beyond the limits of this table is not recommended and may result in permanent damage of the device. Unless otherwise specified, operating conditions are over T_A = -40 C° to +85 C°. Typical is defined as T_A = +25 C°.

Table 6: Operating Electrical Characteristics

Parameter[1]	Symbol	Min	Typ.	Max.	Units
Power Supply (3V3_IN)					
Power Supply Voltage	V_{CC}	3.25	3.3	3.6	V
I/O Supply Voltage	V_{IO}	1.62	1.8	1.98	V
Power Supply Ripple	V_{RIP}			100	mV
Peak Supply Current[2]	I_{CCP}			50	mA
Power Supply Input Current[3]	I_{CC}			35	mA
I/O Input Current	I_{IO}		1.5	2.0	mA
Hibernate Current	I_h		20		uA
UART Interface (GPS_TX, GPS_RX)					
Input Pin Voltage	VRX		1.8	3.6	V
Output Pin Voltage	VTX		1.8		V
GPIO Interface					
GPIO Pin Voltage	V_{GPIO}	1.62	1.8	1.98	V
GPS Enable (GPS_ON/OFF)					
Input Pin Low Voltage	V_{IL}	0		0.45	V
Input Pin High Voltage	V_{IH}	1.35	1.8	3.6	V
RF Input					
Input Impedance	R_{ANT}		50		Ω
Operating Frequency	F_{OPR}		1.575		GHz
Antenna Bias Voltage	V_{ANT}		3.3		V
Antenna Bias Voltage Drop	V_{ANTD}		200		mV
RF Antenna Voltage	V_{CCRF}	3.0	3.3	5.4	V
RF Antenna Output Current	I_{CCRF}			22	mA
RF Characteristics					
Power In @1.5745 GHz	P_{IN}	-157	-131	10	dBm
Noise Figure	NF		2.5		dB
Input Return Loss	RL_{IN}		-10.0		dB
Input VWR	VWR_{IN}		TBD	1.8:1	
Reverse Isolation	ISL		TBD		dB
Stability (100 -10000 MHz)					
Receiver Sensitivity:					
•Signal Acquisition @ 31dBHz	P_{AQC}		-148		dBm
•Signal Tracking	P_{TKS}		-163	-	dBm

Notes:
[1] All parameters are at TA = 25C°, unless otherwise specified.
[2] Defined as peak current drawn during initial acquisition operation of GPS Receiver.
[3] Defined as current drawn during continuous operation at a 10Hz update rate.

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7.1 Performance

7.1.1 Acquisition Time:

The average Time to First Fix (TTFF) for the W2SG0084i when integrated with the W2SG0084i-DEV Development System is presented in Table 7 below:

Table 7: Average Time to First Fix

Parameter[1]	Symbol	Min	Typ.	Max.	Units
Hot Start – Typ. @ -140 dBm	$TTFF_{TYP}$	-	0.7	-	s
Hot Start – Low @ - 146 dBm	$TTFF_{LOW}$	-	1.0	-	s
Hot Start – Weak @ - 150 dBm	$TTFF_{WEAK}$	-	2.0	-	s
Cold Start @ -140 dBm	$TTFF_{CLD}$	-	35	-	s
Cold Start @ -140 dBm	$TTFF_{CLD}$	-	37	-	s
Cold Start @ -146 dBm	$TTFF_{CLD}$	-	38	-	s
Cold Start @ -150 dBm	$TTFF_{CLD}$	-	45	-	s

Notes:
[1] Stationary receiver, unless otherwise specified.
[2] All parameters are at TA = 25C°, unless otherwise specified.

7.1.2 Position Accuracy (3-D NAV):

Table 8 below presents the Positional Accuracy for the W2SG0084i when integrated with the W2SG0084i-DEV Development System.

Table 8: Positional Accuracy

Parameter[1]	Typ.	Units
Horizontal Position Accuracy: CEP (50%)[3]	3	m
Horizontal Position Accuracy: 2dRMS (95%)[3]	5	m
Vertical Position Accuracy: CEP (50%)[3]	3	m
Vertical Position Accuracy: 2dRMS (95%)[3]	5	m
Horizontal Velocity Accuracy: Deviation[3]	0.8	m/s
Vertical Velocity Accuracy: Deviation[3]	0.4	m/s

Notes:
[1] Stationary receiver in the Open Sky at -130dBm, unless otherwise specified.
[2] All parameters are at TA = 25C°, unless otherwise specified.
[3] WAAS SBAS must be enabled

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7.2 Environmental Characteristics

Table 9 establishes the environmental limits for operational use of the W2SG0084i.

Table 9: Environmental Characteristics

Parameter	Symbol	Min	Typ.	Max.	Units
Storage Temperature	T_{STR}	-40	-	+150	C°
Operating Temperature	T_{OPR}	-40	+25	+85	C°
Humidity		5	-	95	%/Non-condensing
Altitude		-	-	60,000/18,288	ft/m
Acceleration		-	-	6.0	g
Velocity				TBD	

7.3 Antenna

The W2SG0084i is designed for connection to either a passive or active antenna. The GPS module includes an internal LNA and is designed to work with the passive antenna, but provides the bias voltage supply for an active antenna to improve the receive sensitivity. If the design is to work with an Active Antenna; Pin 20 (3V3_ANT) is tied to 3.3VDC.

8 Development Support

The W2SG0084i device is embedded with GPS software. This software is optimized to work in very weak signal environments to improve navigation availability and accuracy.

To enable GPS performance testing, Wi2Wi provides a W2SG0084i GPS Evaluation board, along with the SiRFLive 2.2 software tools.

The SiRFLive 2.2 is a PC tool that provides real-time monitoring of an attached GPS receiver's operation such as satellites being tracked, observed signal strength, and current position.

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9 Mechanical Information

9.1 Mechanical Specification:

The module is a Surface Mount Device (SMD). Table 10 presents the mechanical characteristics of the W2SG0084i.

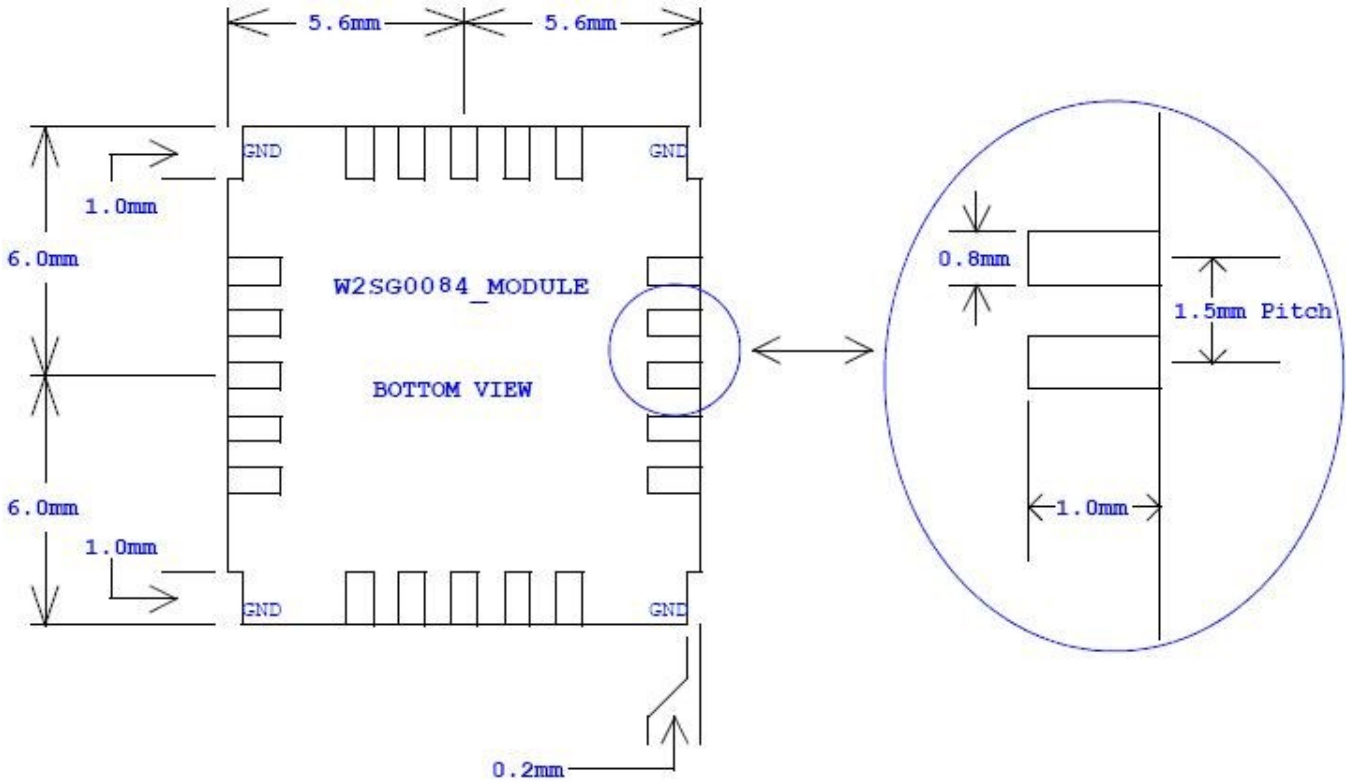
Table 10: Mechanical Characteristics

Parameter	Spec.	Tol.	Units
Size	11.2 x 12 x 2.3	±0.1	mm
Pad Pitch	1.5	±0.05	mm

9.2 Outline Drawing:

The overall dimensions of the W2SG0084i are 11.2mm x 12mm x 2.3mm. The module includes a shield. The Mechanical Dimensions of the module are presented in Figure 6.

Figure 6: Package Outline Drawing



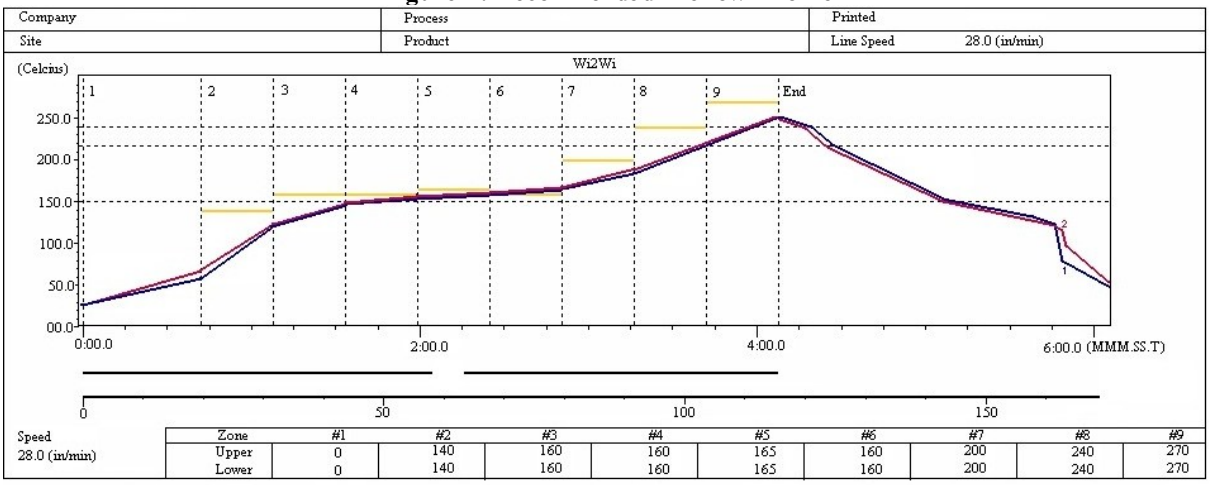
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9.3 Recommended Reflow Profile:

Assembly Guidelines:

1. Follow solder paste manufacturers recommended profile
   a. All RoHS solder pastes contain the same basic chemistry; however, each manufacturer may have a recommended reflow profile that performs best for their product.
2. The profile illustrated in JESD-020 and below is for reference only.
   a. <u>There is no one profile fits all scenario.</u>
3. Profiles must be dialed in to the specific assembly type.
4. ENIG finishes are more susceptible to voids and air entrapment.
   a. Selecting a RoHS solder paste that is "ENIG" compatible is recommended.
5. Recommended finishes for LGA/BGA inclusive assemblies include HASL, OSP, Tin, & Silver.

Figure 7: Recommended Reflow Profile



Recommended Reflow Profile

Key features of the profile in Figure 7:

- Initial Ramp = 1 -2.5^{0}C/Sec to 175^{0}C +/- 25^{0}C equilibrium
- Equilibrium = 60 180 seconds
- Ramp to Maximum (Peak) temperature (245^{0}C) = 3^{0}C/sec max.
- Time above liquidus temperature (217^{0}C): 45-90 seconds

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10 Part Order Options

Part Order Options	Description	Packing Method
W2SG0084i-T	**9600 baud rate***	**in Trays**
W2SG0084i-TR	**9600 baud rate***	**On Tape and Reel**
W2SG0084i-1-T	**4800 baud rate***	**in Trays**
W2SG0084i-1-TR	**4800 baud rate***	**On Tape and Reel**
W2SG0084i-2-T	**Auto Start, 9600 baud rate***	**in Trays**
W2SG0084i-2-TR	**Auto Start, 9600 baud rate***	**On Tape and Reel**
W2SG0084i-3-T	**Reserved for Future Use**	**in Trays**
W2SG0084i-3-TR	**Reserved for Future Use**	**On Tape and Reel**

*** All these options contain static navigation and SBAS, on.**

11 Disclaimers

Wi2Wi, Inc. PRODUCTS ARE NOT AUTHORISED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE MANAGING DIRECTOR OF Wi2Wi, Inc.

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Wi2Wi does not assume responsibility for use of any of the circuitry described, no circuit patent licenses are implied and Wi2Wi reserves the right at any time to change without notice said circuitry and specifications.

11.1 Data Sheet Status

Wi2Wi, Inc. reserves the right to change the specification without prior notice in order to improve the design and supply the best possible product. Updated information, firmware and release notes will be made available on www.wi2wi.com. Please check with Wi2Wi Inc. for the most recent data before initiating or completing a design.

12 Certifications

The W2SG0084i shall conform to the following standards when integrated to the W2SG0084i-DEV development system.

EMC/Immunity

- United States: FCC Part 15(To be tested)
- Canada: ICES 033 (To be Tested)
- European Union: EN 55022, IEC 1004/CISPR 22 (To be Tested)

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- Japan: VCCI - V series (To be Tested)
- Taiwan: CNS 13438 (To be Tested)
- Korea: RRL No. 2005-82 (MIC) (To be Tested)
- Australia New Zealand – CISPR (To be Tested)

Product Safety

- United States/Canada: UL/CSA 60950, UL 61010, UL 60065, CSA 601,CSA 61010,C22.2 No. 225(To be Tested)
- European Union: EN 60950, EN61010, IEC 60065, IEC 60601 (To be Tested)
- Japan: ARIB STD-T66 (To be Tested)
- Restriction of Hazardous Substances Directive (RoHS) 2002/95/EC (To be Tested)

13 References

13.1 Specifications

- System Specification, SirfStarIV GDS4e BGA, issue 3
- NMEA 0183 Version 3.01, January 2002, Addendum NMEA 0183-HS Version 1.0
- OSP Issue 8
- FAA WAAS Specification FAA-E 2892b

13.2 Trademarks, Patents and Licenses

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13.3 Other

- W2SG0084i-DEV Development Kit, GPS Module

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