MOSFETs Silicon N-Channel MOS (π-MOSVII)

TK7J90E

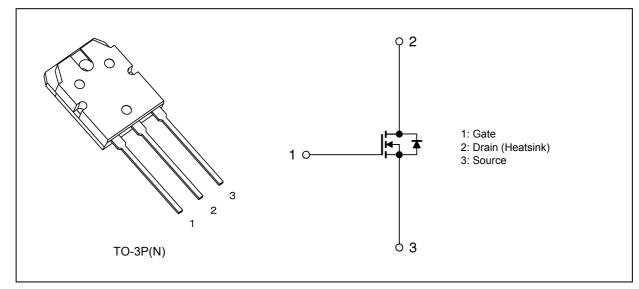
1. Applications

Switching Voltage Regulators

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 1.6 \Omega$ (typ.)
- (2) Low leakage current : $I_{\rm DSS}$ = 10 μA (max) $(V_{\rm DS}$ = 720 V)
- (3) Enhancement mode: V_{th} = 2.5 to 4.0 V (V_{DS} = 10 V, I_D = 0.7 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25$ °C unless otherwise specified)

| Characteristics | | Symbol | Rating | Unit |
|---------------------------------------|----------|------------------|------------|-------|
| Drain-source voltage | | V _{DSS} | 900 | V |
| Gate-source voltage | | V _{GSS} | ±30 |] |
| Drain current (DC) | (Note 1) | I _D | 7 | Α |
| Drain current (pulsed) | (Note 1) | I _{DP} | 21 | |
| Power dissipation (T _c = 2 | 25°C) | PD | 200 | W |
| Single-pulse avalanche energy | (Note 2) | E _{AS} | 235 | mJ |
| Avalanche current | | I _{AR} | 7 | A |
| Reverse drain current (DC) | (Note 1) | I _{DR} | 7 | |
| Reverse drain current (pulsed) | (Note 1) | I _{DRP} | 21 | |
| Channel temperature | | T _{ch} | 150 | ů |
| Storage temperature | | T _{stg} | -55 to 150 | |
| Mounting torque | | TOR | 0.8 | N · m |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

| Characteristics | Symbol | Max | Unit |
|---------------------------------------|-----------------------|-------|------|
| Channel-to-case thermal resistance | R _{th(ch-c)} | 0.625 | °C/W |
| Channel-to-ambient thermal resistance | R _{th(ch-a)} | 50 | °C/W |

Note 1: Ensure that the channel temperature does not exceed 150 °C.

Note 2: V_DD = 90 V, T_ch = 25°C (initial), L = 8.8 mH, R_G = 25 Ω , I_AR = 7 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

6.1. Static Characteristics (Ta = 25 °C unless otherwise specified)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--------------------------------|----------------------|---|-----|------|-----|------|
| Gate leakage current | I _{GSS} | V_{GS} = ±30 V, V_{DS} = 0 V | _ | _ | ±1 | μA |
| Drain cut-off current | I _{DSS} | V _{DS} = 720 V, V _{GS} = 0 V | _ | _ | 10 | |
| Drain-source breakdown voltage | V _{(BR)DSS} | I _D = 10 mA, V _{GS} = 0 V | 900 | _ | _ | V |
| Gate threshold voltage | V _{th} | V _{DS} = 10 V, I _D = 0.7 mA | 2.5 | _ | 4.0 | |
| Drain-source on-resistance | R _{DS(ON)} | V _{GS} = 10 V, I _D = 3.5 A | _ | 1.6 | 2.0 | Ω |

6.2. Dynamic Characteristics (Ta = 25 °C unless otherwise specified)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--------------------------------|------------------|---|-----|------|-----|------|
| Input capacitance | C _{iss} | V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz | _ | 1350 | _ | pF |
| Reverse transfer capacitance | C _{rss} | | _ | 10 | _ | |
| Output capacitance | C _{oss} | | _ | 110 | _ | |
| Gate resistance | r _g | V _{DS} = OPEN, f = 1 MHz | _ | 4.0 | _ | Ω |
| Switching time (rise time) | t _r | See Fig. 6.2.1. | _ | 20 | _ | ns |
| Switching time (turn-on time) | t _{on} | | _ | 55 | _ | |
| Switching time (fall time) | t _f | | _ | 15 | _ | |
| Switching time (turn-off time) | t _{off} | | _ | 85 | _ | |
| MOSFET dv/dt ruggedness | dv/dt | V_{DD} = 0 to 400 V, I _D = 7 A | 20 | | _ | V/ns |

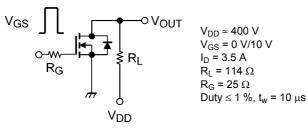


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25$ °C unless otherwise specified)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|------------------|---|-----|------|-----|------|
| Total gate charge (gate-source plus gate-drain) | Qg | $V_{DD} \approx 400 \text{ V}, \text{ V}_{GS}$ = 10 V, I _D = 7 A | _ | 32 | — | nC |
| Gate-source charge 1 | Q _{gs1} | | _ | 10 | _ | |
| Gate-drain charge | Q _{gd} | | | 12 | _ | |

6.4. Source-Drain Characteristics (T_a = 25 °C unless otherwise specified)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-------------------------------|------------------|--|-----|------|------|------|
| Diode forward voltage | V _{DSF} | I _{DR} = 7 A, V _{GS} = 0 V | _ | _ | -1.7 | V |
| Reverse recovery time | | I _{DR} = 7 A, V _{GS} = 0 V | _ | 1100 | _ | ns |
| Reverse recovery charge | Q _{rr} | -dI _{DR} /dt = 100 A/μs | _ | 8 | _ | μC |
| Peak reverse recovery current | I _{rr} | | _ | 18 | _ | А |

7. Marking (Note)

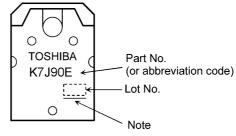


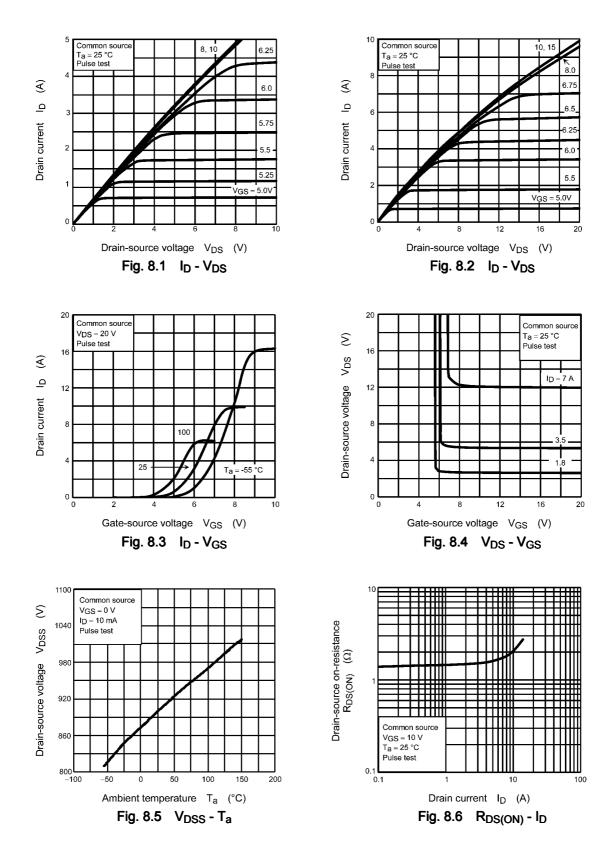
Fig. 7.1 Marking

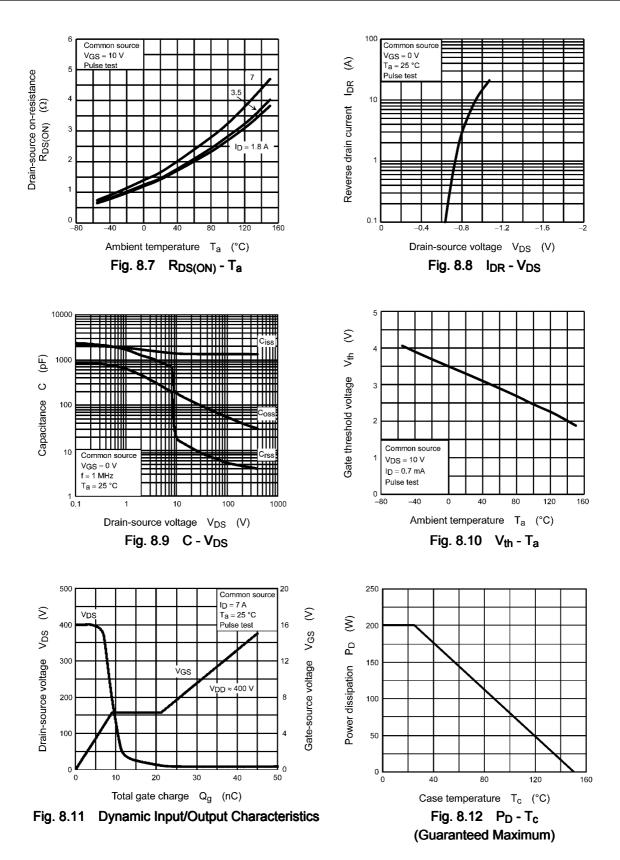
 Note:
 A line under a Lot No. identifies the indication of product Labels.

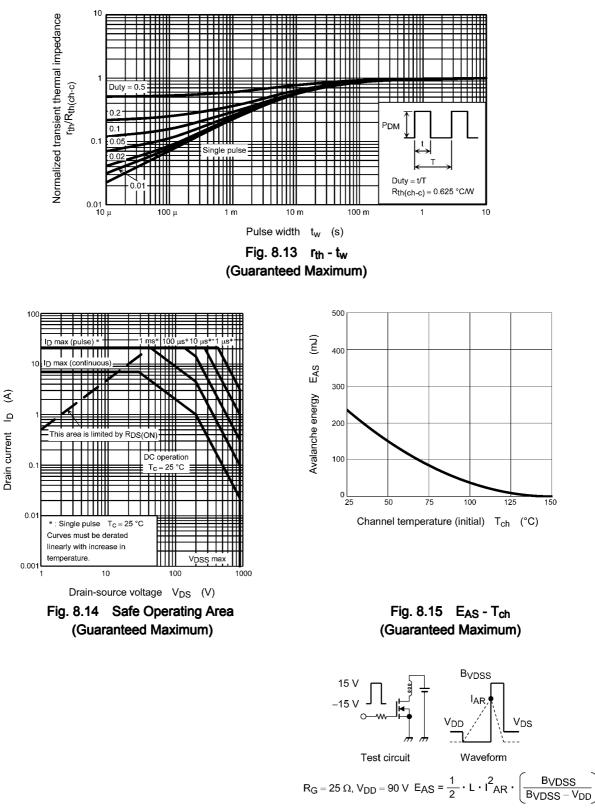
 Not underlined: [[Pb]]/INCLUDES > MCV
 Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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8. Characteristics Curves (Note)







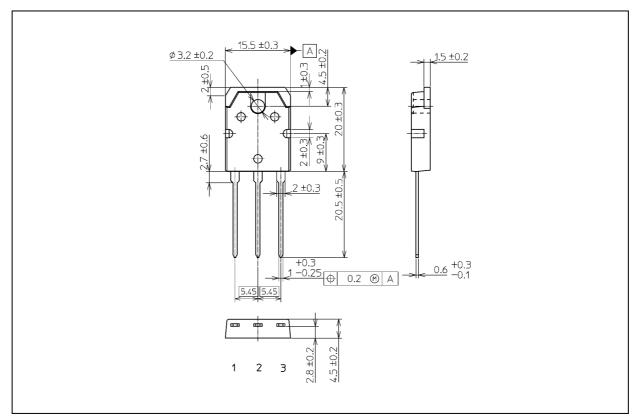


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

TK7J90E

Package Dimensions

Unit: mm



Weight: 4.6 g (typ.)

| Package Name(s) | |
|--------------------|--|
| JEITA: SC-65 | |
| TOSHIBA: 2-16C1S | |
| Nickname: TO-3P(N) | |

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