

RQ6E030SP

## Pch -30V -3A Small Signal MOSFET

## Datasheet

V <sub>DSS</sub>	-30V
R <sub>DS(on)</sub> (Max.)	80mΩ
I <sub>D</sub>	±3A
P <sub>D</sub>	1.25W

## Features

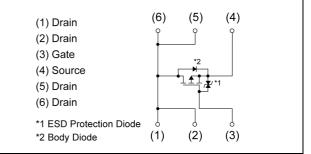
Application

Switching

- 1) Low on resistance
- 2) Built-in G-S protection diode
- 3) Small surface mount package(TSMT6)
- 4) Pb-free lead plating ; RoHS compliant

● Outline	
SOT-457T	
SC-95	
TSMT6	(1)

#### ●Inner circuit



## Packaging specifications

Туре	Packing	Embossed Tape
	Reel size (mm)	180
	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TR
	Marking	TN

## • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-30	V
Continuous drain current	I <sub>D</sub>	±3	А
Pulsed drain current	I*1	±12	А
Gate - Source voltage	V <sub>GSS</sub>	±20	V
Dower dissinction	P <sub>D</sub> *2	1.25	W
Power dissipation	P <sub>D</sub> *3	0.95	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

#### •Thermal resistance

Deremeter	Symbol	Values			Linit
Parameter		Min.	Тур.	Max.	Unit
Thermal resistance in action exclinate	$R_{thJA}^{*2}$	-	-	100	°C/W
Thermal resistance, junction - ambient	$R_{thJA}^{*3}$	-	-	132	°C/W

## •Electrical characteristics (T<sub>a</sub> = 25°C)

Deverseter	Currente e l	Candiliana	Values			1.1:4	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = -1mA$		-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = -1mA$ referenced to 25°C		-	-20.7	-	mV/°C	
Zero gate voltage drain current	$I_{DSS}$ $V_{DS}$ = -30V, $V_{GS}$ = 0V		-	-	-1	μA	
Gate - Source leakage current	kage current $I_{GSS}$ $V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -1mA	-1.0	-	-2.5	V	
		I <sub>D</sub> = -1mA referenced to 25°C	-	3.1	-	mV/°C	
		V <sub>GS</sub> = -10V, I <sub>D</sub> = -3A	-	60	80		
Static drain - source on - state resistance	$R_{DS(on)}^{*4}$	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3A	-	90	125	mΩ	
		V <sub>GS</sub> = -4.0V, I <sub>D</sub> = -1.5A	-	100	140		
Forward Transfer Admittance	Y <sub>fs</sub>   <sup>*4</sup>			-	-	S	

\*1 Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

\*2 Mounted on a ceramic boad (30×30×0.8mm)

\*3 Mounted on a FR4 (25×25×0.8mm)

\*4 Pulsed





# • Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Cump of	Conditions		Linit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	C <sub>iss</sub> V <sub>GS</sub> = 0V		440	-	
Output capacitance C <sub>oss</sub>		V <sub>DS</sub> = -10V	-	110	-	pF
Reverse transfer capacitance	verse transfer capacitance C <sub>rss</sub>		-	80	-	
Turn - on delay time	$t_{d(on)}^{*4}$ V <sub>DD</sub> ~ -15V,V <sub>GS</sub> = -10V		-	10	-	
Rise time t <sub>r</sub> *4		I <sub>D</sub> = -1.5A	-	13	-	20
Turn - off delay time	$t_{d(off)}^{*4}$	R <sub>L</sub> ≃ 10Ω	-	40	-	ns
Fall time	$t_{f}^{*4}$	R <sub>G</sub> = 10Ω	-	12	-	

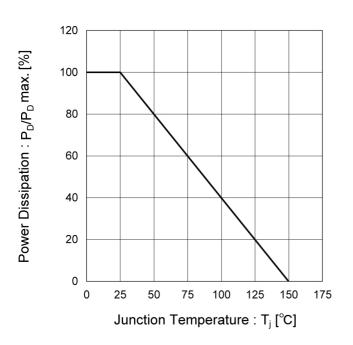
## • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg <sup>*4</sup>	V <sub>DD</sub> ≃ -15V,	-	6.0	-	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = -3A, V <sub>GS</sub> = -5V	-	1.6	-	nC
Gate - Drain charge	${\sf Q}_{\sf gd}{}^{*4}$	V <sub>GS</sub> = -5V	-	2.0	-	

# •Body diode electrical characteristics (Source-Drain) ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ <sub>s</sub>	$T = 25^{\circ}$	-	-	-1	А
Pulse forward current	$I_{SP}^{*1}$	T <sub>a</sub> = 25°C	-	-	-4	А
Forward voltage	$V_{SD}^{*4}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A	-	-	-1.2	V





### Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

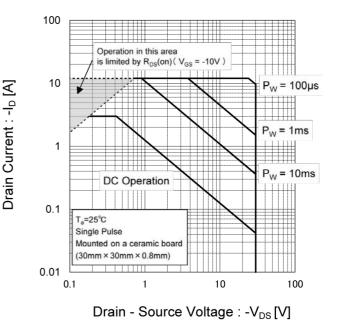
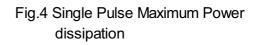
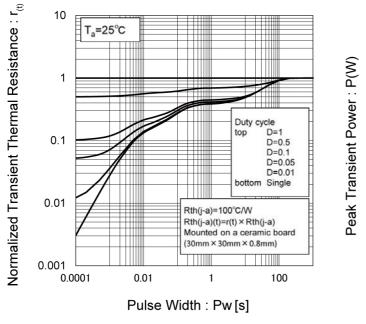
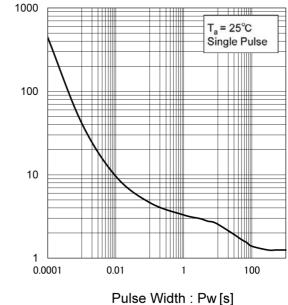


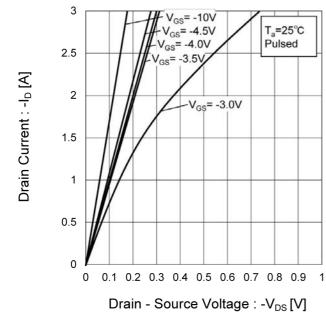
Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width









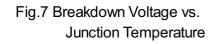


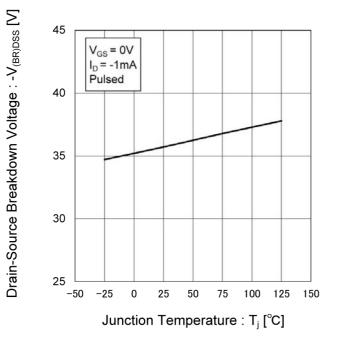
### Fig.5 Typical Output Characteristics(I)

3 T<sub>a</sub>=25°C  $V_{GS} = -10V$ Pulsed V<sub>GS</sub>= -4.5V 2.5 V<sub>GS</sub>= -4.0V V<sub>GS</sub>= -3.5V V<sub>GS</sub>= -3.0V 2 1.5 1 0.5 0 0 1 2 3 4 5 6 7 8 9 10 Drain - Source Voltage : -V<sub>DS</sub> [V]

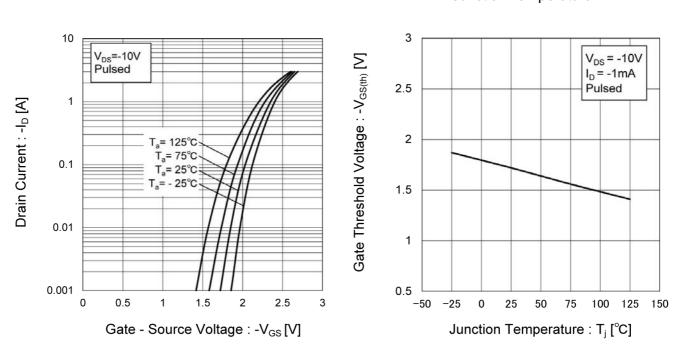
Drain Current : -I<sub>D</sub> [A]

## Fig.6 Typical Output Characteristics(II)









## Fig.8 Typical Transfer Characteristics

Fig.9 Gate Threshold Voltage vs. Junction Temperature





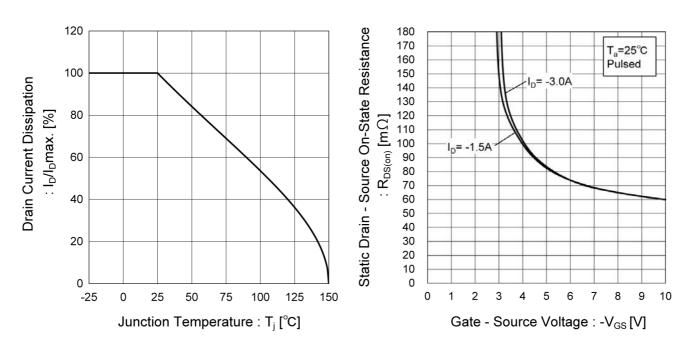
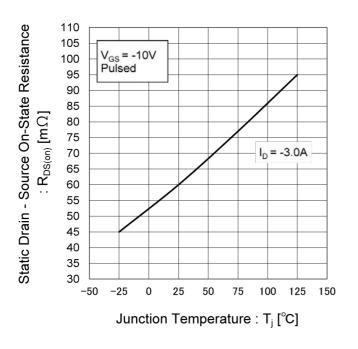


Fig.10 Drain Current Derating Curve

Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.12 Static Drain - Source On - State Resistance vs. Junction Temperature





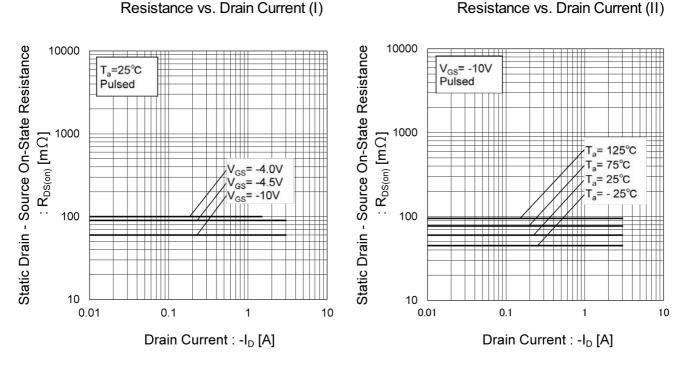


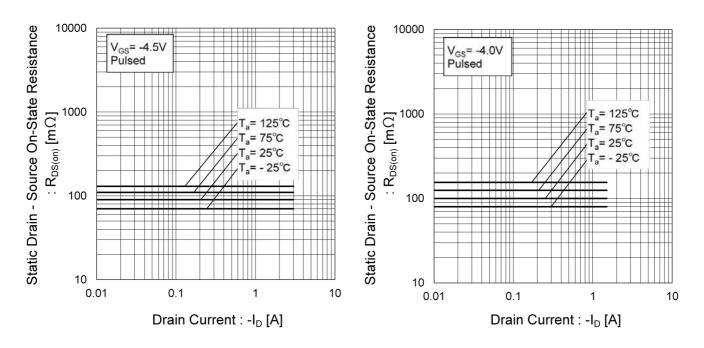
Fig.13 Static Drain - Source On - State Resistance vs. Drain Current (I)

Fig.15 Static Drain - Source On - State

Resistance vs. Drain Current (III)

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (IV)

Fig.14 Static Drain - Source On - State





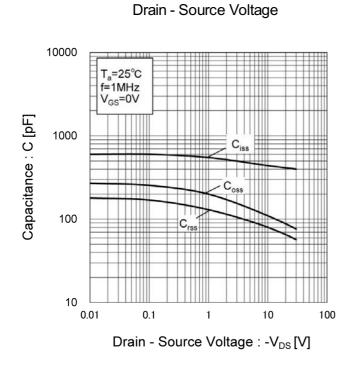


Fig.17 Typical Capacitance vs.

## Fig.18 Switching Characteristics

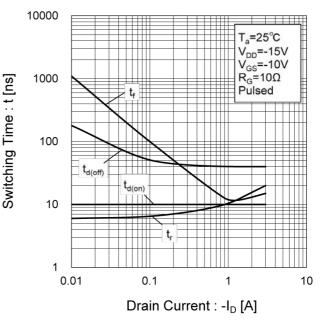
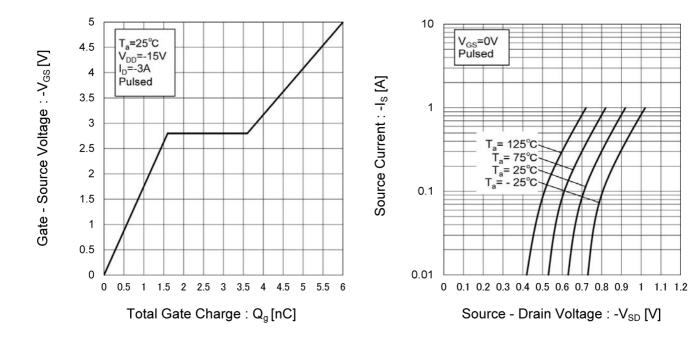


Fig.19 Dynamic Input Characteristics

Fig.20 Source Current vs. Source Drain Voltage





#### Measurement circuits



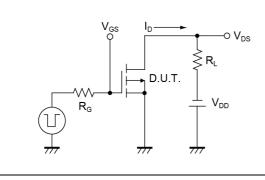


Fig.2-1 Gate Charge Measurement Circuit

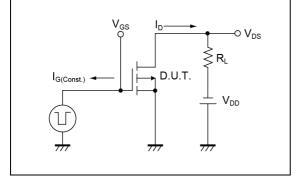
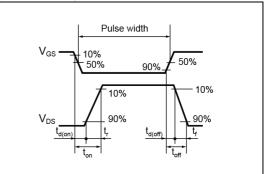
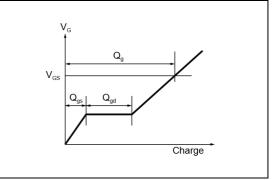


Fig.1-2 Switching Waveforms







## Notice

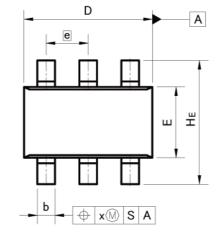
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

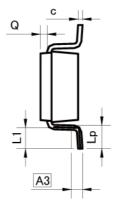


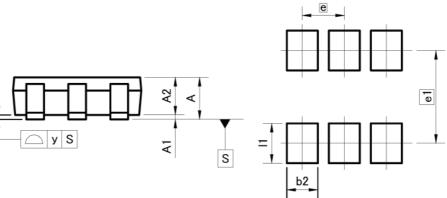


#### Dimensions









Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
	MIN	MAX	MIN	MAX	
A	8 <del>4</del>	1.00	-	0.039	
A1	0.00	0.10	0.000	0.004	
A2	0.75	0.95	0.030	0.037	
A3	0.	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.10	0.26	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.95		0.037		
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.05	0.25	0.002	0.010	
x	-	0.20	-	0.008	
У	1	0.10	-	0.004	
	MILIM	ETERS	INC	HES	
DIM -	MIN	MAX	MIN	MAX	
b2	]	0.70	-	0.028	
e1	2.	10	0.0	83	
		V-CEALSTICKY		NOTICE AND ADDRESS OF A	

Dimension in mm/inches

-

11



0.90

-



0.035

# Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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