Bluetooth[®] Low Energy IoT Development Kit (B-IDK) Getting Started Guide

INTRODUCTION

This document helps you get started with the Bluetooth Low Energy IoT Development Kit (B–IDK). The B–IDK is a comprehensive node–to–cloud and a modular IoT platform that allows development of various BLE based use cases. Along with the hardware and software, the B–IDK includes a mobile app to interact with sensors and actuators.

The B–IDK features RSL10, Industry's lowest power Bluetooth 5 SoC and comprises of a baseboard (BDK–GEVK) and several sensor and actuator daughter cards. For a complete listing of available daughter cards, please visit https://www.onsemi.com/B–IDK. The daughter cards connect to the baseboard, via the two PMOD connectors and/or the Arduino connector to enable various use cases.

Scope

This document covers the hardware setup, software architecture, B–IDK documentation and provides instructions on downloading firmware to the board. The details regarding the mobile app and cloud connectivity are not covered in this document.

HARDWARE

- BDK-GEVK B-IDK Baseboard
- Daughter Cards Optional
- BDK-DCDC-GEVB Power Shield For Use With Higher Power Daughter Cards Optional

Default Configuration

The BDK-GEVK is shipped with the following jumper configuration. As the board supports OBD, there is no need for an external debugger. In case an external debugger is used, connect it to SWD header, J6.

Powering the Board

Multiple options are available to power the BDK-GEVK.

- USB
- Coin Cell (CR2032)
- External AC/DC Adapter plus power shield (BDK–DCDC–GEVB)
- External Supply

When higher power daughter cards (listed below) are attached to the baseboard, external supply either using the power shield or direct is required.

Higher Power Daughter Cards

- D-LED-B-GEVK Dual LED Ballast
- D-STPR-GEVK Dual Stepper Motor Driver
- BLDC-GEVK BLDC Motor Driver



ON Semiconductor®

www.onsemi.com

EVAL BOARD USER'S MANUAL

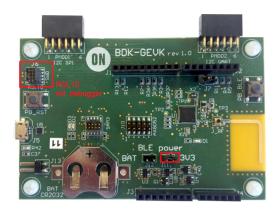


Figure 1. Board Photo

USB

The B–IDK can be powered via the USB port when the use case doesn't need any higher power daughter cards. An example configuration with the baseboard and a couple of sensor boards is shown below.



Coin Cell

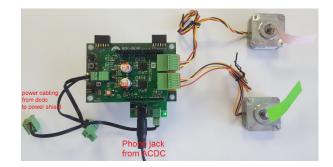
Once the firmware is flashed onto the baseboard, a coin cell (CR2032) may be used to power the system. Similar to USB based power supply, this method of powering is for use cases that don't utilize the higher power daughter cards. The jumper configuration must match the below table to allow for various power modes.

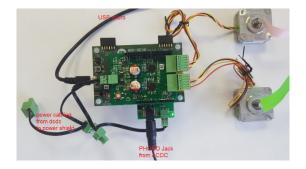
Table 1. JUMPERS

J11	J12	Usage
IN	Х	Programming and Power over USB
X	IN	After programming. Only RSL10 is powered.
IN	IN	After programming. Both RSL 10 and OBD Microcontroller are powered

External AC/DC Adapter Plus Power Shield (BDK-DCDC-GEVB)

For use cases that utilize higher power daughter cards, an external AC/DC power supply (Ex: SMI24–12–V–P6) plus the power shield (BDK–DCDC–GEVB) are needed to power the system. While the 3.3 V supply to the baseboard is provided by the power shield via the Arduino connector, power cables (Green connector) are required between BDK–DCDC–GEVB and the higher power daughter card. For firmware flashing and debugging, the USB cable may be plugged in simultaneously with this mode as shown below.





External Supply

The B–IDK can be powered by an external supply via J13. In this mode, the battery cannot be installed. Jumpers J11 and J12 must be installed.

SOFTWARE

The B-IDK software allows for rapid development of various use cases. This section details the prerequisites and detailed steps in downloading firmware onto the baseboard.

Prerequisites

- 1. Install 64-bit version of Java from https://www.java.com/en/download/
- 2. Install J-Link Version 6.32f or later from https://www.segger.com/downloads/jlink (select J-Link software and documentation pack)
- 3. Download and install"On Semiconductor IDE Installer" from
 - https://www.onsemi.com/PowerSolutions/product.do?id=RSL10
 - a. Download the RSL10 SDK Getting Started Guide and RSL10 CMSIS pack under "RSL10 Software Package" from the above site. All of these are highlighted in the picture below. Save the CMSIS pack in a folder, for example, C:\cmsis_packs



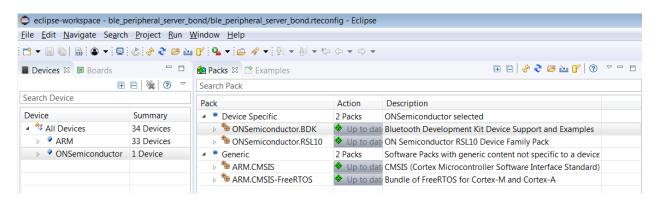
- 4. Download the B-IDK CMSIS pack from https://www.onsemi.com/B-IDK and save it in the same folder as the RSL10 CMSIS pack (see 3.a above)
- 5. CMSIS pack at item 4. is dependent on ARM CMSIS pack as well. Please install ARM CMSIS pack 5.5.1 or higher after download from: <u>https://github.com/ARM-software/CMSIS_5/releases</u>
- 6. CMSIS pack at item 4. is also dependent on ARM CMSIS FreeRTOS version 10.2.0 or higher for users exposed to design the code under FreeRTOS with RSL10: <u>https://github.com/ARM-software/CMSIS-FreeRTOS/releases</u>

The next section provides details on importing the downloaded CMSIS packs into the SDK.

Importing CMSIS Packages

1. Launch the RSL10 SDK ON Semiconductor IDE

- NOTE: Please import RSL10 CMSIS pack first as the B-IDK CMSIS pack (step 4 in the Prerequisites section) depends on the RSL10.
 - 2. Refer to Chapter 3 of RSL10 SDK Getting Started Guide (step 3.a) for step-by-step instructions on importing the CMSIS packs.
 - 3. Once all packs are successfully imported, they can be viewed in the CMSIS pack manager perspective as shown below.

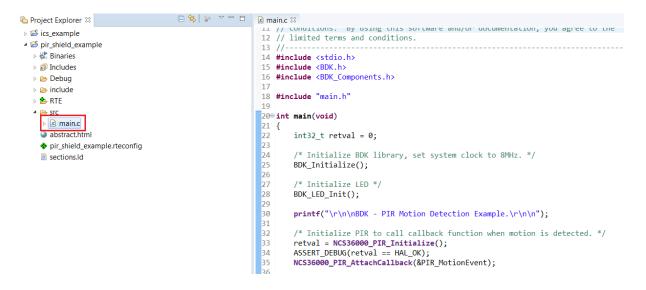


Compiling and Flashing

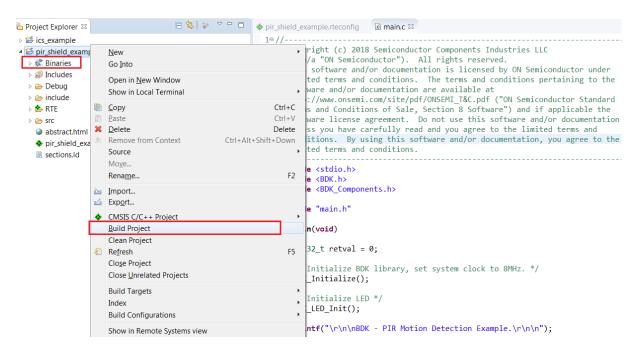
1. Choose an example (for example, pr_shield_example) to flash by copying it to the workspace.

eclipse-workspace -	Eclipse			
<u>File Edit N</u> avigate :	Search Project Run Window Help			
🖻 🕶 🔛 🚳 🛯 📾 🗖	• 🕒 👌 🐂 🍣 🐸 🗞 🔤 💁 🕶 😂 🖉	• [st] • 問 • ♡ ↔ • → •		Quick Access
Devices Boards	E E 🛛 🙀 🔨 🗆 🖬	😢 Packs 📑 Examples 😫	🗆 Only show examples from installed packs 🛛 🕐 🍣 😂 💝 🖻 🛡	🗄 🗏 Pack Properties 🛛 🗎 🗄 🕐 💎
type filter text		Search Example		type filter text
Board	Summary	Example Action	Description	
🔺 🍄 All Boards	2 Boards	Battery Service Example (Bluetooth Devel 💠 Cor	Example usage of BDK Battery Service Profile	Boards
Bluetooth Dev	elopm RSL10	BDK Push Button Example (Bluetooth Dev 💠 Co	sy Simple example on how to generate events when on-boa	Components
RSL10 Evaluat	ion Bo RSL10	bdk_blinky (Bluetooth Development Kit) 💠 Cor	by Example that blinks the on-board LED	Examples
		BME680 Sensor Example (Bluetooth Deve & Co	Uses BME680 located on MULTI-SENSE-GEVB to measure	 Battery Service Example (Bluetooth Development Kit)
		BN0055 Sensor Example (Bluetooth Deve 💠 Cor	Uses BNO055 located on MULTI-SENSE-GEVB to determin	 BDK Push Button Example (Bluetooth Development Kit)
		Custom Service Firmware (Bluetooth Deve 💠 Co	exposes sensor data over Cusrom Service BLE Profile.	 Bdk_blinky (Bluetooth Development Kit)
		IDK Custom Service Example (Bluetooth E 🍫 Cop	y Example usage of IDK Custom Service Profile	BME680 Sensor Example (8 Gollapse Selected
		LED Ballast Shield Example (Bluetooth De 💠 Co	y Control two LEDs connected to D-LED-B-GEVK	
		NOA1305 Sensor Example (Bluetooth Dev 🔷 Cop	Measure Ambient Light levels by using NOA1305 sensor o	BNO055 Sensor Example (B Copy
		PIR Shield Example (Bluetooth Developm 💠 Co	y Example that blinks on-board LED when motion is detect	 Custom Service Firmware (Bluetooth Development Kit)
		Software Timer Example (Bluetooth Deve 💠 Cop	Example showing Ticker, Timeout and Timer use cases of	 IDK Custom Service Example (Bluetooth Development Kit)
		Stepper Shield Example (Bluetooth Devel 💠 Co	control two stepper motors via D-STPR-GEVB	 LED Ballast Shield Example (Bluetooth Development Kit)
				 NOA1305 Sensor Example (Bluetooth Development Kit)
				 PIR Shield Example (Bluetooth Development Kit)
				 Software Timer Example (Bluetooth Development Kit)
				 Stepper Shield Example (Bluetooth Development Kit)

NOTE: Once the example is copied, it can be viewed under Project Explorer. All source files including main are located in the src folder.

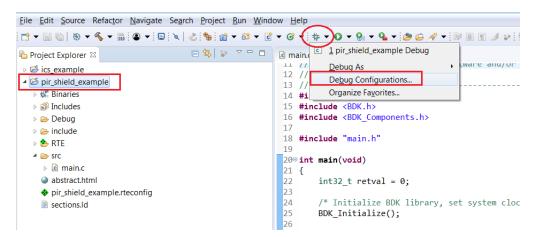


2. Right click and build the project. This creates binaries to be flashed to BDK-GEVK.



NOTE: If the binaries are not seen, press F5 (refresh).

3. Once the build is done, the code is ready to be flashed to the BDK–GEVK. Select the project (pir_shield_example), and go to the debug configurations as shown below.



4. Double click **GDB Segger J–Link Debugging** to create the debug configuration for the selected example.

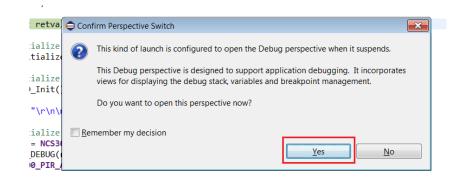
1 圖 ¥ 目 非 ▼ Vue filter text	Name: pir shield example Debug	
	Name: pir shield example Debug	
type filter text		
Spe mer tent	🖹 Main 🚿 Debugger 🕨 Startup 🧤 Source	ce 🗖 Common
C/C++ Application C/C++ Attach to Application	Project:	
C/C++ Postmortem Debugger	pir_shield_example	Browse
C/C++ Remote Application	C/C++ Application:	
GDB Hardware Debugging	Debug\pir_shield_example.elf	
GDB OpenOCD Debugging GDB SEGGER J-Link Debugging		Variables Search Project Browse
c pir_shield_example Debug	Build (if required) before launching	
🔹 Launch Group	Build Configuration: Select Automatically	
Launch Group (Deprecated)		
	Enable auto build	Disable auto build
	Use workspace settings	Configure Workspace Settings

NOTE: The debug configuration for the selected example is automatically saved and there's no need to re-create it.

5. On the **Debugger** tab, set RSL10 as the device name. Click **Debug** to launch the code.

Debug Configurations	
Create, manage, and run configurations	
Image: Second Secon	Name: pir_shield_example Debug Main: \$\$ Debugger \$ Startup \$ Source Common J-Link GDB server Setup \$ Startup \$ Source Connect to running target Executable: \$ Slink.path/\$/Slink.gdbserver/ Browse. Actual executable: \$ C/Program Files (x86)/SEGGER/JLink_V634c//JLinkGDBServer/Lexe Variables. Actual executable: \$ C/Program Files (x86)/SEGGER/JLink_V634c//JLinkGDBServer/Lexe Supported device names Interface: \$ USB \$ P \$ (USB serial or IP name/address) Interface: \$ SVD \$ TAG Initial speed: Auto Adaptiv \$ Fixed 1000 \$ SWO port: 2331 \$ Verify downloads \$ Initialize registers on start
Filter matched 10 of 10 items	Re <u>v</u> ert Apply
0	Close

6. For application debugging, confirm perspective switch by clicking Yes.



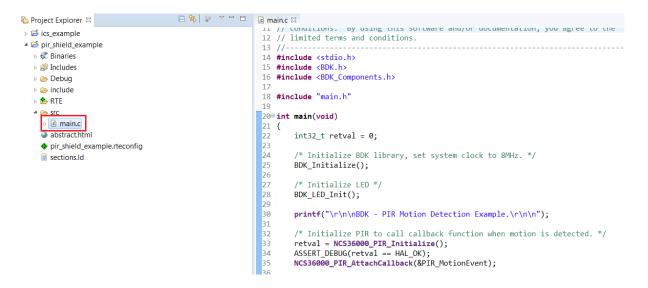
7. The debug session is now launched. Click Resume (F8) to start the target CPU.

Compiling and Flashing

1. Choose an example (for example, pr_shield_example) to flash by copying it to the workspace.

eclipse-workspace - Eclip	pse			
<u>Eile E</u> dit <u>N</u> avigate Se <u>a</u> ro	th <u>P</u> roject <u>R</u> un <u>W</u> indow <u>H</u> elp			
📬 🕶 🔛 🐚 📄 🖨 🕶 🖻 🖻) 👌 🐂 🍣 🐸 🤌 🕍 💁 🗸 ·	• ③ • 答 • · · · · · ·		Quick Access
Devices 🖪 Boards 😫	🗷 E 🕐 🐂 🔍 🗆 🗆	🗶 Packs 📑 Examples 😫	🗆 Only show examples from installed packs 🛛 🔊 🍣 😂 🔶 🔤 🤝 🚍	🖷 Pack Properties 🛙 🔳 🗇 🌣
type filter text		Search Example		type filter text
Board	Summary	Example Action	Description	ONSemiconductor.BDK.0.4.1
🔺 🍄 All Boards	2 Boards	Battery Service Example (Bluetooth Devel 🗇 Copy	Example usage of BDK Battery Service Profile	Boards
Bluetooth Develop	orr RSL10	BDK Push Button Example (Bluetooth Dev 🔶 Copy	Simple example on how to generate events when on-boa	A Components
RSL10 Evaluation E	Bo RSL10	bdk_blinky (Bluetooth Development Kit) 💠 Copy	Example that blinks the on-board LED	Examples
		BME680 Sensor Example (Bluetooth Deve 💠 Copy	Uses BME680 located on MULTI-SENSE-GEVB to measure	 Battery Service Example (Bluetooth Development Kit)
		BNO055 Sensor Example (Bluetooth Deve 💠 Copy	Uses BNO055 located on MULTI-SENSE-GEVB to determin	 BDK Push Button Example (Bluetooth Development Kit)
		Custom Service Firmware (Bluetooth Deve 🔶 Copy	Exposes sensor data over Cusrom Service BLE Profile.	 Bdk_blinky (Bluetooth Development Kit)
		IDK Custom Service Example (Bluetooth E 💠 Copy	Example usage of IDK Custom Service Profile	BME680 Sensor Example (B Collapse Selected
		LED Ballast Shield Example (Bluetooth De 🔶 Copy	Control two LEDs connected to D-LED-B-GEVK	ONSemiconductor::Bluet
		NOA1305 Sensor Example (Bluetooth Dev 🗇 Copy	Measure Ambient Light levels by using NOA1305 sensor o	BNO055 Sensor Example (B Copy
		PIR Shield Example (Bluetooth Developm 🔶 Copy	Example that blinks on-board LED when motion is detect	 Custom Service Firmware (Bluetooth Development Kit)
		Software Timer Example (Bluetooth Deve 💠 Copy	Example showing Ticker, Timeout and Timer use cases of	 IDK Custom Service Example (Bluetooth Development Kit)
		Stepper Shield Example (Bluetooth Devel 🔶 Copy	Control two stepper motors via D-STPR-GEVB	 LED Ballast Shield Example (Bluetooth Development Kit)
				NOA1305 Sensor Example (Bluetooth Development Kit)
				 PIR Shield Example (Bluetooth Development Kit)
				 Software Timer Example (Bluetooth Development Kit)
				 Stepper Shield Example (Bluetooth Development Kit)

NOTE: Once the example is copied, it can be viewed under Project Explorer. All source files including main are located in the src folder.

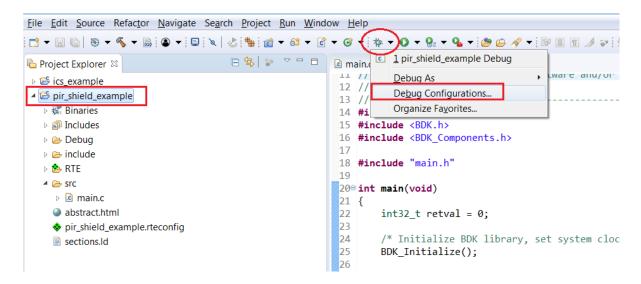


2. Right click and build the project. This creates binaries to be flashed to BDK-GEVK.

🍋 Project Explorer 🛛		□ \$ \$ `	🗸 🗖 🗖 🚸 pir_shield_e	xample.rteconfig 🔒 main.c ⊠
ics_example			19//	
 ▷ ⇔ src ④ abstract.html ♦ pir_shield_exa B sections.ld 		<u>N</u> ew Go Into Open in <u>N</u> ew Window Show in Local Terminal		right (c) 2018 Semiconductor Components Industries LLC /a "ON Semiconductor"). All rights reserved. software and/or documentation is licensed by ON Semiconductor under ted terms and conditions. The terms and conditions pertaining to the ware and/or documentation are available at ://www.onsemi.com/site/pdf/ONSEMI T&C.pdf ("ON Semiconductor Standard
	 □ × × × × 	<u>Copy</u> Paste Delete Remove from Context Source		s and Conditions of Sale, Section 8 Software") and if applicable the
		Move Rena <u>m</u> e Import Exp <u>o</u> rt	F2	
(CMSIS C/C++ Project Build Project	•	n(void)
		Clean Project Refresh Cloge Project Close Unrelated Projects	F5	32_t retval = 0; Initialize BDK library, set system clock to 8MHz. */ _Initialize();
		Build Targets Index Build Configurations	۵ ۱	Initialize LED */ _LED_Init();
		Show in Remote Systems view		<pre>ntf("\r\n\nBDK - PIR Motion Detection Example.\r\n\n");</pre>

NOTE: If the binaries are not seen, press F5 (refresh).

3. Once the build is done, the code is ready to be flashed to the BDK-GEVK. Select the project (pir_shield_example), and go to debug configurations as shown below.



4. Double click GDB Segger J–Link Debugging to create the debug configuration for the selected example.

Debug Configurations	
Create, manage, and run configurations	
Image: Image	Name: pir_shield_example Debug Image: Main Image: Debugger Project: pir_shield_example Browse C/C++ Application: Debug\pir_shield_example.elf Build (if required) before launching Build Configuration: Search Project Btowse Build Configuration: Search Project Btowse Image: Debug\pir_shield_example.elf Variables Search Project Build Configuration: Select Automatically Image: Configure Workspace Settings

NOTE: The debug configuration for the selected example is automatically saved and there's no need to re-create it.

5. On the Debugger tab, set RSL10 as the device name. Click Debug to launch the code.

Cebug Configurations	
Create, manage, and run configurations	-
Create, manage, and run configurations	Name: pir_shield_example Debug Main * Debugger > Startup > Source Common J-Link GDB Server Setup Start the J-Link GDB Server locally Connect to running target Executable: Siglink_gdbserver} Actual executable: (to change it use the global or workspace preferences pages or the global or workspace preferences pages or the global device names Enclanness: Little Big Connection: Connection: USB Initial speed: Auto Adaptiv Fixed SWO port: 2332 Verify downloads Initialize registers on start
Filter matched 10 of 10 items	Reyert Apply
0	Qebug Close

6. For application debugging, confirm perspective switch by clicking Yes.

,

retva.	Confirm Perspective Switch
ialize	This kind of idunct is configured to open the Debug perspective when it suspends.
:ialize _Init()	This Debug perspective is designed to support application debugging. It incorporates views for displaying the debug stack, variables and breakpoint management.
["\r\n\I	Do you want to open this perspective now?
:ialize = NCS30 DEBUG(1 0_PIR_0	

7. The debug session is now launched. Click Resume (F8) to start the target CPU.

eclipse-workspace - pir_shield_example/src/main.c - Eclipse				
Eile Edit Source Refactor Navigate Search Project Run Window Help				
🗂 ▾ 🗟 🐚 🐘 Ø ▼ 🗉 ¥ 🌘)) ■ M 3. 🦘 🖉 ₩ 🗟 % & ¶ 👘 🖗 ▼ Ø ▼ 💁 ▼ Ø Ø Ø 🔹 Ø	😵 🌒 🕶 🕅 🕶 🏷 🗢 🖛	- <> -		Quick A
* Debug 🛙 Resume (F8)	🔆 🙀 🖓 🗖 🗖	🕬 Variables 🕴 💁 Breakpo	ints 🕮 Registers 🛋 Modules 强 Periphe	rals 🕹 🗷
Ic pir_shield_example Debug [GDB SEGGER J-Link Debugging]		Name	Туре	Value
A 🔐 pir_shield_example.elf		00- retval	int32_t	-536813312
A 🧬 Thread #1 57005 (Suspended : Breakpoint)				
main() at main.c:22 0x10025a				
JLinkGDBServerCLexe				
arm-none-eabi-gdb				
Semihosting and SWV				
🖻 main.c 🕴 📧 0x0				🗄 Outline 😂 📃 🗎 🗮 🕷 🕷
11 // conditions. by using this software and/or documentation, you agree to the 12 // limited terms and conditions.			*	stdio.h
12 // Timited terms and conditions.				BDK.h
14 #include <stdio.h></stdio.h>				BDK_Components.h
15 #include <bdk.h></bdk.h>				main.h
16 #include <bdk_components.h></bdk_components.h>			=	 main(void) : int
17 18 #include "main.h "				PIR_MotionEvent(void) : void
19				
200 int main(void)				
21 {				
<pre> 22 int32_t retval = 0; </pre>				
23 24 /* Initialize BDK library, set system clock to 8MHz. */				
25 BDK Initialize():				
26				
4			F.	1
📮 Console 🛿 🧟 Tasks 🔝 Problems 🕖 Executables 🍠 Terminal 🖳 Debugger Console 🔋 Memory				= × 🔌 🔒 🖬 🖗 🗲 🗷
pir_shield_example Debug [GDB SEGGER J-Link Debugging] JLinkGDBServerCL.exe				
Read 2 bytes @ address 0x00102988 (Data = 0x4D3C) Read 4 bytes @ address 0x00102AAC (Data = 0x001053D4)				
Read 2 bytes (address $0x00102AAC$ (Data = $0x001033D4$) Read 2 bytes (address $0x001029BA$ (Data = $0x483D$)				
Read 4 bytes @ address 0x00102AB2 (Data = 0x00042000)				
Read 2 bytes @ address 0x001029BC (Data = 0x4295)				
Read 2 bytes @ address 0x001029BE (Data = 0x4A3D)				
Read 4 bytes @ address 0x00102AB6 (Data = 0x53D02000)				
Read 2 bytes @ address 0x001029C0 (Data = 0xD908)				

Logging/Debugging

The following options are available to log/debug the downloaded firmware:

- Eclipse RTT Console
- J-Link RTT
- AX8052F100 UART-SPI bridge

This section provides instructions for each of the above options.

Using Eclipse Console

1. Click the Open a Terminal Icon

eclipse-workspace - Source not found Eclipse	
<u>File Edit Navigate Search Project Run Winde</u>	w <u>H</u> elp
: ➡ - 🖫 🐚 📾 : ● - (□) 🔍 D= 14 3.	≳
☆ Debug 🛛 Open a Terminal (Ctrl+A	lt+Shift+T)
▲ ⓒ pir_shield_example Debug [GDB SEGGER J-Lin	(Debugging]
# Zer pir_shield_example.elf	
🔎 Thread #1 57005 (Running : User Request)
JLinkGDBServerCL.exe	
📕 arm-none-eabi-gdb	
📕 Semihosting and SWV	٠

2. Enter the values shown below and launch the session. The incoming events are printed on the terminal window.

Caunch Terminal		
Choose terminal:	Telnet Terminal 🔹	
Settings		
Hosts:	▼ 🗶	
Host:	localhost	
Port:	19021 -	
Timeout (sec):	5	
End of Line:	CR+NUL 🔻	
Encoding: Default (ISO-8859-1)		
?	OK Cancel	

File Edit Navigate Search Project Run Window Help	
me ear havigate sealer hojeet kan window heip ™ = % ∰ @ = @ % № ■ # % % % % !!!! ₩ ≅ % & % % + Ø	
存 Debug ⊠	🔆 🖬 🐨 🗖 🔅 Vari
Image: Contemporary Contempo	Name
 # 2 pir_shield_example.elf 	
P Thread #1 57005 (Running : User Request)	
JLinkGDBServerCL.exe	
arm-none-eabi-gdb	
Semihosting and SWV	
	٠
Configure when this editor is shown Preferences	
🗉 Console 🧟 Tasks 🔝 Problems 🕡 Executables 🐙 Terminal 🛱 🗟 Debugger Console	
E Telnet localhost (8/27/18 8:50 AM)	
SEGGER J-Link V6.34c - Real time terminal output	
J-Link OB-SAM3U128 V3 compiled Jul 12 2018 12:17:50 V3.0, SN=48303 Process: JLinkGDBServerCL.exe	0634
PIR: Motion detected at 368662 ms.	
PIR: No motion at 370868 ms.	
PIR: Motion detected at 518577 ms.	
PIR: No motion at 520783 ms.	

PIR: No motion at 520783 ms. PIR: Motion detected at 521608 ms. PIR: No motion at 523814 ms.

Using Eclipse Serial Console via UART-SPI Bridge.

When you do not want to use the Segger RTT viewer as serial console, the BDK–GEVK board is equipped with UART–SPI uC AX8052F100 flashed with special firmware, taking care of the entire serial communication with values returned on Terminal.

3. Click on the example's rteconfig file and choose under *Device/BDK/Output redirection*, SPI Bridge AXEM. Save, compile and flash the whole project.

➡ 🔜 💿 🔹 🔦 🕶 🗟 🕶		° - 0	१ • † • 0 • 9⊨ • 9	• 😕 🗁 🛷 🕶		鉛 ▼ 剤 ▼ や ◆ ▼ ◆ ▼				
Project Explorer 🛛 👘	bme680_example.rtec	onfig 8	3						- 0	문
🖹 😫 🔛	Components I F	esolve							? 🔛	4.0
bdk_ics_firmware bme680 example	Software Components	Sel	Variant	Vendor	Version	Description				An
Binaries	RSL10			ONSemiconduc		ARM Cortex-M3 48 MHz, 32 kB F	AM, 384 kB ROM			
▷ S Includes	BLE		BDK	ONSemiconduc	1.1.1	RSL10 BLE stack implementation	s for BDK based applicatio			
Debug	Board Support		BDK-GEVK	ONSemiconduc	1.1.1	Board Support package for BDK	-GEVK evaluation board.			
include	Components					Platform independent drivers for	r various external componei			
🖻 🎃 RTE	A I Device									
Src	🔺 🌳 BDK					BDK software components comp	patible with all evaluation b		=	
abstract.html	AES			ONSemiconduc	1.1.1	AES module from mbedTLS				
bme680_example.rteconfig	Event Callba			ONSemiconduc	1.1.1	Library for assigning of multiple	callbacks to events.			
sections.ld	HAL			ONSemiconduc		Peripheral HAL drivers and RSL1				
	Output Red		SPI Bridge Axem	ONSemiconduc		Redirects standart output calls u				
	Scheduling		SEGGER RTT	VSemiconduc	1.1.1	Management layer for Event Ker	nel Application Task		_	
	Software Ti		SPI Bridge Axem	NSemiconduc	1.1.1	Allows to create multiple timer e	events while using only sing			
	Bluetooth Prof	les	UART						-	
	Validation Output			Description						
	'Invoking: GNU ARM	Cons 80_exa targe Cross		lf'		i = k	EmbSys Registers 88	a chip using the pre Hex	eference pay Bin	je (c₁

4. When the project runs, Click the **Open a Terminal** Icon.

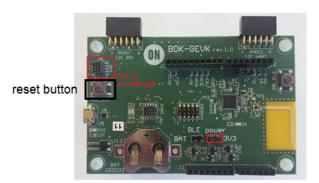
😑 eclipse-workspace - Source not found Eclipse				
<u>File E</u> dit <u>N</u> avigate Search <u>P</u> roject <u>R</u> un <u>W</u> indow <u>H</u> elp				
i 🗂 🗕 🗑 👼 i 👁 √ 💷 🔍 🕨 💷 🛤 32. 🖘 12. i+ 🗮 32	* 🖶 🏷	- 0 -	9 - D	🛷 🕶 🖗
* Debug Ø Open a Terminal (Ctrl+Alt+Shift+T)				
Image: Second				
4 🕃 pir_shield_example.elf				
Thread #1 57005 (Running : User Request)				
JLinkGDBServerCL.exe				
📕 arm-none-eabi-gdb				
Semihosting and SWV	۰			

5. Enter the appropriate COM port as shown below, and launch the session. The incoming events are entered on the terminal window.

¢	Launch Terr	ninal 🗖 🗖 💌
	Choose term	inal: Serial Terminal 🔹
	Settings	
	Serial port:	_
	Baud rate:	115200 ▼
	Data size:	8 🗸
	Parity:	None
	Stop bits:	1
	Encoding:	Default (ISO-8859-1) 🔹
	?	OK Cancel

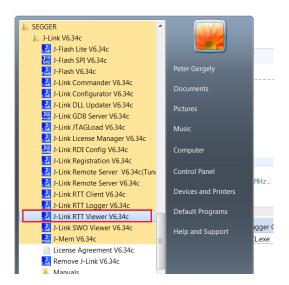
😑 eclipse-workspace - Source not found Eclipse		
File Edit Navigate Search Project Run Window Help		
: 🖆 ▾ 🔛 🐚 🐘 🔍 ▾ 💭 🕨 🖿 🗰 🕺 🔊 🖉 🖄 🗇 🕬 🖄 🖘 🕬 🏕 🔛 🐨 🔛 🐨 🔛 🐨 🔛 🐨 🔛	• * • • •	
眷 Debug ⊠	🔌 ite 🗢 🗆	(x)= Varia
[c] pir_shield_example Debug [GDB SEGGER J-Link Debugging] [c] pir_shield_example.eff [v] Thread = 157005 (Running : User Request) JinkGDBServer(Lexe		Name
📕 arm-none-eabi-gdb		
Semihosting and SWV		
le main.c		
Break at address "0xdeadbeee" with no debug information available, or outside of program code.		
View Disassembly		
view bisossenibly		
Configure when this editor is shown Preferences		
🗳 Console 🧟 Tasks 🔝 Problems 📀 Executables 🖉 Terminal 🕸 🚱 Debugger Console		
E Telnet localhost (8/27/18 8:50 AM)		
SEGGER J-Link V6.34c - Real time terminal output		
J-Link OB-SAM3U128 V3 compiled Jul 12 2018 12:17:50 V3.0, SN=483035634		
Process: JLinkGDBServerCL.exe PIR: Motion detected at 368662 ms.		
PIR: No motion at 370868 ms.		
PIR: Motion detected at 518577 ms.		
PIR: No motion at 520783 ms.		
PIR: Motion detected at 521608 ms.		
PIR: No motion at 523814 ms.		

NOTE: You may reset (PB_RST) the BDK-GEVK (shown below) to launch the RTT terminal without needing to launch Eclipse.



Using J–Link RTT

6. After step 14 is done, open J-Link RTT viewer (should be installed when J-Link software package was installed per Step 2).



7. Select USB and click OK.

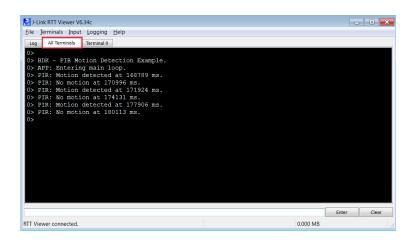
J-Link RTT Viewer V6.34c		
<u>File Terminals Input Loc</u>	🔜 J-Link RTT Viewer V6.34c Configuration 🛛 💦 💌]
Log All Terminals Terr LOG: J-Link RTT Vie LOG: Terminal 0 add	Connection to J-Link USB Serial No LCP/IP Existing Session Specify Target Device Script file (optional)	
	Target Interface & Speed	
	SWD 4000 kHz	
	RTT Control Block Auto Detection <u>A</u> ddress <u>Search Range</u>	
	OK Cancel	Enter Clear
Ready.		0.00 MB

8. RTT prompts you to select the appropriate microcontroller. Select RSL10 and click OK. The serial terminal is ready to use and the events from RSL10 can be observed by clicking the All Terminals Window.

🔜 J-Link RTT Vie	wer V6.34c	X
<u>File</u> <u>Terminals</u>	Input Logging Help	
Log All Termi	inals Terminal 0	
	: RTT Viewer V6.34c: Logging started. al 0 added.	
🛃 J-L	ink V6.34c Device Selection	23
•	The selected device "UNSPECIFIED" is unknown to this version of the J-Link software. Please make sure that at least the core J-Link shall connect to, is selected. Proper device selection is required to use the J-Link internal flash loaders for flash download or unlimited flash breakpoints. For some devices which require a special handling, selection of the correct device is in <u>UNE</u>	iportant.
	E	inter Clear
Ready.	Establishing J-Link connection 0.00 MB	

▼ Device RSL10	Core *	-		endian •
Device	Core	NumCo	Flash size	RAM size
RSL10	Cortex-M3 r2p1	1	390 KB	24 KB
d,modification of flash mem	iory during a debug sessio			ell <u>C</u> ancel
	FSL10 Device RSL10 ror J-Link. the is not required for most d d.modification of flash merei	IRSL10 Core Core RSL10 Cortex-M3 r2p1 Cortex-M3 r2p1 or J-Link. re is not required for most devices, but allows more aff	FRSL10 For a constraint of the set	

🔜 J-Link RTT Viewer V6.34c	- • •
Eile Terminals Input Logging Help	
Log All Terminals Terminal 0	
LOG: SWD speed too high. Reduced from 2667 kHz to 1800 kHz LOG: Scanning AP map to find all available APs LOG: AP[0]: Stopped AP scan as end of AP map has been reac LOG: AP[0]: AHB-AP (IDR: 0x24770011) LOG: Terating through AP map to find AHB-AP to use LOG: AP[0]: Core found LOG: AP[0]: AHB-AP ROM base: 0xE00FF000 LOG: CPUID register: 0x412FC231. Implementer code: 0x41 (A LOG: FPUnit: 2 code (BP) slots and 0 literal slots LOG: CoreSight components: LOG: CoreSight components:	ched
LOG: ROMTb1[0][0]: E000E000, CID: B105E00D, PID: 000BB000 LOG: ROMTb1[0][1]: E0001000, CID: B105E00D, PID: 003BB002 LOG: ROMTb1[0][2]: E0002000, CID: B105E00D, PID: 002BB003 LOG: RTT Viewer connected.	DWT FPB Enter Clear
RTT Viewer connected.	0.000 MB



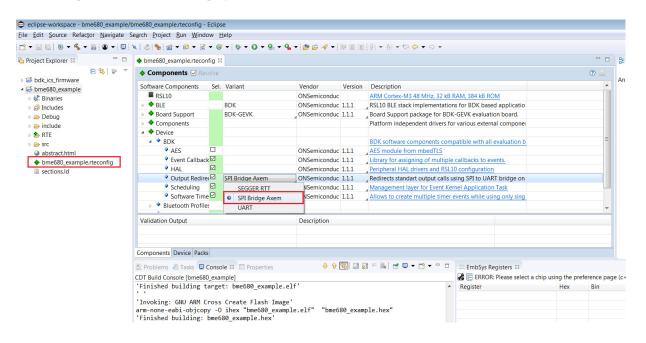
NOTE: You may reset (PB_RST) the BDK-GEVK (shown below) to launch RTT terminal without needing to launch Eclipse.



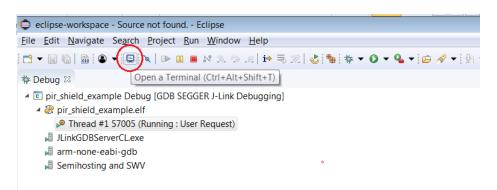
Using Eclipse Serial Console via UART-SPI Bridge

The BDK–GEVK board is equipped with UART–SPI microcontroller AX8052F100 flashed with special firmware, to enable serial communication with values returned to Terminal.

9. Click on example's rteconfig file and choose "SPI Bridge AXEM" under *Device/BDK/Output redirection*. Save, compile and flash the whole project.



10. When the project runs, Click the Open a Terminal Icon.

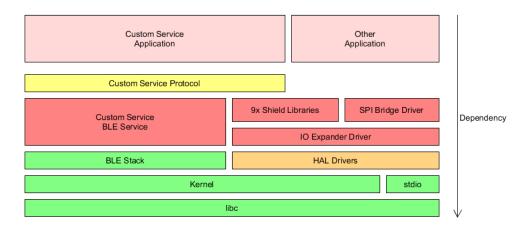


11. Enter the appropriate COM port as shown below and launch the session. The incoming events are printed on the terminal window.

	🖨 Launch Terminal		
	Choose terminal: Serial Ter Settings Serial port Baud rate: 115200 Data size: 8 Parity: None Stop bits: 1 Encoding: Default (ISO-84	• • • • 859-1) •	
 eclipse-workspace - Source not found. File Edit Navigate Search Project Ri Image: Image: Image:	un Window Help ■ 해 & 아내 바 등 있니 같 한 GGER J-Link Debugging]	Cancel 1 * • • • • • • • • : ⊕ • • • : ⊕ : • • •	¥ i i v □ □ 00• Varia Name
main.c COxdeadbeee 33 Break at address "Oxdeadbeee" with no de View Disassembly Configure when this editor is shown Pref	ferences		4
□ Console ② Tasks № Problems ○ Exe □ Tenet localhost (8/27/18 8:50 AM) ☆ SEGGER J-Link V6.34c - Real time J-link 06-SAM3U128 V3 compiled : Process: JLinkGD8ServerCL.exe V1R: Motion detected at 368662 V1R: Motion detected at 518577 r V1R: Nontion at 520783 ms.	e terminal output Jul 12 2018 12:17:50 V3.0, ms.	ger Console	

SOFTWARE ORGANIZATION

For users modifying the example code and building new projects, the following sections detail the B-IDK software organization. The stack overview is shown below.



B-IDK CMSIS Software Organization

CMSIS pack and the associated software components handle multiple evaluation boards as different bundles of the standardized Board Support Cclass.

- This bundle shows only components supported by ON Semiconductor for a given board
- No confusing component variants

Common libraries and HAL are in a separate group within the Device class

Cbundle	Cclass	Cgroup	Csub	Cvariant	Description
BDK-GEVK					Board support package for BDK-GEVK evalution board
		Libraries	1		Board specific libraries
			LED	1	On-board LED support
			Button		On-board push button support
			PCA9655E		16-bit I2C IO Expander library
		IDK Shields			Support for Arduino / PMOD extension boards
			PIR-GEVB	1	PIR Motion detection using NCS36000
			ALS-GEVB	1	Measure Ambient light levels using NOA1305 ambient light sensor
			MULTI-SENSE-GEVB	rev2.1	Combines 3 sensors: BME680, BNO055, NOA1305
			BLDC-GEVK		
			D-LED-B-GEVK	1	
			D-STPR-GEVK	1	
		ICS Protocol			Libraries that allow connected BLE devices to take control over sensors / actuators using ICS Service.
			System Node	1	Protocol implementation and sytem node used by other sensor / actuator nodes.
			PIR Node	1	Exposes motion data provided by NCS36000 from PIR-GEVB
			ALS Node	1	Exposes ambient light levels measured by NOA1305 from ALS-GEVB
			ENV Node	1	Exposes environmental data measured by BME680 from MULTI-SENSE-GEVB
			AO Node	1	Exposes absolute orientation measured by BNO055 from MULTI-SENSE-GEVB
			STPR Node	1	Allows remote control of two stepper motors connected to D-STPR-GEVB.
			LEDB Node	1	Allows remote control of two power LEDs connected to D-LED-B-GEVK
			BLDC Node	1	Allows to remote control BLDC motor connected to BLDC-GEVB.
	Components				Platform independent software drivers for controlling of various external IC.
		LED Driver		_	
			NCV78763]	Dual LED Driver and Power Ballast, for Automotive Front Lighting, 1.6 A, 2nd Generation
		Ambient Light Sensor			
			NOA1305]	Ambient Light Sensor with I2C Interface and DarkCurrent Compensation
		Motor Driver			
			AMIS-3054		Micro-stepping stepper motor driver with SPI interface for bipolar stepper motors
			LV8907UW	J	Sensor-less Three-phaseBrushless DC MotorController, with GateDrivers, for Automotive
		Environmental Sensor			
			bme680	J	Low power gas, pressure, temperature & humidity sensor
		Motion sensor			
			bno055		Intelligent 9-axis absolute orientation sensor
		Touch Sensor			
			LC717A00AR		Capacitance-Digital-Converter for Electrostatic Capacitive Touch Sensors
	Device		•		
		BDK		•	
			HAL	-	RSL10 Peripheral abstraction layers for BDK applications.
			Scheduling	-	Event Kernel wrapper for BDK applications.
			Software Timer	-	Allows to create multiple timer events while using only single hardware timer.
			Event Callback	-	Library for executing multiple event handlers when an event occurs.
			Output Redirection		Redirects standard library output calls (printf,) to specified channel
				SEGGER RTT	Output is transmitted using UART peripheral
			450	UARI	Output is transmitted over SWD using the on-board or external J-LINK deug probe
BDI/	BLE		AES	J	
<u>BDK</u>	BLE	Design and Comme	1		
		Peripheral Server	Pattery Carries	1	Eveness surrent bettery level to connected elient and configuration
			Battery Service ICS Service	-	Exposes current battery level to connected client and application. IDK Custom Service used to transmit sensor data using ICS Protocol library.
			Peripheral Server		
			Peripheral Server		BLE Peripheral Server implementation for BDK applications.

Board Support

- Libraries to support BDK-GEVK, GPIO Expander, Various daughter cards and custom protocol (required for the mobile app)

Components

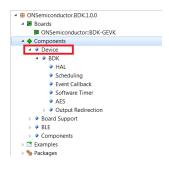
- Libraries attached to board support
- ◆ B ONSemiconductor.BDK.1.0.0

 ▲ Boards
 ■ Components
 ▶ Onsemiconductor.BDK-GEVK

 ◆ Components
 ▶ Board Support
 ▶ Motor Driver
 ▶ Motor Driver
 ▶ Motor Driver
 ▶ Motor Driver
 ▶ LED Driver
 ▶ Ele Driver
 ▶ Ele Driver
 ▶ Ele Driver
 ▶ Motion Sensor
 ▶ Packages
 ■ Sensor
 ■ One Sensor
 ■ Motion Sensor
 ■ Motion Sensor
 ■ Motion Sensor
 ■ Packages
 ■ Packages

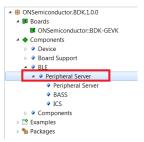
Device

• Abstraction layers for interfaces, timers, AES, serial re-direction, etc.



BLE

• Peripheral Server Support



CONFIGURATION SETUP

System settings can be configured directly from within the CMSIS pack. Each example is equipped with basic system configuration that covers three main categories. These are accessible in the RTE/BDK folder within the project. Each system configuration starts with "RTE_". As shown below, opening the RTE_... header files using the CMSIS configuration wizard (right click on the header file), displays the configuration table. Various application specific parameters can be set. This allows pre-configuration of RSL10 without the need for explicit programming.

Project Explorer 😂		83 ▼ 8 ▼ 6 ▼ 1 巻 ▼ 0 ▼ 9; ▼ 4 ▼ 1 9 ⊕ <i>A</i> ▼ 1 8 ■ 1 2 2 2 2 1 4 7 0 0 ▼ 0 ▼ B mainc ⊠
S noa1305_example	A	19//
Includes		2 // Copyright (c) 2018 Semiconductor Components Industries LLC
include		3 // (d/b/a "ON Semiconductor"). All rights reserved. 4 // This software and/or documentation is licensed by ON Semiconductor under
🔺 🏝 RTE		5 // limited terms and conditions. The terms and conditions pertaining to the
A BDK	utton.c [ONSemiconductor::BDK	6 // software and/or documentation are available at
	[ONSemiconductor::BDK.Librari	7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Semiconductor Standard 8 // Terms and Conditions of Sale, Section 8 Software") and if applicable the
	emiconductor::BDK.Libraries.Co	9 // software license agreement. Do not use this software and/or documentation
	ck.c [ONSemiconductor::BDK.Lit	10 // unless you have carefully read and you agree to the limited terms and 11 // conditions. By using this software and/or documentation, you agree to the
	ONSemiconductor::BDK.Librar	12 // limited terms and conditions.
	[ONSemiconductor::BDK.Librari ONSemiconductor::BDK.Librarie:	13 //
	ONSemiconductor::BDK.Librarie:	14 #include <stdio.h> 15 #include <bdk.h></bdk.h></stdio.h>
HAL_UART.	ONSemiconductor::BDK.Libra	16 #include "BDK_Components.h"
	emiconductor::BDK.Libraries.Co	17 18 #include "main.h "
	[ONSemiconductor::BDK.Librari	18 #include main.n
	ONSemiconductor::BDK.Compo	200 int main(void)
	[ONSemiconductor::BDK.Comp	21 { 22 int32_t status = 0;
RTE_BDK.h	ONSemiconductor::BDK.Librar	23
-	55E.h [ONSemiconductor::BDK.C =	24 BDK_Initialize(); 25
	reTimer.h [ONSemiconductor::B T_printf.c [ONSemiconductor::B[<pre>26 printf("\r\nAPP: NOA1305_ALS example.\r\n");</pre>
	T.c [ONSemiconductor::BDK.Libi	27
	ner.c [ONSemiconductor::BDK.Li	<pre>28 status = NOA1305_ALS_Initialize(); 29 ASSERT DEBUG(status == 0);</pre>
	[ONSemiconductor::BDK.Librari	30
	VSemiconductor::BDK.Libraries.S ger_rtt.c [ONSemiconductor::BD	<pre>31 status = NOA1305_ALS_StartContinuous(APP_ALS_PERIODIC_INTERVAL_MS, 32 &&ALS ReadCallback);</pre>
	xx.c [ONSemiconductor::BDK.Lib	<pre>33 ASSERT_DEBUG(status == 0);</pre>
▲ BDK ▷ 🔒 BDK_Pus	hButton.c [ONSemiconducto	5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Se
 ■ BDK 	kc [ONSemiconductor::BDK. INSemiconductor::BDK.Libra Ibackc [ONSemiconductor::l ckc [ONSemiconductor::BDK or.c [ONSemiconductor::BDK.Li c [ONSemiconductor::BDK.Li	m::BDK 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set librarines.Co 8 // Terms and Conditions of Sale, Section 8 Software") and 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //
■ BDK ■ BDK_Pus ■ BDK_Tas ■ BDK_Tas ■ BDK.c [C ■ EventCal ■ HAL_clo ■ HAL_clo ■ HAL_2CC ■ HAL_SPI	kc [ONSemiconductor::BDK. INSemiconductor::BDK.Librai Ibackc [ONSemiconductor::f ckc [ONSemiconductor::BDK or.c [ONSemiconductor::BDK.Li c [ONSemiconductor::BDK.Li c [ONSemiconductor::BDK.Li	5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Se 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentation 12 // limited terms and conditions. 13 //
	kc [ONSemiconductor::BDK. INSemiconductor::BDK.Librai Ibackc [ONSemiconductor::B ckc [ONSemiconductor::BDK or.c [ONSemiconductor::BDK.Li c [ONSemiconductor::BDK.Li c [ONSemiconductor::BDK.Li RT.c [ONSemiconductor::BDK.Li	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Se 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software" 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentation 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK. INSemiconductor::BDK.Librar Ibackc [ONSemiconductor::BDK or.c [ONSemiconductor::BDK or.c [ONSemiconductor::BDK.Librar c [ONSemiconductor::BDK.Librar RT.c [ONSemiconductor::BDK.Librar	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Se 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software" 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentation 12 // limited terms and conditions. 13 //</pre>
■ BDK ■ BDK-Pus ■ BDK-Pus ■ BDK-Tas ■ BDK-C ■ BDK-C ■ BDK-C ■ BDK-C ■ BDK-C ■ BDK-C ■ BDK-Pus ■ BD	kc [ONSemiconductor::BDK. INSemiconductor::BDK.Libral Ibackc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK.Li cc [ONSemiconductor::BDK.Libral NSemiconductor::BDK.Libral New	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Se 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
▲ BDK ■ BDK ■ BDK ■ BDK-Pus ■ BDK-Tas ■ BDK.c [C ■ BDK.c [C ■ BDK.c [C ■ AL_cl ■ AL_	kc [ONSemiconductor::BDK.Libra INSemiconductor::BDK.Libra Ibackc [ONSemiconductor::BDK or.c [ONSemiconductor::BDK or.c [ONSemiconductor::BDK c [ONSemiconductor::BDK.Libra Newigpen	<pre>5 // limited terms and conditions. The terms and condition cr:BDK Librari ries.Co BDK.Lit Librari fies.Co BDK.Lit Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librari Librar</pre>
	kc [ONSemiconductor::BDK.Libra INSemiconductor::BDK.Libra Ibackc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK c [ONSemiconductor::BDKLI RT.c [ONSemiconductor::BDK INSemiconductor::BDK.Libra New Qpen Open With	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Se 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK.Libra INSemiconductor::BDK.Libra Ibackc [ONSemiconductor::BDK or.c [ONSemiconductor::BDK or.c [ONSemiconductor::BDK c [ONSemiconductor::BDK.Libra Newigpen	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the I 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK. INSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK cr.c [ONSemiconductor::BDK cr.c [ONSemiconductor::BDK c [ONSemiconductor::BDK C [ONSemiconductor::BDK INSemiconductor::BDK INSemiconductor::BDK INSemiconductor::BDK New Qpen Open With Show in Local Terminal	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK or.c [ONSemiconductor::BDK c.c [ONSemiconductor::BDK.Li C [ONSemiconductor::BDK.Li RT.c [ONSemiconductor::BDK INSemiconductor::BDK INSemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconductor::BDK Insemiconduc	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentation 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK. INSemiconductor::BDK.Librat Ibackc [ONSemiconductor::BDK ckc [ONSemiconductor::BDK cr. [ONSemiconductor::BDK c [ONSemiconductor::BDK.Librat RT.c [ONSemiconductor::BDK.Librat New Qpen Open With Show in Local Terminal Copy	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the D 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK.Libral Ibackc [ONSemiconductor::BDK.Libral Ibackc [ONSemiconductor::BDK ckc [ONSemiconductor::BDK cr.c [ONSemiconductor::BDK c [ONSemiconductor::BDKLLibral RT.c [ONSemiconductor::BDK.Libral New Open With Show in Local Terminal Copy Paste	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK.Libra Ibackc [ONSemiconductor::BDK.Libra Ibackc [ONSemiconductor::BDK ckc [ONSemiconductor::BDK cr.c [ONSemiconductor::BDK c [ONSemiconductor::BDK.Libra Rc [ONSemiconductor::BDK.Libra New Open Open With Show in Local Terminal Copy Paste Delete	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON See 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the I 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK ckc [ONSemiconductor::BDK cc [ONSemiconductor::BDK.Li c [ONSemiconductor::BDK.Li c [ONSemiconductor::BDK.Libra Nsemiconductor::BDK.Libra Nsemiconductor::BDK.Libra Nsemiconductor::BDK.Libra New Open Open With Show in Local Terminal Copy Paste Delete Remove from Context	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
	kc [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK ckc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK Libra NSemiconductor::BDK.Libra NSemiconductor::BDK.Libra New Qpen Open With Show in Local Terminal Copy Paste Delete Remove from Context Source	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the I 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>
BDK, Pus BDK, Tas BDK, C BHAL, CO BHAL, SPI BHAL, CLC BHAL, CLC <t< td=""><td>kc [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK ckc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK NSemiconductor::BDK.Librar New Open Open With Show in Local Terminal Copy Paste Delete Remove from Context Source Moye Rename</td><td><pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre></td></t<>	kc [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK.Libra Iback.c [ONSemiconductor::BDK ckc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK cc [ONSemiconductor::BDK NSemiconductor::BDK.Librar New Open Open With Show in Local Terminal Copy Paste Delete Remove from Context Source Moye Rename	<pre>5 // limited terms and conditions. The terms and condition 6 // software and/or documentation are available at 7 // http://www.onsemi.com/site/pdf/ONSEMI_T&C.pdf ("ON Set 8 // Terms and Conditions of Sale, Section 8 Software") ar 9 // software license agreement. Do not use this software 10 // unless you have carefully read and you agree to the 1 11 // conditions. By using this software and/or documentat 12 // limited terms and conditions. 13 //</pre>

A brief description on the header files is given below.

RTE_BDK.h

Parameters such as system clock frequency and the board that feature RSL10 (default set to BDK–GEVK), etc. can be set. Descriptions of each of these parameters are also provided.

ption	Valu	Je	
SYSCLK Frequency	8 M	Hz	
APP Task Event Kernel message handler cour		48 MHz	
HAL Pinmap Configuration		24 MHz	
Board selection		16 MHz	
Custom Pinmap	•	8 MHz	
USART0_TX Pin	Z		
USART0_RX Pin	4		
SPI0_MOSI Pin	7		
SPI0_MISO Pin	10		
SPI0_SSEL Pin	5		
SPI0_SCLK Pin	6		
I2C0_SCL Pin	1		
SPI1_SDA Pin	0		
I2C0 DIO Low Pass Filter	ENA	BLED	
I2C0 DIO Drive Strength	6X		
I2C0 DIO Pull Selection	Nop	pull	
LED Pin	14		
Button Pin	15		

RTE_Software_Timer.h

Various timers (4) supported by RSL10 can be configured by invoking the CMSIS configuration wizard on this header file. Timer 1 is used for B–IDK components.

ption	Value
Software Timer Configuration	
Timer resolution [us]	100
Hardware Timer Select	TIMER1
	TIMERO
	TIMER1
	TIMER2
	TIMER3

RTE_PCA9655.h

PCA9655 is the GPIO expander chip assembled on most daughter cards to expand interface functionality. Parameters related to this chip can be set here.

Value
13
0
1234

RTE_x.h

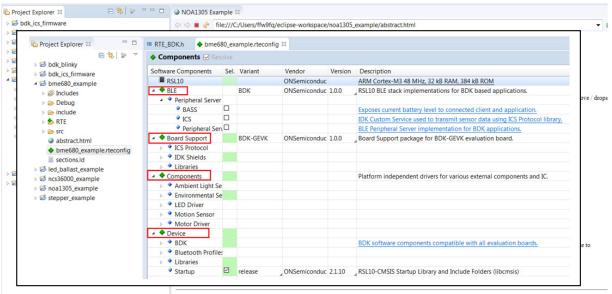
In addition to configuring system settings, all the supported daughter cards' parameters can be configured directly using the configuration wizard, without the need for programming. Once the parameters are changed per the application requirements, saving, rebuilding and flashing the project will let the new parameters take effect. Examples for the stepper and LED ballast daughter cards are shown below. Other daughter cards can be configured in a similar fashion.

-					
Option	Value				
Stepper Shield Left Channel					
Step Mode	1 / 4 Micro - Step				
Coil Peak Current	245 mA				
Direction Of Rotation	CW motion				
NXT Edge Trigger	Rising Edge				
Turn On / Off Slopes of Motor Driver	Very Fast				
Speed Load Angle Transparency Bit	SLA is not transparent				
Speed Load Angle Gain	0.5				
Enables doubling of the PWM frequency					
Enables jittery PWM					
Steps Per Revolution	200				
Stepper Shield Right Channel					
Step Mode	1 / 4 Micro - Step				
Coil Peak Current	1 / 32 Micro - Step				
Direction Of Rotation	1 / 128 Micro - Step				
NXT Edge Trigger	1 / 64 Micro - Step				
Turn On / Off Slopes of Motor Driver	Compensated Full Step, 2 phase on				
Speed Load Angle Transparency Bit	Compensated Full Step, 1 phase on				
Speed Load Angle Gain	1 / 16 Micro - Step				
Enables doubling of the PWM frequency	1 / 8 Micro - Step				
Enables jittery PWM	1 / 4 Micro - Step				
Steps Per Revolution	Compensated Half Step				
•	Uncompensated Half Step				
itep Mode	Uncompensated Full Step				

ption	Value
•	
	Internal
	242 kHz
,	
	10 mV / us
	30 uS
	5.8 V
	-1 V
-	0.4 V
-	115 ns
Booster Minimum On Time	150 ns
Booster Regulation Setpoint Voltage	45.0 V
	100 mV
Activate VBOOST AUX SUPPLY	
Booster Skip Clock Cycles	Disabled
D-LED-B-GEVK Channel 1 Peak current [m	252
D-LED-B-GEVK Channel 1 Average current	140
Enables the offset compensation for buck	
Comparator Threshold Voltage	0
Tunes the Toff x VLED value for channel 1	0
> Overcurrent Settings	
> Enable Buck Regulator Channel 2	
General Settings	
Thermal warning threshold	0
LED sampling duration selection	88
en en en en	• • •

DOCUMENTATION

Detailed documentation of all functions, code, APIs, HALs is part of the CMSIS package. Every use case (for a particular daughter card, service, etc.) copied into the workspace has its own manual with key description in the abstract.html page. URL Information and orderable part numbers are also provided as shown below.



Copyright (c) 2018 Semiconductor Components Industries, LLC (d/b/a ON Semiconductor).

*.rteconfig

The *.rteconfig file lists the software components within the CMSIS pack as described in the B_IDK CMSIS Software Organization section. To access the components, double click *.rteconfig file. Extensive help is provided under the description tab.

Software Components	Sel.	Variant	Vendor	Version	Description	
RSL10			ONSemiconduc		ARM Cortex-M3 48 MHz, 32 kB RAM, 384 kB ROM	
🔺 💠 BLE		BDK	ONSemiconduc	1.0.0	RSL10 BLE stack implementations for BDK based applications.	
Peripheral Server						
BASS					Exposes current battery level to connected client and application.	
ICS					IDK Custom Service used to transmit sensor data using ICS Protocol library.	
Peripheral Sen C					BLE Peripheral Server implementation for BDK applications.	
A Soard Support		BDK-GEVK	ONSemiconduc	1.0.0	Board Support package for BDK-GEVK evaluation board.	
ICS Protocol						
IDK Shields						
AMIS30543_ST		D-STPR-GEVK			Control two stepper motors connected to D-STPR-GEVK using AMIS-30543 motor driver.	
BME680_ENV MULTI-SENSE-C				Measure temperature, humidity and atmospheric pressure using BME680 environmental ssensor.		
BNO055_NDO		MULTI-SENSE-C			Determine absolute orientation of the board in space using BNO055 sensor	
NCS36000_PIR		PIR-GEVB			PIR motion detection using NCS36000 sensor	
NCV78763_LEE		D-LED-B-GEVK			Control two power LEDs connected to D-LED-B-GEVK using NCV78763 LED driver.	
NOA1305_ALS		ALS-GEVB			Measure ambient light level using NOA1305 ambient light sensor	
Libraries						
Button					On-board Push Button support	
🖉 LED 🛛 🖸					On-board LED support	
PCA9655E					16-bit I2C IO Expander library	
A					Platform independent drivers for various external components and IC.	
👂 🕈 Ambient Light Se						
Environmental Se						
LED Driver						
Motion Sensor						
Motor Driver						

ON Sewiconductor ⁴ OI BLUE Development (If: for RSL10
BN0055 Absolute Orientation Sensor
Absolute orientation ennorr library (acceleronneter, gyroscope, magnetometer). More
Data Structures
Btvct BNOSS_NDOF_Calibatus
BVODSS satisfation status at socials. More etat. BNODS NOT Resources
Marros
maclubs setime BN0555,100F_0EVP_ADDRESS (0x45 + 1)
12C address of IO expander on Multisensor shield.
Refere NotoSS_UCOF_UCCEP_OTT (1) O equative Constanting SUCOST estude signal.
Redne BNOSS, NOOF JOER, RT PH (P) 10 opprehe minute for BNOSS service Service Service
Refere BN0055 NDOF_ICERP_RAT_PIN_MA3K (1~< BN0055_NDOF_ICERP_RAT_PIN)
Fastne BNOSS, JOOF, JOEP, JIT, FM (1) 10 oppunde minuter for BNOSS Interpt signal.
Retine BN0055 ND0F_IOEKP_INT_FNI_MASK (1 << BN0055_ND0F_IOEKP_INT_FNI)
Enumerations
erum BNOSS, NDOF, Powerlloos (BNOSS, NDOF, POWER, MODE, NORMAL = 0, BNOSS, NDOF, POWER, MODE, LOW POWER = 1, BNOSS, NDOF, POWER, MODE, INSPEND = 2) Available power modes of BNOSSS, More
Functions
nt21_1 (RU055_NDD7_Initiatizer (xod) Initiatize the XNOS5 and east in the Name Degrees of Presiden (NOS7) generation mode. None
Int2 1 BNORS NDOF Welf-Wentlook (num BNORS NDOF Powerload mode) Alows to scholps power mode to instale carried or sampling or disable series. Nore
In32_1 BNORF_GelCalibration Status (struct BNORS5_NDOF_Calibratus *status) Reads calibration status of BNOR55 emotion. More
Int21 E IND059, INDOF Read LinearAccole (Nucle Moste), Integrating and Linear Accole (Nucle Moste), Integrating and Accole (Nucle Moste), Integrat
In22_1 (INCOSE_INCOSE_INSCIENTING) (INCOMENDATION (INCOMENDATION) (INCOMENDATION (INCOMENDATION) (INCOMENDATIO
Int2_E IRX0555, INCOF_RestAngRedBallon (Retrot DroSS_prop_Datg_1*pt) Read- Utility Traigner futility reviser (Int PS from Service, More,
Int32 1 ENC055 NICOF_Readabordrentation (struct bnc055; euler_losa (1 par) Reads latest absolute orientation vector in degrees from device. More
Run Time Environment Configuration
These parameters are part of the RTE_BNOSS_NOOP.h RTE configuration file and can be used to adjust library behavior. This file is copied into the Exisps project when the BNOSS_NOOP component is seeded and can be edited by using the CARS Configuration Witard editor.
Redne RTE_BNO055_NDOF_EXT_CLK_SRC 1
Detailed Description
Absolute orientation sensor library (acotestrameter).
The BNOD55 is a System in Package Integrating a transit accelerometer, a transit accelerometer, a transit gyrotecope, a

Main Help Page

The main help page is accessible via Device/BDK, visible for all use cases in *.rteconfig file. It's further divided into various modules as shown below.

Software Components	Sel.	Variant	Vendor	Version	Description
RSL10			ONSemiconduc		ARM Cortex-M3 48 MHz, 32 kB RAM, 384 kB ROM
🖻 🚸 BLE		BDK	ONSemiconduc	1.0.0	RSL10 BLE stack implementations for BDK based applications.
Board Support		BDK-GEVK	ONSemiconduc	1.0.0	Board Support package for BDK-GEVK evaluation board.
Omponents					Platform independent drivers for various external components and IC.
🔺 💠 Device					
🔺 🕈 BDK					BDK software components compatible with all evaluation boards.
AES			ONSemiconduc	1.0.0	AES module from mbedTLS
Event Callback			ONSemiconduc	1.0.0	Library for assigning of multiple callbacks to events.
🕈 HAL			ONSemiconduc	1.0.0	Peripheral HAL drivers and RSL10 configuration
Output Redirection		SEGGER RTT	ONSemiconduc	1.0.0	Redirects standart output calls using SEGGER RTT
Scheduling			ONSemiconduc	1.0.0	Management layer for Event Kernel Application Task
Software Timer			ONSemiconduc	1.0.0	Allows to create multiple timer events while using only single hardware timer
Bluetooth Profiles					
Libraries					
Startup		release	ONSemiconduc	2.1.10	RSL10-CMSIS Startup Library and Include Folders (libcmsis)

ON Semiconductor [®] BDK v1.0.0 Bluetooth LE Development Kit for RSL10
BDK
Abstraction layers for RSL10 Bluetooth Development Kit based applications. More
Modules
COMPONENTS
TASK_APP Management Application Task management & custom event scheduling.
Event Callback Library for attaching multiple callback functions (listeners) to single event source.
HAL Peripheral Hardware Abstraction Layer for RSL10.
Software Timer Allows creation of unlimited number of software timers with Ticker, Timeout and Timer functionality.
ANSI Terminal Color support Bring color to your terminal screen.
Target Evaluation board specific definitions.
API
Bluetooth Low Energy Library for handling of BLE functionality and libraries of supported BLE profiles.

Sub-sections may be expanded for further information (Ex: HAL interfaces shown below)

Periphera	al Hardware Abstraction Layer for RSL10. More
Vodul	es
	k Configurations tes possible clock configurations for proper operation of BDK.
12C 12C i	nterface for communication with connected shields.
SPI i	nterface for communication with connected shields.
UAR	T T interface for communication with connected shields.
Macro	s

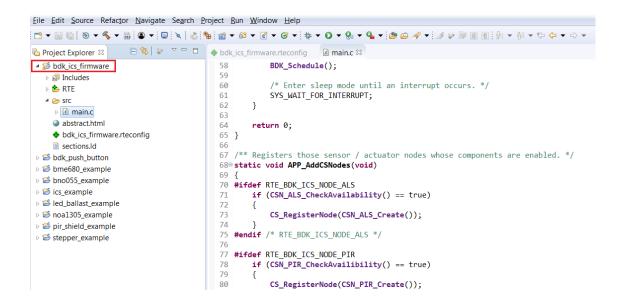
B-IDK also provides software timers and applications task manager abstraction layers to enable management of specific tasks and timing within the event kernel.

ON Semiconductor* ON BDK v1.0.0 Bluetooth LE Development Kit for RSL10
BDK
Abstraction layers for RSL10 Bluetooth Development Kit based applications. More
Modules
COMPONENTS
TASK_APP Management
Application Task management & custom event scheduling.
Event Callback Library for attaching multiple callback functions (listeners) to single event source.
HAL Peripheral Hardware Abstraction Layer for RSL10.
Software Timer Allows creation of unlimited number of software timers with Ticker, Timeout and Timer functionality.
ANSI Terminal Color support Bring color to your terminal screen.
Target Evaluation board specific definitions.
API
Bluetooth Low Energy Library for handling of BLE functionality and libraries of supported BLE profiles.

Custom Service Firmware

In order to read sensor data and control actuators connected to the BDK–GEVK from the RSL10 Sense and Control mobile app, the Custom Service Firmware must be downloaded onto the BDK–GEVK. This firmware can be found as Custom Service Firmware under examples in the CMSIS pack.

eclipse-workspace - ics_	example/src/main.c - Eclipse				- 0
Eile Edit Source Refacto	or <u>N</u> avigate Se <u>a</u> rch <u>P</u> roject <u>R</u> un <u>W</u>	indow Help			
📑 🕶 🔛 🚳 🐘 i 👁 🕶 i 🖻	0 8 🐐 8 8 8 🖬 9 🗸 🖉 🙆	≪▼[]J⊅[]] + 8] + 10 Φ + 0 +			Quick Access 🕴 😰 🗮 💽
Devices 🖪 Boards 😫	BE 🛛 🙀 🐃 🗆	📾 Packs 📑 Examples 😫		🗆 Only show examples from installed packs 🛛 🔊 🍣 😂 🧇 🔤 🤝 🖱	🗏 Pack Properties 🛛 🕀 🖂 🐨 😁
type filter text		Search Example			type filter text
Board	Summary	Example	Action	Description	A B ONSemiconductor.BDK.0.4.1
🔺 🍄 All Boards	2 Boards	Battery Service Example (Bluetooth Deve	Copy	Example usage of BDK Battery Service Profile	Boards
Bluetooth Develop	pm RSL10	BDK Push Button Example (Bluetooth Dev	Copy	Simple example on how to generate events when on-boa	A Components
RSL10 Evaluation I	Bo RSL10	bdk_blinky (Bluetooth Development Kit)	Copy	Example that blinks the on-board LED	 Examples
		BME680 Sensor Example (Bluetooth Deve	Copy	Uses BME680 located on MULTI-SENSE-GEVB to measure	 Battery Service Example (Bluetooth Development Kit)
		BNO055 Sensor Example (Bluetooth Deve	Copy	Uses BNO055 located on MULTI-SENSE-GEVB to determin	 BDK Push Button Example (Bluetooth Development Kit)
		Custom Service Firmware (Bluetooth Dev	Copy	Exposes sensor data over Cusrom Service BLE Profile.	 Bdk_blinky (Bluetooth Development Kit)
		IDK Custom Service Example (Bluetooth I	🕈 Сору	Example usage of IDK Custom Service Profile	 BME680 Sensor Example (Bluetooth Development Kit)
		LED Ballast Shield Example (Bluetooth De	Copy	Control two LEDs connected to D-LED-B-GEVK	 BNO055 Sensor Example (Bluetooth Development Kit)
		NOA1305 Sensor Example (Bluetooth De	Copy	Measure Ambient Light levels by using NOA1305 sensor o	 Custom Service Firmware (Bluetooth Development Kit)
		PIR Shield Example (Bluetooth Developm	Copy	Example that blinks on-board LED when motion is detect	IDK Custom Service Exampl Expand Selected
		Software Timer Example (Bluetooth Deve	🕈 Сору	Example showing Ticker, Timeout and Timer use cases of	LED Ballast Shield Example Copy
		Stepper Shield Example (Bluetooth Devel	🕈 Сору	Control two stepper motors via D-STPR-GEVB	NOA1305 Sensor Example (Build Configurations
					PIR Shield Example (Bluetor
					Software Timer Example (BI
					Stepper Shield Example (Bli Source)



ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

The evaluation board/kit (research and development board/kit) (hereinafter the "board") is not a finished product and is as such not available for sale to consumers. The board is only intended for research, development, demonstration and evaluation purposes and should as such only be used in laboratory/development areas by persons with an engineering/technical training and familiar with the risks associated with handling electrical/mechanical components, systems and subsystems. This person assumes full responsibility/liability for proper and safe handling. Any other use, resale or redistribution for any other purpose is strictly prohibited.

The board is delivered "AS IS" and without warranty of any kind including, but not limited to, that the board is production-worthy, that the functions contained in the board will meet your requirements, or that the operation of the board will be uninterrupted or error free. ON Semiconductor expressly disclaims all warranties, express, implied or otherwise, including without limitation, warranties of fitness for a particular purpose and non-infringement of intellectual property rights.

ON Semiconductor reserves the right to make changes without further notice to any board.

You are responsible for determining whether the board will be suitable for your intended use or application or will achieve your intended results. Prior to using or distributing any systems that have been evaluated, designed or tested using the board, you agree to test and validate your design to confirm the functionality for your application. Any technical, applications or design information or advice, quality characterization, reliability data or other services provided by ON Semiconductor shall not constitute any representation or warranty by ON Semiconductor, and no additional obligations or liabilities shall arise from ON Semiconductor having provided such information or services.

The boards are not designed, intended, or authorized for use in life support systems, or any FDA Class 3 medical devices or medical devices with a similar or equivalent classification in a foreign jurisdiction, or any devices intended for implantation in the human body. Should you purchase or use the board for any such unintended or unauthorized application, you shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the board.

This evaluation board/kit does not fall within the scope of the European Union directives regarding electromagnetic compatibility, restricted substances (RoHS), recycling (WEEE), FCC, CE or UL, and may not meet the technical requirements of these or other related directives.

FCC WARNING - This evaluation board/kit is intended for use for engineering development, demonstration, or evaluation purposes only and is not considered by ON Semiconductor to be a finished end product fit for general consumer use. It may generate, use, or radiate radio frequency energy and has not been tested for compliance with the limits of computing devices pursuant to part 15 of FCC rules, which are designed to provide reasonable protection against radio frequency interference. Operation of this equipment may cause interference with radio communications, in which case the user shall be responsible, at its expense, to take whatever measures may be required to correct this interference.

ON Semiconductor does not convey any license under its patent rights nor the rights of others.

LIMITATIONS OF LIABILITY: ON Semiconductor shall not be liable for any special, consequential, incidental, indirect or punitive damages, including, but not limited to the costs of requalification, delay, loss of profits or goodwill, arising out of or in connection with the board, even if ON Semiconductor is advised of the possibility of such damages. In no event shall ON Semiconductor's aggregate liability from any obligation arising out of or in connection with the board, under any theory of liability, exceed the purchase price paid for the board, if any For more information and documentation, please visit www.onsemi.com

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT: Email Requests to: orderlit@onsemi.com

TECHNICAL SUPPORT North American Technical Support:

ON Semiconductor Website: www.onsemi.com

Voice Mail: 1 800-282-9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support: Phone: 00421 33 790 2910 For additional information, please contact your local Sales Representative



Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

Благодаря сотрудничеству с мировыми поставщиками мы осуществляем комплексные и плановые поставки широчайшего спектра электронных компонентов.

Собственная эффективная логистика и склад в обеспечивает надежную поставку продукции в точно указанные сроки по всей России.

Мы осуществляем техническую поддержку нашим клиентам и предпродажную проверку качества продукции. На все поставляемые продукты мы предоставляем гарантию.

Осуществляем поставки продукции под контролем ВП МО РФ на предприятия военно-промышленного комплекса России, а также работаем в рамках 275 ФЗ с открытием отдельных счетов в уполномоченном банке. Система менеджмента качества компании соответствует требованиям ГОСТ ISO 9001.

Минимальные сроки поставки, гибкие цены, неограниченный ассортимент и индивидуальный подход к клиентам являются основой для выстраивания долгосрочного и эффективного сотрудничества с предприятиями радиоэлектронной промышленности, предприятиями ВПК и научноисследовательскими институтами России.

С нами вы становитесь еще успешнее!

Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург, Промышленная ул, дом № 19, литера Н, помещение 100-Н Офис 331