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### NC7ST08 TinyLogic® HST 2-Input AND Gate

#### **General Description**

The NC7ST08 is a single 2-Input high performance CMOS AND Gate, with TTL-compatible inputs. Advanced Silicon Gate CMOS fabrication assures high speed and low power circuit operation. ESD protection diodes inherently guard both inputs and output with respect to the  $V_{CC}$  and GND rails. High gain circuitry offers high noise immunity and reduced sensitivity to input edge rate. The TTL-compatible inputs facilitate TTL to NM OS/CMOS interfacing. Device performance is similar to MM74HCT but with 1/2 the output current drive of HC/HCT.

#### **Features**

■ Space saving SOT23 or SC70 5-lead package

January 1997

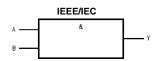
Revised August 2004

- Ultra small MicroPak<sup>™</sup> leadless package
- High Speed:
- $t_{PD}$  6 ns (typ),  $V_{CC} = 5V$ ,  $C_L = 15$  pF,  $T_A = 25^{\circ}C$
- Low Quiescent Power, I<sub>CC</sub> < 1 µA, V<sub>CC</sub> = 5.5V
- Balanced Output Drive; 2 mA I<sub>OL</sub>, -2 mA I<sub>OH</sub>
- TTL-compatible inputs

### **Ordering Code:**

Order Number	Package Number	Product Code Top Mark	Package Description	Supplied As
NC7ST08M5X	MA05B	8S08	5-Lead SOT23, JEDEC MO-178, 1.6mm	3k Units on Tape and Reel
NC7ST08P5X	MAA05A	T08	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3k Units on Tape and Reel
NC7ST08L6X	MAC06A	NN	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel

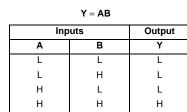
#### Logic Symbol



#### **Pin Descriptions**

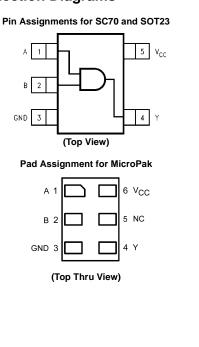
Pin Names	Description
A, B	Inputs
Y	Output
NC	No Connect

#### **Function Table**





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### Absolute Maximum Ratings(Note 1)

Supply Voltage (V <sub>CC</sub> )	-0.5V to +7.0V
DC Input Diode Current (IIK)	
@V <sub>IN</sub> < -0.5V	–20 mA
$@V_{IN} \ge V_{CC} + 0.5V$	+20 mA
DC Input Voltage (VIN)	-0.5V to V <sub>CC</sub> + 0.5V
DC Output Diode Current (I <sub>OK</sub> )	
V <sub>OUT</sub> < -0.5V	–20 mA
$V_{OUT} > V_{CC} + 0.5V$	+20 mA
Output Voltage (V <sub>OUT</sub> )	–0.5V to $V_{CC}$ + 0.5V
DC Output Source or Sink Current	
(I <sub>OUT</sub> )	±12.5 mA
DC V <sub>CC</sub> or Ground Current per	
Supply Pin (I <sub>CC</sub> or I <sub>GND</sub> )	±25 mA
Storage Temperature (T <sub>STG</sub> )	-65°C to +150°C
Junction Temperature (T <sub>J</sub> )	150°C
Lead Temperature (T <sub>L</sub> );	
(Soldering, 10 seconds)	260°C
Power Dissipation (P <sub>D</sub> ) @+85°C	
SOT23-5	200 mW
SC70-5	150 mW

### Recommended Operating

Conditions (Note 2) Supply Voltage 4.5V to 5.5V Input Voltage (VIN) 0.0V to  $V_{\mbox{\scriptsize CC}}$ 0V to  $\rm V_{\rm CC}$ Output Voltage (V<sub>OUT</sub>) -40°C to +85°C Operating Temperature (T<sub>A</sub>) Input Rise and Fall Time  $(t_r, t_f)$  $V_{CC} = 5.0V$ 0 ns to 500 ns Thermal Resistance  $(\theta_{JA})$ 300°C/W SOT23-5 SC70-5 425°C/W

Note 1: Absolute Maximum Ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation of circuits outside the databook specifications.

Note 2: Unused inputs must be held HIGH or LOW. They may not float.

### **DC Electrical Characteristics**

Symbol	mbol Parameter			$T_A = +25^{\circ}C$		$T_A = 40^{\circ}C$ to $+85^{\circ}C$		Units	Conditions
Symbol	Falameter	(V)	Min	Тур	Max	Min	Max	Units	conditions
V <sub>IH</sub>	HIGH Level Input Voltage	4.5-5.5	2.0			2.0		V	
VIL	LOW Level Input Voltage	4.5-5.5			0.8		0.8	V	
V <sub>OH</sub>	HIGH Level Output Voltage	4.5	4.4	4.5		4.4			I <sub>OH</sub> = -20 μA
		4.5	4.18	4.35		4.13		V	$I_{OH} = -2 \text{ mA}$
									$V_{IN} = V_{IH}$
V <sub>OL</sub>	LOW Level Output Voltage	4.5		0	0.1		0.1		I <sub>OL</sub> = 20 μA
		4.5		0.10	0.26		0.33	V	$I_{OL} = 2 \text{ mA}$
									$V_{IN} = V_{IL}$
I <sub>IN</sub>	Input Leakage Current	5.5			±0.1		±1.0	μA	$0 \le V_{IN} \le 5.5V$
I <sub>CC</sub>	Quiescent Supply Current	5.5			1.0		10.0	μA	$V_{IN} = V_{CC}$ or GND
I <sub>CCT</sub>	I <sub>CC</sub> per Input	5.5			2.0		2.9	mA	One Input $V_{IN} = 0.5V$ or 2.4V,
									Other Input V <sub>CC</sub> or GND

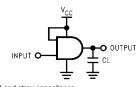
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Symbol	Parameter	v <sub>cc</sub>	T <sub>A</sub> = +25°C		;	$T_A = 40^{\circ}C \text{ to } +85^{\circ}C$		11-14-1	Conditions	Figure
Symbol	Farameter	(V)	Min	Тур	Max	Min	Max	Units	Conditions	Number
t <sub>PLH</sub> ,	Propagation Delay	5.0		4	12				0 1	
t <sub>PHL</sub>				6	17			ns	C <sub>L</sub> = 15 pF	
		4.5		6	16		20			Figures
				12	27		31	ns	C 50 pF	1, 3
		5.5		5	14		18	ns	C <sub>L</sub> = 50 pF	
				11	26		30			
t <sub>TLH</sub> ,	Output Transition Time	5.0		4	10			ns	$C_L = 15 \text{ pF}$	
t <sub>THL</sub>		4.5		11	25		31		0 50 - 5	Figures 1, 3
		5.5		10	21		26	ns	C <sub>L</sub> = 50 pF	1,0
CIN	Input Capacitance	Open			10			pF		
CPD	Power Dissipation Capacitance	5.0		6				pF	(Note 3)	Figure 2

Note 3: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I<sub>CCD</sub>) at no output loading and operating at 50% duty cycle. (See Figure 2.) C<sub>PD</sub> is related to I<sub>CCD</sub> dynamic operating current by the expression:

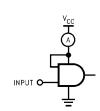
 $I_{CCD} = (C_{PD})(V_{CC})(f_{IN}) + (I_{CC}static).$ 

### AC Loading and Waveforms



 $C_L$  includes load and stray capacitance Input PRR = 1.0 MHz;  $t_w$  = 500 ns

#### FIGURE 1. AC Test Circuit



Input = AC Waveform; PRR = variable; Duty Cycle = 50% FIGURE 2. I<sub>CCD</sub> Test Circuit

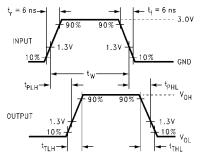


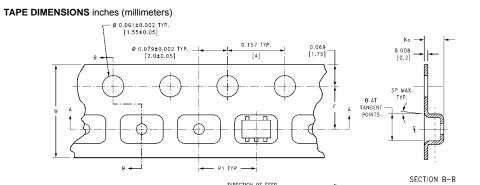
FIGURE 3. AC Waveforms

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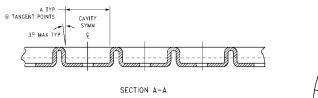


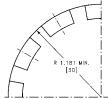
# Tape and Reel Specification TAPE FORMAT for SC70 and SOT23

Package	Таре	Number	Cavity	Cover Tape
Designator	Section	Cavities	Status	Status
	Leader (Start End)	125 (typ)	Empty	Sealed
M5X, P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

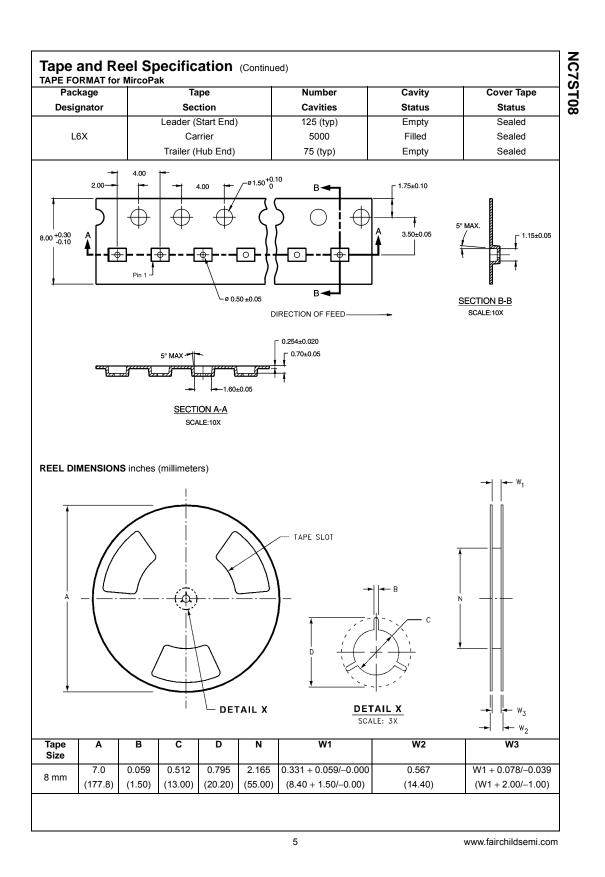


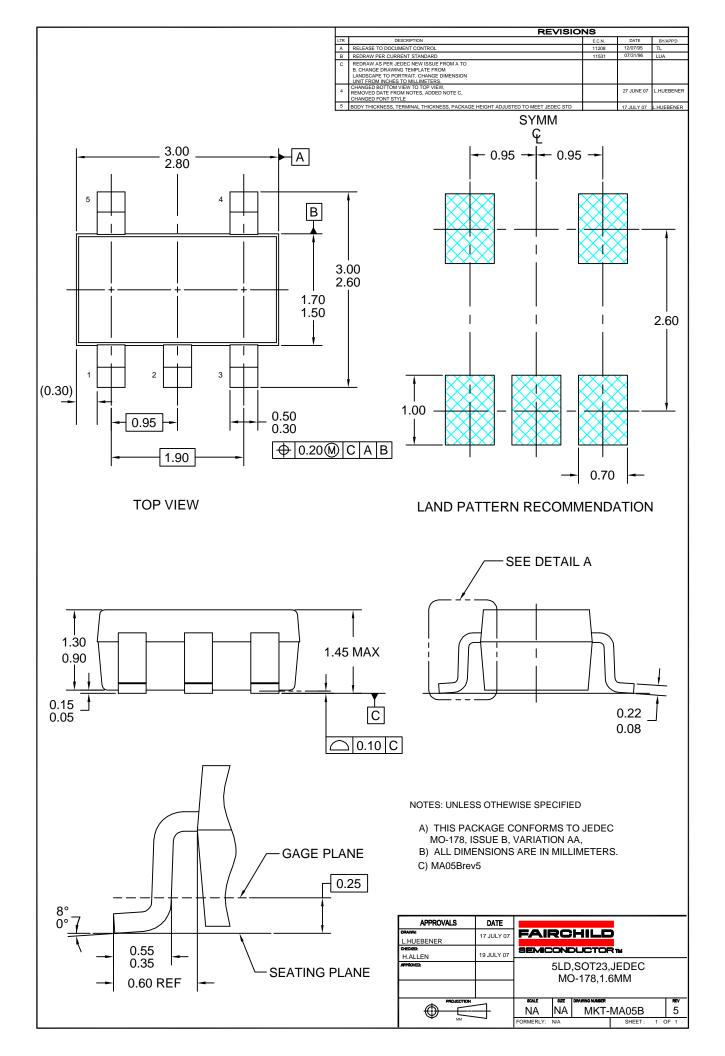
DIRECTION OF FEED -

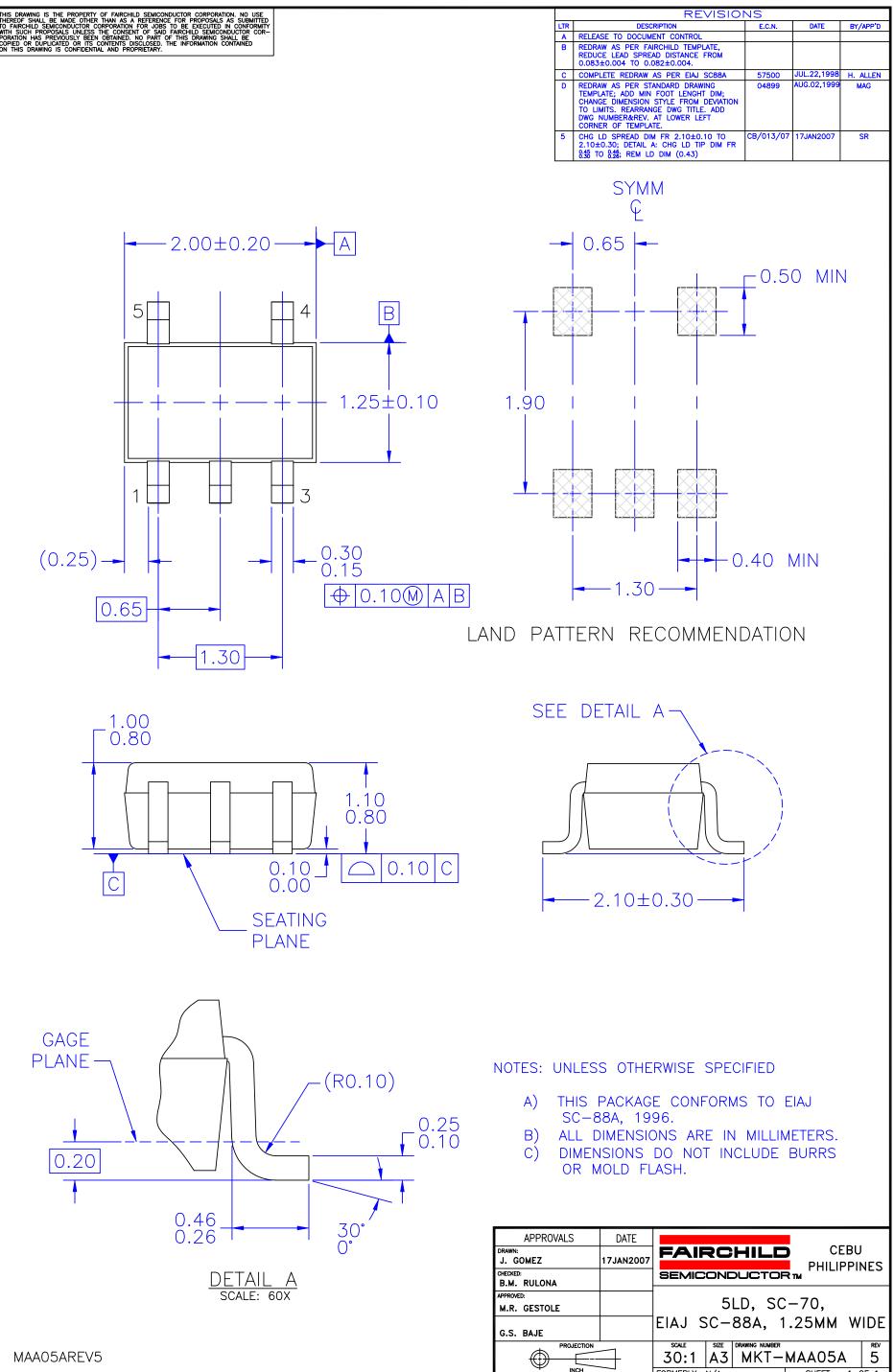




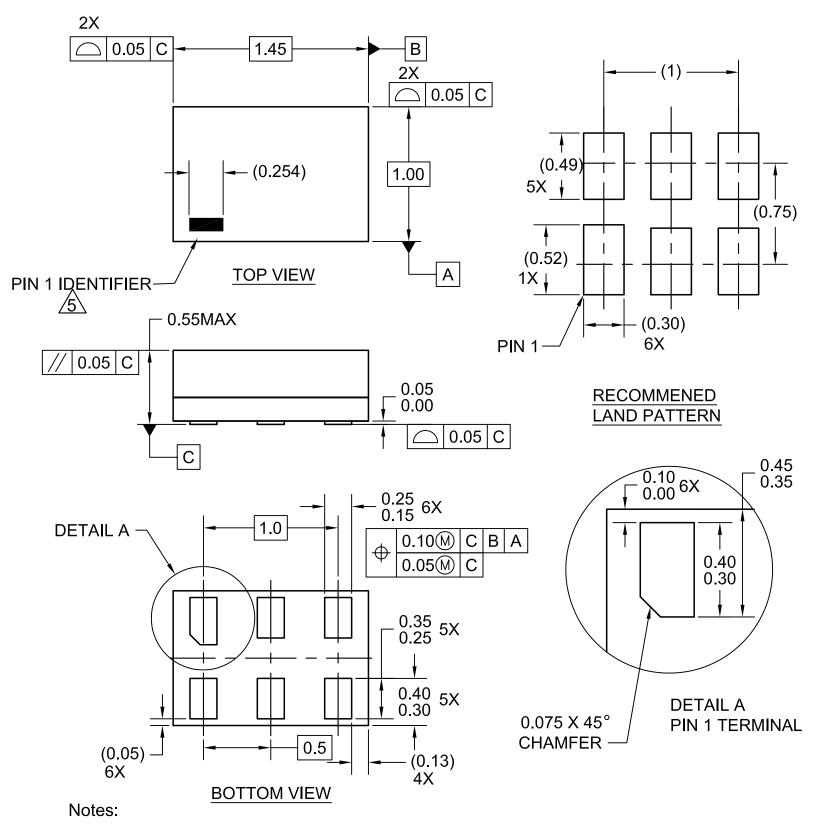
					BEND R	ADIUS NOT TO SC	ALE
Package	Tape Size	DIM A	DIM B	DIM F	DIM K <sub>o</sub>	DIM P1	DIM W
SC70-5	8 mm	0.093	0.096	$0.138\pm0.004$	$0.053\pm0.004$	0.157	$0.315\pm0.004$
3070-3	0 11111	(2.35)	(2.45)	$(3.5\pm0.10)$	$(1.35\pm0.10)$	(4)	$(8\pm0.1)$
SOT23-5	9 mm	0.130	0.130	$0.138\pm0.002$	$0.055\pm0.004$	0.157	$0.315\pm0.012$
30123-5	DT23-5 8 mm	(3.3)	(3.3)	$(3.5\pm0.05)$	$(1.4 \pm 0.11)$	(4)	$(8\pm0.3)$







APPROVALS	DATE					
DRAWN: J. GOMEZ	17JAN2007	FAI	RC		CEBU	
CHECKED: B.M. RULONA		SEMIC		DUCTOR		
APPROVED: M.R. GESTOLE				LD, SC	•	
G.S. BAJE		EIAJ S	SC-	88A, 1	.25MM W	IDE
	7	scale 30:1	size A3	MKT-	MAA05A	5
		FORMERLY:	N/A		SHEET : 1 (	DF 1



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994
- 4. FILENAME AND REVISION: MAC06AREV4
- <u>/5.</u> PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

OTHER LINE IN THE MARK CODE LAYOUT.

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