**AEC-Q101 Qualified** 

# 4V Drive Nch+Nch MOSFET SP8K33FRA

#### Structure

Silicon N-channel MOSFET

#### Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small surface Mount Package (SOP8).

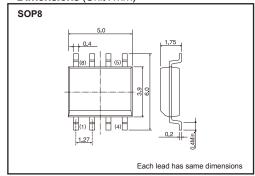
# Application

Switching

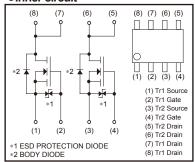
# Packaging specifications

	Package	Taping
Туре	Code	TB
	Basic ordering unit (pieces)	2500
SP8K33FRA	0	

#### ●Dimensions (Unit: mm)



# ●Inner circuit



A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

# ● Absolute maximum ratings (Ta=25°C)

<It is the same ratings for the Tr1 and Tr2.>

Parameter		Symbol	Limits	Unit	
Drain-source voltage		VDSS	60	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain aurrent	Continuous	I <sub>D</sub>	±5.0	Α	
Drain current	Pulsed	I <sub>DP</sub> *1	±20	Α	
Source current	Continuous	Is	1.0	Α	
(Body diode)	Pulsed	I <sub>SP</sub> *1	20	Α	
Total power dissipation		P <sub>D</sub> *2	2.0	W/TOTAL	
Channel temperature		Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C	

<sup>\*1</sup> Pw≤10μs, Duty cycle≤1% \*2 Mounted on a ceramic board.

# ●Electrical characteristics (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	±10	μА	V <sub>G</sub> s=±20V, V <sub>D</sub> s=0V
Drain-source breakdown voltage	V <sub>(BR) DSS</sub>	60	-	_	V	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	1.0	_	2.5	V	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA
0.00		_	34	48		ID=5.0A, VGS=10V
Static drain-source on-state resistance	R <sub>DS (on)</sub> *	_	38	54	mΩ	I <sub>D</sub> =5.0A, V <sub>GS</sub> =4.5V
resistance		_	40	56		I <sub>D</sub> =5.0A, V <sub>GS</sub> =4.0V
Forward transfer admittance	Y <sub>fs</sub>  *	5.0	_	_	S	I <sub>D</sub> =5.0A, V <sub>DS</sub> =10V
Input capacitance	Ciss	_	620	_	pF	V <sub>DS</sub> =10V
Output capacitance	Coss	_	145	_	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	Crss	-	70	_	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	_	12	_	ns	I <sub>D</sub> =2.5A, V <sub>DD</sub> ≒30V
Rise time	tr *	_	20	_	ns	V <sub>G</sub> s=10V
Turn-off delay time	t <sub>d (off)</sub> *	_	40	_	ns	R <sub>L</sub> =12Ω
Fall time	t <sub>f</sub> *	_	20	_	ns	$R_G = 10\Omega$
Total gate charge	Qg *	-	8.0	12	nC	ID=5.0A, VDD≒30V
Gate-source charge	Qgs *	_	2.0	_	nC	Vgs=5V
Gate-drain charge	Q <sub>gd</sub> *	_	2.6	_	nC	$R_L=6\Omega$ , $R_G=10\Omega$

<sup>\*</sup>Pulsed

# ●Body diode characteristics (Source-drain) (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	V <sub>SD</sub> *	_	_	1.2	V	I <sub>S</sub> =5.0A, V <sub>GS</sub> =0V

<sup>\*</sup>Pulsed

#### Electrical characteristic curves

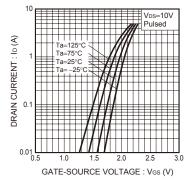


Fig.1 Typical Transfer Characteristics

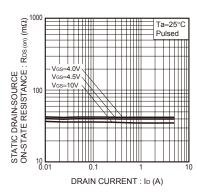


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current( I )

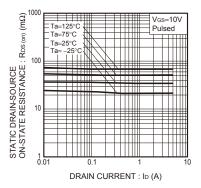


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current(II)

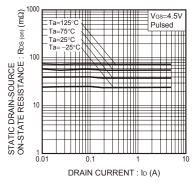


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(III)

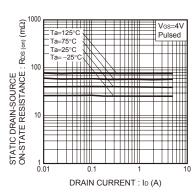


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(IV)

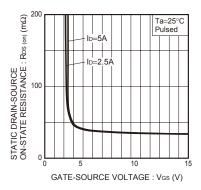


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

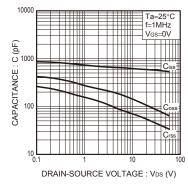


Fig.7 Typical Capacitance vs. Drain-Source Voltage

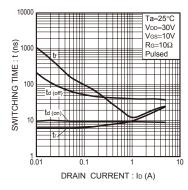


Fig.8 Switching Characteristics

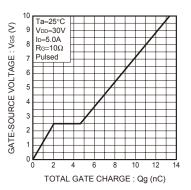


Fig.9 Dynamic Input Characteristics

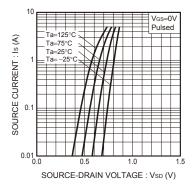


Fig.10 Source Current vs. Source-Drain Voltage

# Measurement circuit

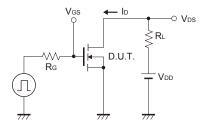


Fig.11 Switching Time Test Circuit

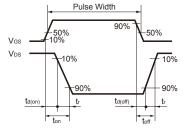


Fig.12 Switching Time Waveforms

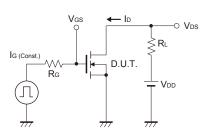


Fig.13 Gate Charge Test Circuit

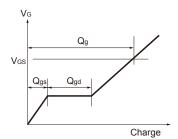


Fig.14 Gate Charge Waveform

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Γ	CLASSⅢ	CLACCIII	CLASS II b	СГУССШ	
Γ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ	

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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