

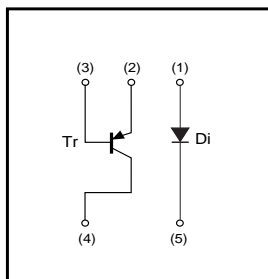
Low-frequency transistor

UML1N

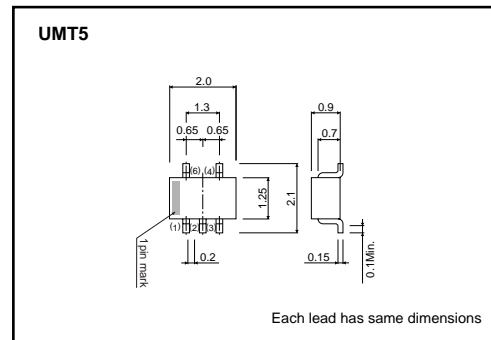
●Features

- 1) The 2SA1037AK and a diode are housed independently in a UMT package.

●Equivalent circuit



●External dimensions (Unit : mm)



●Packaging specifications

Type	FML10
Package	SMT5
Marking	L10
Code	TR
Basic ordering unit(pieces)	3000

●Absolute maximum ratings (Ta=25°C)

Tr

Parameter	Symbol	Limits	Unit
Collector-base voltage	V _{CB0}	-60	V
Collector-emitter voltage	V _{CE0}	-50	V
Emitter-base voltage	V _{EB0}	-6	V
Collector current	I _C	-0.15	A
Collector power dissipatio	P _C	0.15	W
Junction temperature	T _J	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Di

Parameter	Symbol	Limits	Unit
DC reverse voltage	V _R	80	V
Peak reverse voltage	V _{RM}	80	V
Mean rectifying current	I _O	0.1	A
Peak forward voltage	I _{FM}	0.3	A
Surge current	I _{surge}	4	A
Junction temperature	T _J	150	°C
Storage temperature	T _{stg}	-55 to +150	°C
Specified I/O frequencies	f	100	MHz

Transistors

●Electrical characteristics (Ta=25°C)

Tr

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BV _{CEO}	-50	-	-	V	I _c = -1mA
Collector-base breakdown voltage	BV _{CBO}	-60	-	-	V	I _c = -50μA
Emitter-base breakdown voltage	BV _{EB0}	-6	-	-	V	I _E = -50μA
Collector cutoff current	I _{CB0}	-	-	-0.1	μA	V _{CB} = -60V
Emitter cutoff current	I _{EB0}	-	-	-0.1	μA	V _{EB} = -5V
Collector-emitter saturation voltage	V _{CE(sat)}	-	-	-0.5	V	I _c /I _B = -50mA/-5mA
DC current transfer ratio	h _{FE}	120	-	560	-	V _{CE} = -6V, I _c = -1mA
Transition frequency	f _r	-	140	-	MHz	V _{CE} = -12V, I _E = 2mA, f= 100MHz
Output capacitance	C _{ob}	-	4	5	pF	V _{CB} = -12V, I _E = 0A, f= 1MHz

Di

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _F	-	-	1.2	V	I _F =100mA
Reverse current	I _R	-	-	0.1	μA	V _R =70V
Capacitance between terminals	C _T	-	-	3.5	pF	V _R =6V, f=1MHz
Reverse recovery time	t _{rr}	-	-	4	ns	V _R =6V, I _F =5mA, R _L =50Ω

●Electrical characteristic curves

Tr

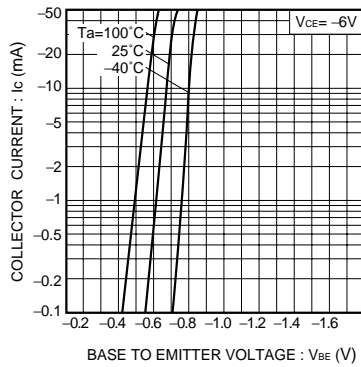


Fig.1 Grounded emitter propagation characteristics

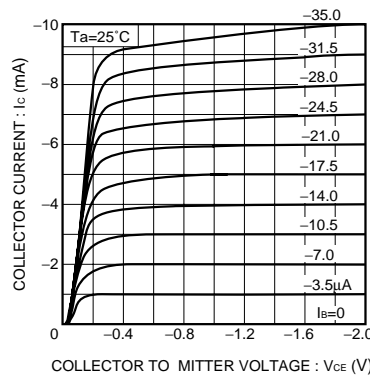


Fig.2 Grounded emitter output characteristics (I)

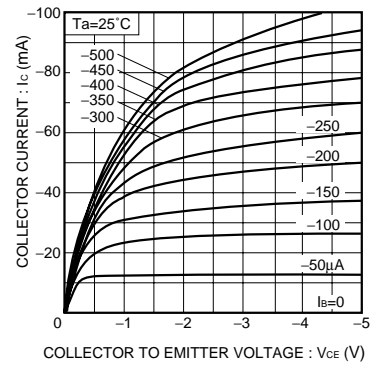


Fig.3 Grounded emitter output characteristics (II)

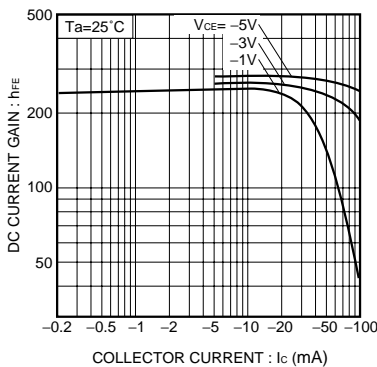


Fig.4 DC current gain vs. collector current (I)

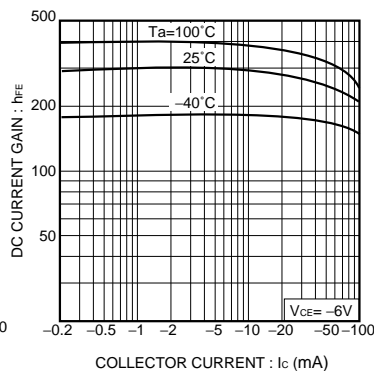


Fig.5 DC current gain vs. collector current (II)

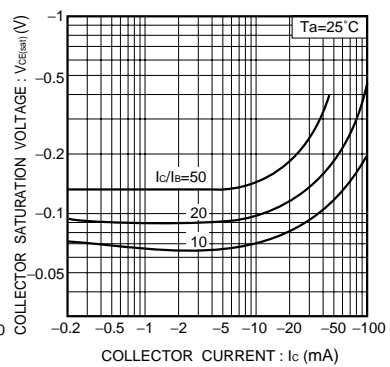


Fig.6 Collector-emitter saturation voltage vs. collector current (I)

Transistors

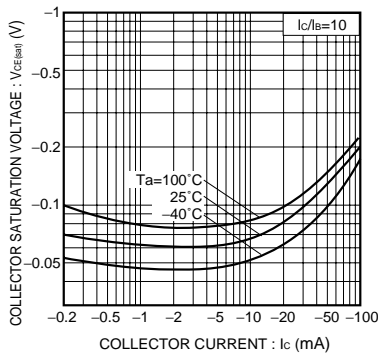


Fig.7 Collector-emitter saturation voltage vs. collector current (II)

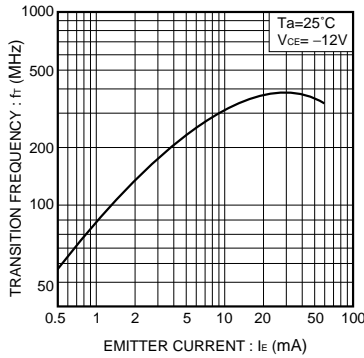


Fig.8 Gain bandwidth product vs. emitter current

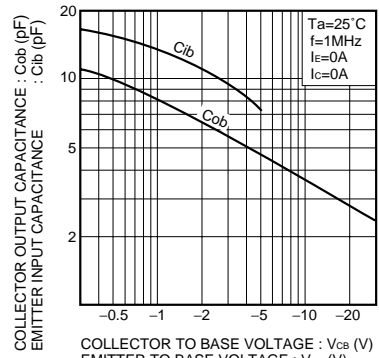


Fig.9 Collector output capacitance vs. collector-base voltage
Emitter input capacitance vs. emitter-base voltage

Di

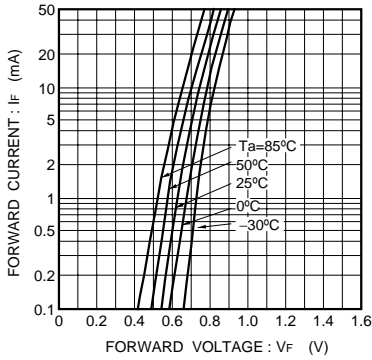


Fig.10 Forward characteristics

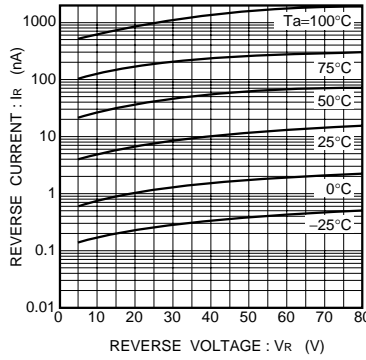


Fig.11 Reverse characteristics

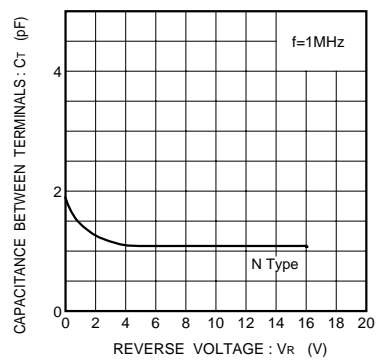


Fig.12 Capacitance between terminals characteristics

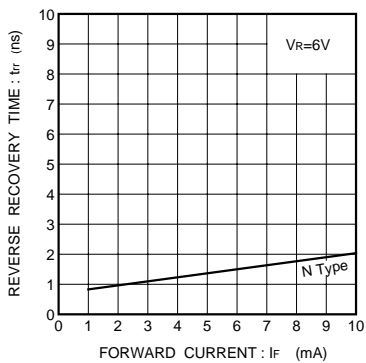


Fig.13 Reverse recovery time

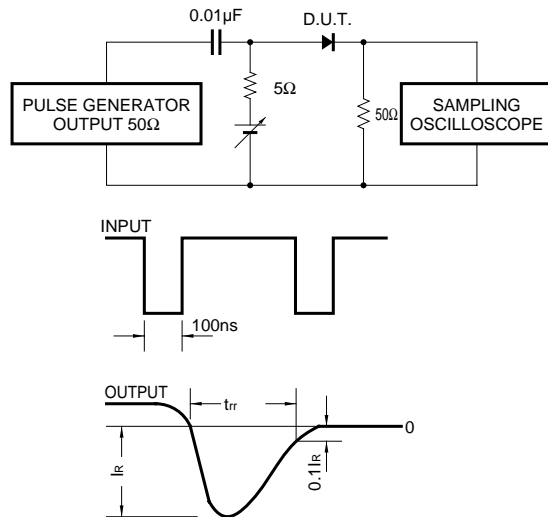


Fig.14 Reverse recovery time (t_{rr}) measurement circuit

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