

Dual N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)} \text{ max}$	$I_D \text{ max}$ $T_A = +25^\circ\text{C}$
20V	11.5m Ω @ $V_{GS} = 4.5\text{V}$	10 A
	14m Ω @ $V_{GS} = 2.5\text{V}$	9 A

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected**
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

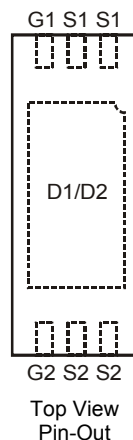
- Case: W-DFN5020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.03 grams (approximate)



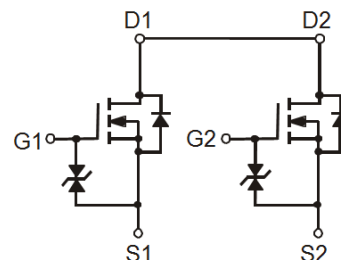
Top View



Bottom View



Top View
Pin-Out



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2013UFX-7	W-DFN5020-6	3000 / Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



FX = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: X = 2010)
M = Month (ex: 9 = September)

Date Code Key

Year	2010	2011	2012	2013	2014	2015	2016
Code	X	Y	Z	A	B	C	D

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	10 8	A
Continuous Drain Current (Note 5) V _{GS} = 2.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	9 7	A
Pulsed Drain Current (Note 7)			I _{DM}	80	A

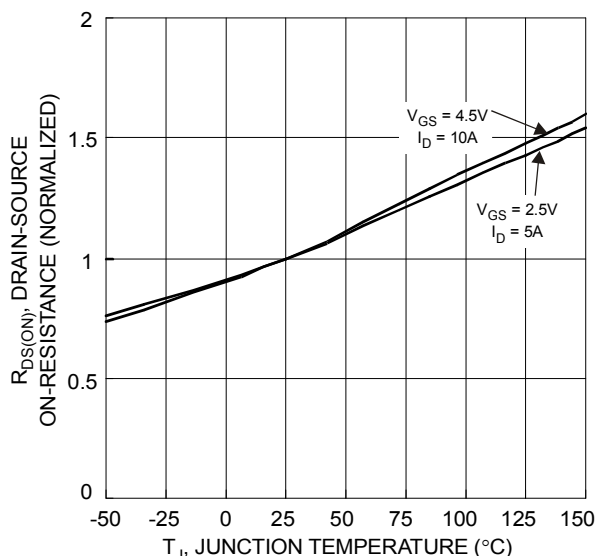
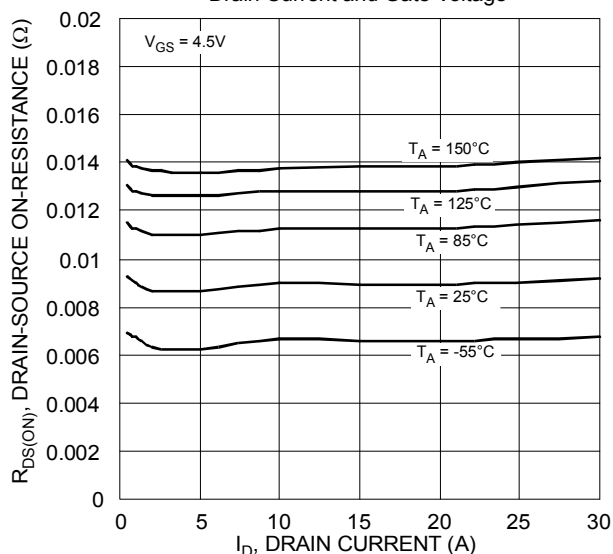
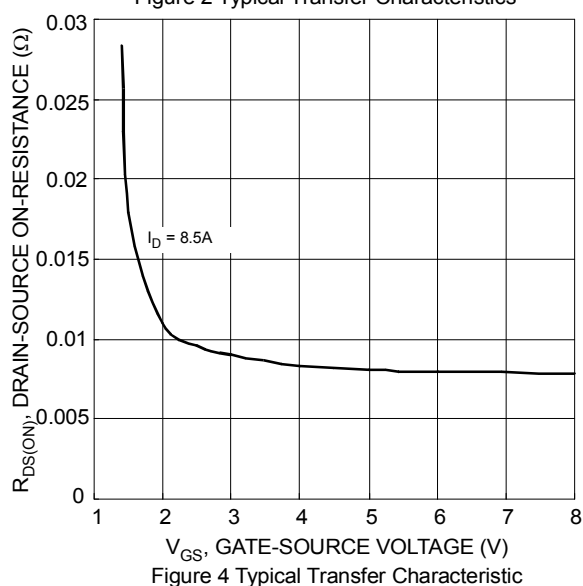
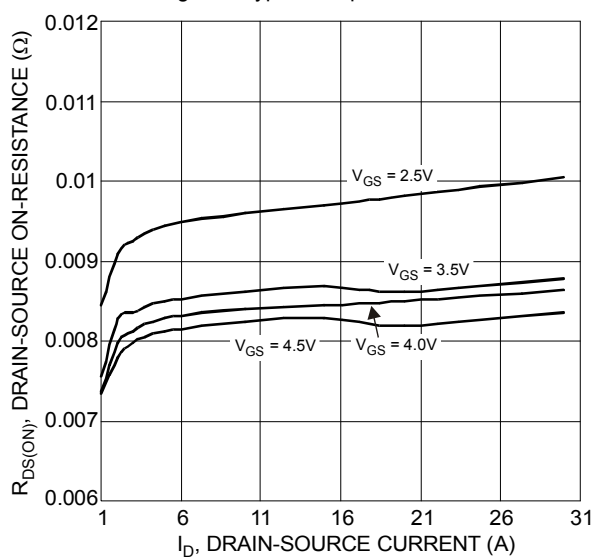
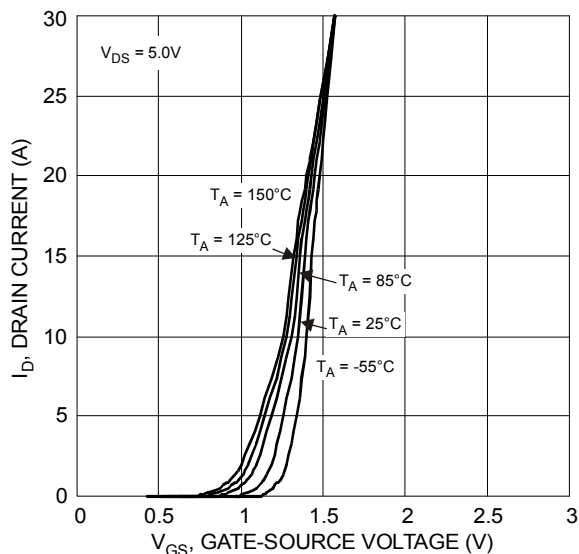
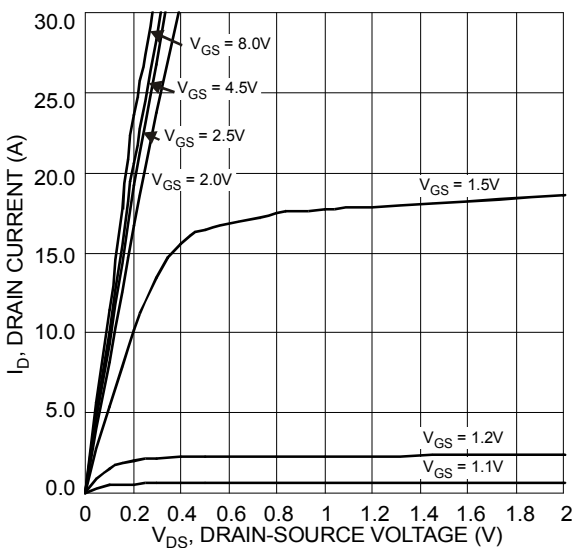
Thermal Characteristics

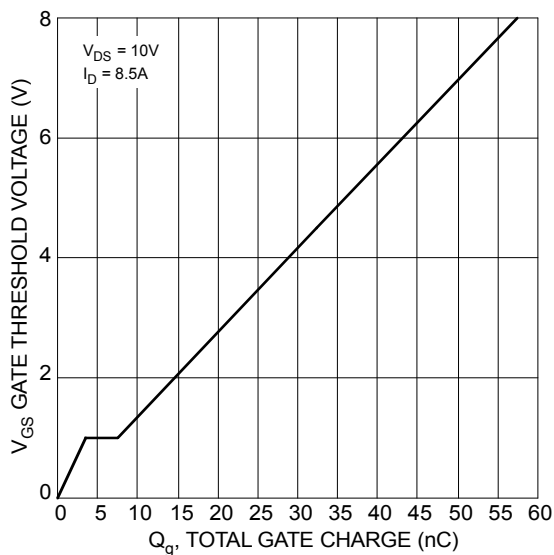
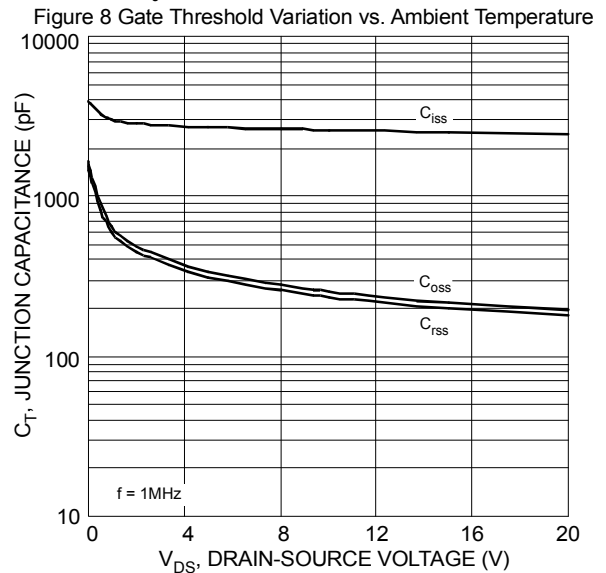
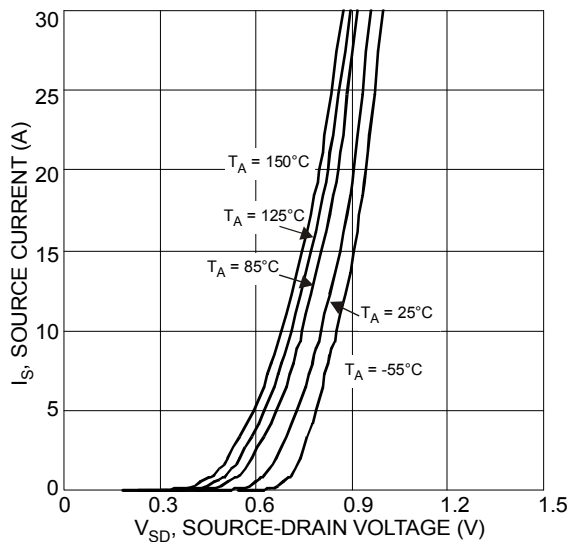
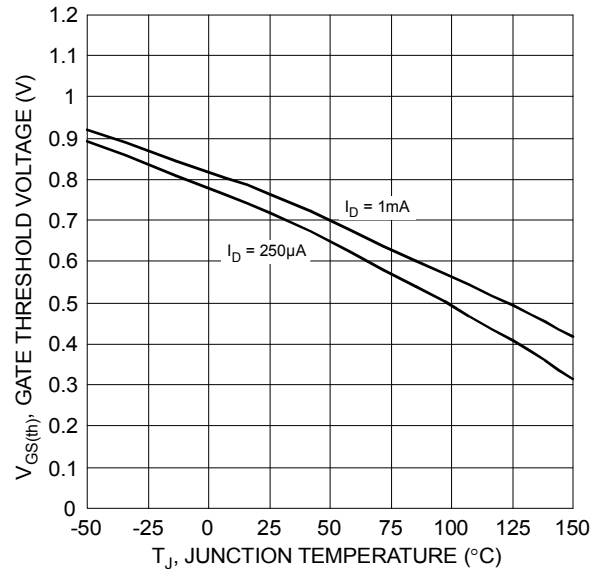
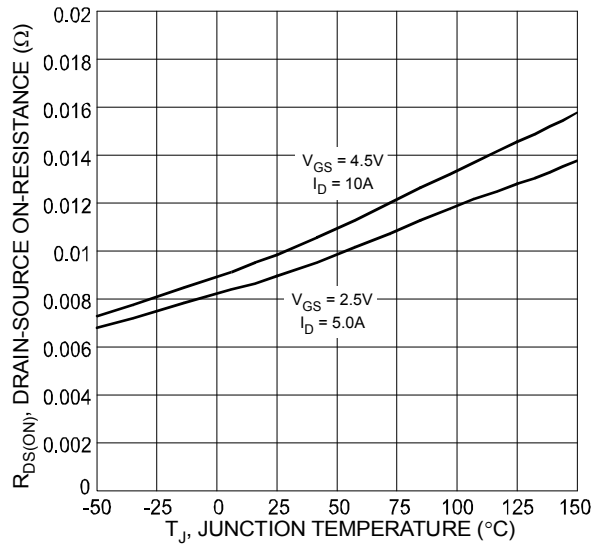
Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	P _D	0.78	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	163	°C/W
Power Dissipation (Note 6)	P _D	2.14	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	R _{θJA}	59	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	1	μA	V _{DS} = 16V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	0.5	—	1.1	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	8.4	11.5	mΩ	V _{GS} = 4.5V, I _D = 8.5A
			8.5	12.0		V _{GS} = 4.0V, I _D = 8.5A
			8.6	12.5		V _{GS} = 3.5V, I _D = 8.5A
			9.0	13.5		V _{GS} = 3.1V, I _D = 8A
			9.6	14.0		V _{GS} = 2.5V, I _D = 8A
Forward Transfer Admittance	Y _{fs}	—	18.2	—	S	V _{DS} = 5V, I _D = 4A
Diode Forward Voltage	V _{SD}	—	—	1.2	V	V _{GS} = 0V, I _S = 8.5A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	2607	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	255	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	236	—	pF	
Gate Resistance	R _g	—	1.2	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	32.4	—	nC	V _{DS} = 10V, I _D = 8.5A
Total Gate Charge (V _{GS} = 8V)	Q _g	—	57.4	—	nC	
Gate-Source Charge	Q _{gs}	—	3.5	—	nC	
Gate-Drain Charge	Q _{gd}	—	4.0	—	nC	
Turn-On Delay Time	t _{D(on)}	—	8.6	—	ns	V _{DS} = 10V, I _D = 8.5A V _{GS} = 4.5V, R _G = 1.8Ω
Turn-On Rise Time	t _r	—	20.3	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	42.5	—	ns	
Turn-Off Fall Time	t _f	—	13.7	—	ns	

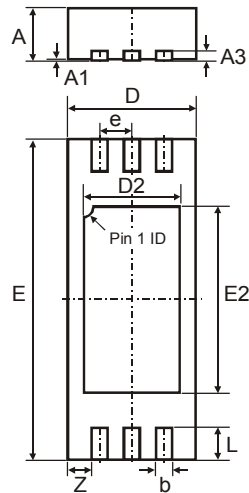
- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate
 - Repetitive rating, pulse width limited by junction temperature.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.





Package Outline Dimensions

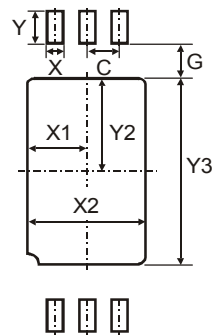
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



W-DFN5020-6			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0	0.05	0.02
A3	—	—	0.15
b	0.20	0.30	0.25
D	1.90	2.10	2.00
D2	1.40	1.60	1.50
e	—	—	0.50
E	4.90	5.10	5.00
E2	2.80	3.00	2.90
L	0.35	0.65	0.50
Z	—	—	0.375
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.50
G	0.35
X	0.35
X1	0.90
X2	1.80
Y	0.70
Y2	1.60
Y3	3.20

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