

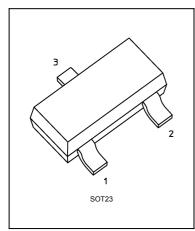
Schottky Barrier Diode Silicon Epitaxial

# TBAT54, TBAT54A, TBAT54C, TBAT54S

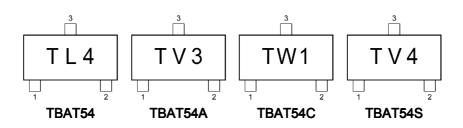
#### 1. Applications

• Ultra-High-Speed Switching

#### 2. Packaging

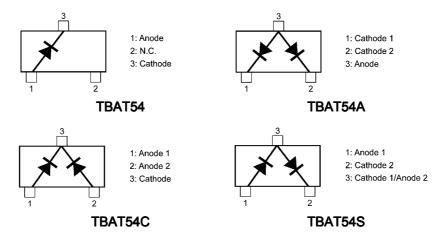


#### 3. Marking



Part Number	Marking Code	Configuration
TBAT54	TL4	single
TBAT54A	TV3	common anode
TBAT54C	TW1	common cathode
TBAT54S	TV4	series

#### 4. Internal Circuit



#### 5. Absolute Maximum Ratings (Note) (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Peak reverse voltage	V <sub>RM</sub>		35	V
Reverse voltage	V <sub>R</sub>		30	
Average rectified current	Ι <sub>Ο</sub>	(Note 3)	200	mA
Peak forward current	I <sub>FM</sub>	(Note 3)	300	
Non-repetitive peak forward surge current	I <sub>FSM</sub>	(Note 1), (Note 3)	1	A
Power dissipation	PD	(Note 2), (Note 3)	320	mW
Junction temperature	Tj		150	°C
Storage temperature	T <sub>stg</sub>		-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Measured with a 10 ms pulse.

Note 2: Mounted on an FR4 board (25.4 mm  $\times$  25.4 mm  $\times$  1.6 mm, Cu Pad: 0.42 mm²  $\times$  3)

Note 3: Unit rating. Total rating = unit rating × 1.5 (TBAT54A,TBAT54C), Total rating = unit rating × 0.7 (TBAT54S)

#### 6. Electrical Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 0.1 mA		0.16	_	V
		I <sub>F</sub> = 1 mA		0.21	0.32	
		I <sub>F</sub> = 10 mA		0.28	0.39	
		I <sub>F</sub> = 30 mA		0.37	0.50	
		I <sub>F</sub> = 100 mA		0.45	0.58	
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 25 V	_	0.6	2	μA
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 10 mA	_	1.5	—	ns

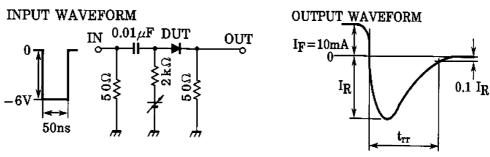
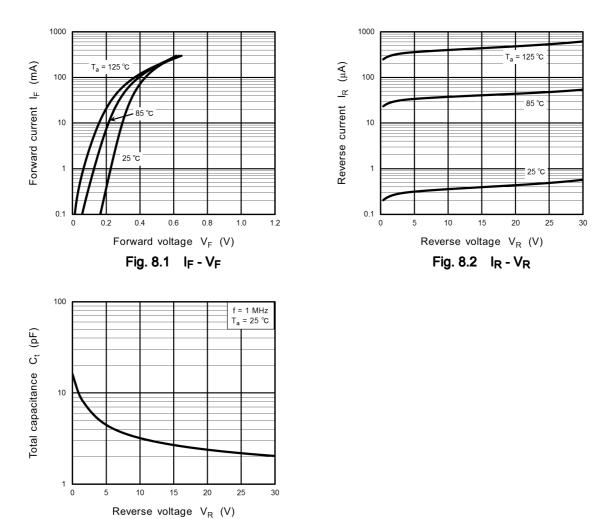


Fig. 6.1 Reverse recovery time (trr) test circuit

#### 7. Usage Considerations

• Schottky barrier diodes (SBDs) have reverse leakage greater than other types of diodes. This makes SBDs more susceptible to thermal runaway under high-temperature and high-voltage conditions. Thus, both forward and reverse power losses of SBDs should be considered for thermal and safety design.

#### 8. Characteristics Curves (Note)



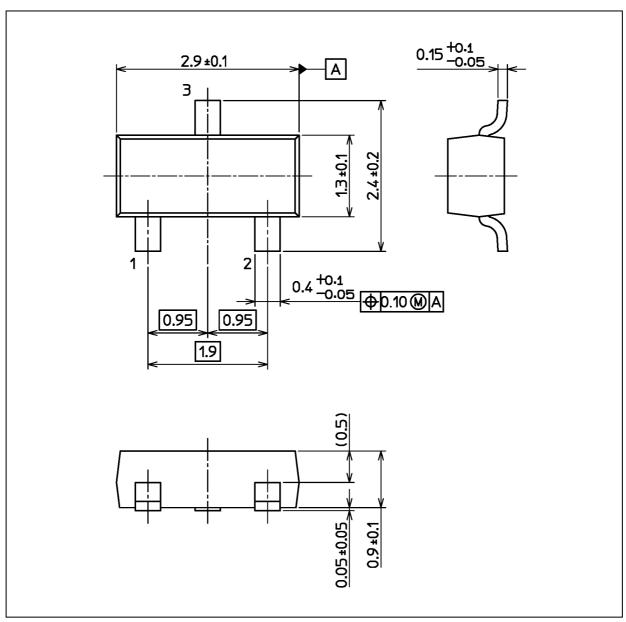
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Fig. 8.3 Ct - VR



#### **Package Dimensions**

Unit: mm



#### Weight: 0.009 g (typ.)

	Package Name(s)
Nickname: SOT23	

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