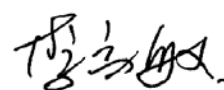


Product Specification

Product Name: VGM096032A3W03

Product Code: M01633

Customer
Approved by Customer
Approved Date:

Designed By	Checked By	Approved By	
		R&D	QA
 2015.4.8	 2015.4.11	 2015.4.10	 4/10/15

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1 Overview

VGM096032A3W03 is a monochrome OLED display module with 96×32 dot matrix. The characteristics of this display module are high brightness, self-emission, high contrast ratio, slim/thin outline, wide viewing angle, wide temperature range, and low power consumption.

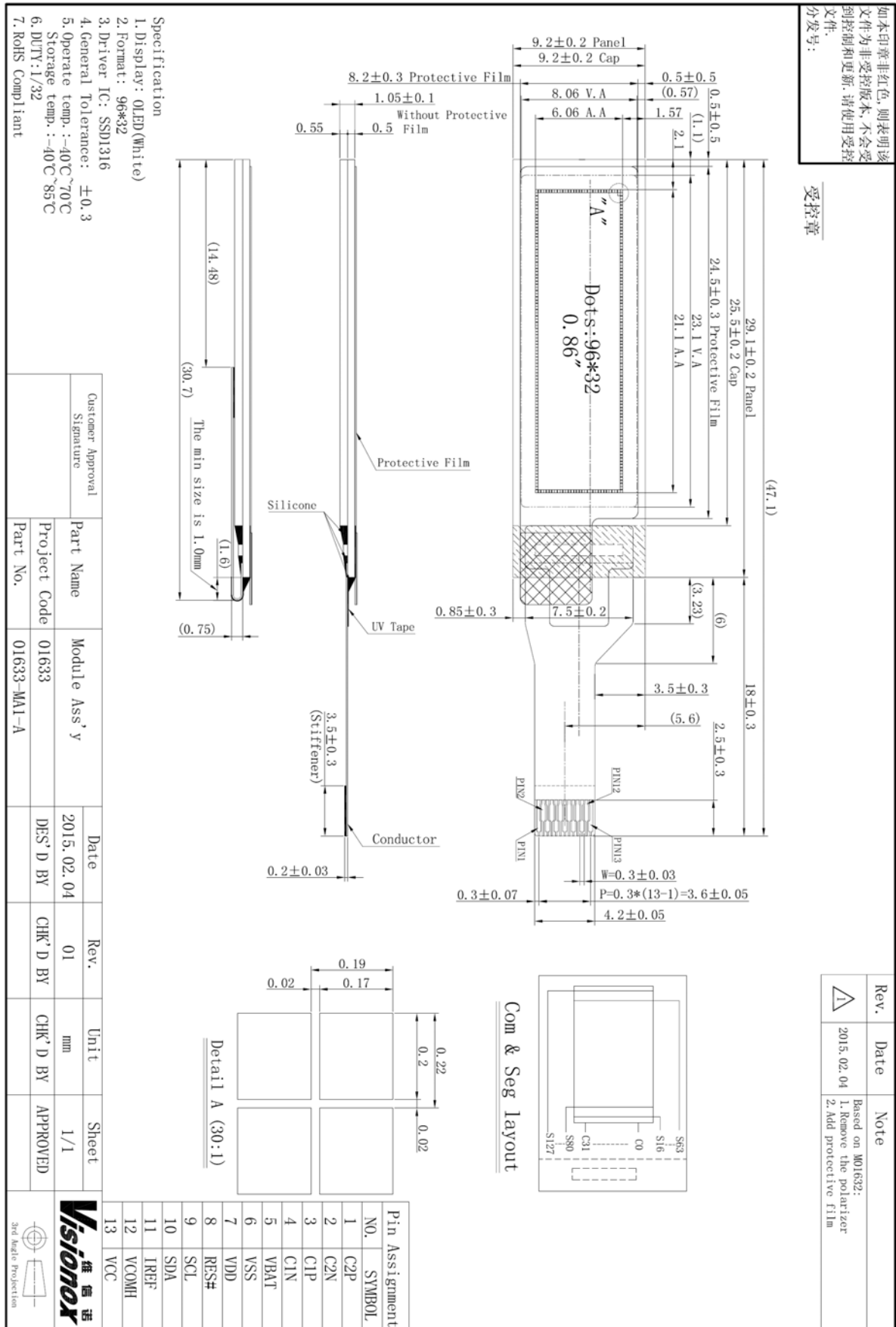
2 Features

- Display Color: White
- Dot Matrix: 96×32
- Driver IC: SSD1316
- Interface: I²C
- Wide range of operating temperature: -40°C to 70°C

3 Mechanical Data

NO.	ITEM	SPECIFICATION	UNIT
1	Dot Matrix	$96(\text{W}) \times 32(\text{H})$	-
2	Dot Size	$0.2(\text{W}) \times 0.17(\text{H})$	mm^2
3	Dot Pitch	$0.22(\text{W}) \times 0.19(\text{H})$	mm^2
4	Aperture Rate	81	%
5	Active Area	$21.1(\text{W}) \times 6.06(\text{H})$	mm^2
6	Panel Size	$29.1(\text{W}) \times 9.2(\text{H}) \times 1.05(\text{T})$	mm^3
7	Module Size	$47.1(\text{W}) \times 9.2(\text{H}) \times 1.05(\text{T})$	mm^3
8	Diagonal A/A Size	0.86	inch
9	Module Weight	$0.70 \pm 10\%$	gram

4 Mechanical Drawing



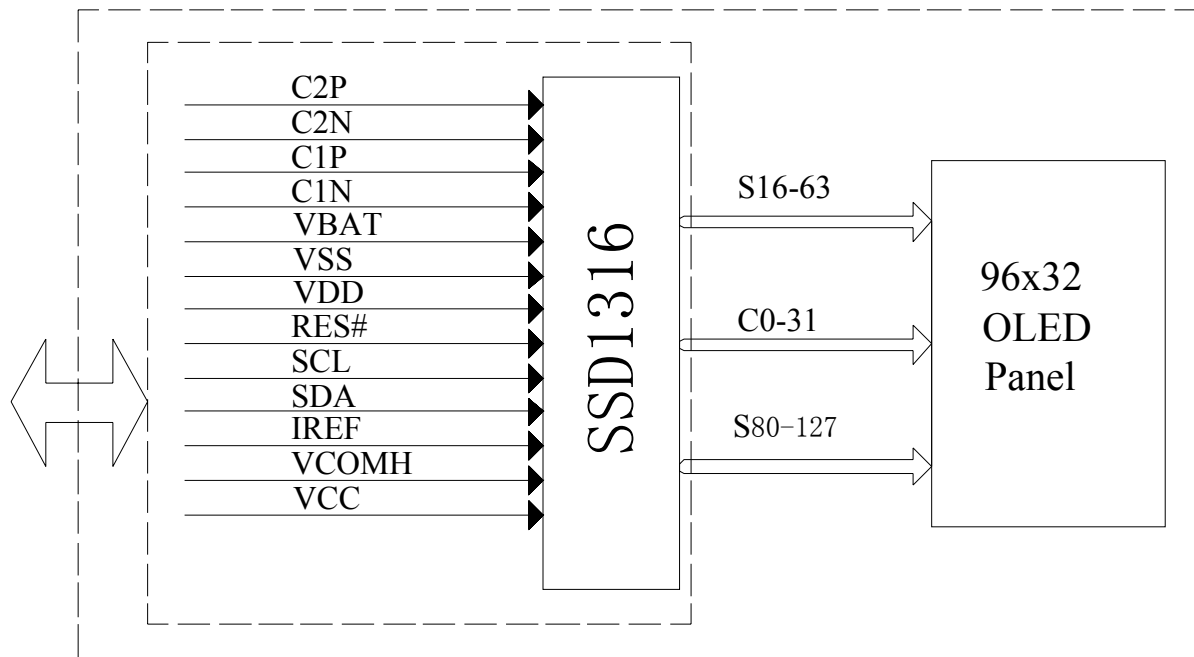
5 Module Interface

PIN NO.	PIN NAME	DESCRIPTION
1	C2P	C1P/C1N – Pin for charge pump capacitor; Connect to each other with a capacitor. C2P/C2N – Pin for charge pump capacitor; Connect to each other with a capacitor.
2	C2N	
3	C1P	
4	C1N	
5	VBAT	Power supply for charge pump regulator circuit. Table 5.1
6	VSS	This is a ground pin.
7	VDD	Power supply pin for core logic operation.
8	RES#	This pin is reset signal input. When the pin is LOW, initialization of the chip is executed.
9	SCL	Serve clock pin.
10	SDA	Serve data pin.
11	IREF	This pin is segment current reference pin. A resistor should be connected between this pin and VSS.
12	VCOMH	This pin is the input pin for the voltage output high level for COM signals. A capacitor should be connected between this pin and VSS.
13	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin. When charge pump is enabled, a capacitor should be connected between this pin and VSS.

Table 5.1

Status	VBAT	VDD	VCC
Enable charge pump	Connect to external VBAT source	Connect to external VDD source	A capacitor should be connected between this pin and VSS
Disable charge pump	Keep float	Connect to external VDD source	Connect to external VCC source

6 Function Block Diagram



7 Absolute Maximum Ratings

ITEM	SYMBOL	MIN	MAX	UNIT	REMARK
Supply Voltage	VDD	-0.3	4.0	V	IC maximum rating
	VBAT	-0.3	5.0	V	IC maximum rating
OLED Operating Voltage	VCC	0	16	V	IC maximum rating
Operating Temp.	Top	-40	70	°C	-
Storage Temp	Tstg	-40	85	°C	-

Note (1): All of the voltages are on the basis of “VSS = 0V”.

Note (2): Permanent breakage of module may occur if the module is used beyond the maximum rating. The module can be normal operated under the conditions according to Section 8 “Electrical Characteristics”.

Malfunctioning of the module may occur and the reliability of the module may deteriorate if the module is used beyond the conditions.

8 Electrical Characteristics

8.1 DC Electrical Characteristics

ITEM	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Logic Supply Voltage	VDD	22±3°C, 55±15%R.H	1.65	2.8	3.3	V
Supply Voltage for DC/DC	VBAT	22±3°C, 55±15%R.H	3.0	3.7	4.2	
OLED Driver Supply Voltage (Generated by Internal DC/DC)	VCC	22±3°C, 55±15%R.H	-	9.0	-	V
High-level Input Voltage	V _{IH}	-	0.8 × VDD	-	-	V
Low-level Input Voltage	V _{IL}	-	-	-	0.2 × VDD	V
High-level Output Voltage	V _{OH}	-	0.9 × VDD	-	-	V
Low-level Output Voltage	V _{OL}	-	-	-	0.1 × VDD	V

Note : The VCC input must be kept in a stable value; ripple and noise are not allowed.

8.2 Electro-optical Characteristics

ITEM	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Normal Mode Brightness	L_{br}	All pixels ON(1) (VCC generated by internal DC/DC)	380	480	-	cd/m ²
ICC,Sleep mode Current	ICC,SLEEP	VDD=1.65V~3.3V,VCC=7~15V Display OFF,No panel attached	-	-	20	uA
IDD,Sleep mode Current	IDD,SLEEP	VDD=1.65V~3.3V,VCC=7~15V Display OFF,No panel attached	-	-	20	uA
Normal Mode Power Consumption (VCC Generated by Internal DC/DC)	$P_t=(I_{BAT} \times V_{BAT})$	All pixels ON(1) (VCC generated by internal DC/DC)	-	79.55	96.2	mW
C.I.E(White)	(x)	x,y(CIE1931)	0.26	0.30	0.34	-
	(y)		0.29	0.33	0.37	-
Dark Room Contrast	CR	-	$\geq 2000:1$	-	-	-
Response Time	-	-	---	10	-	μs
View Angle	-	-	≥ 160	-	-	Degree

Note(1): Normal Mode test conditions are as follows:

- Driving voltage : 9.0V(VCC Supplied Externally) or VBAT:3.7V(VCC Generated by Internal DC/DC).
- Frame rate : 115Hz
- Contrast setting : 0x75
- Duty setting : 1/32

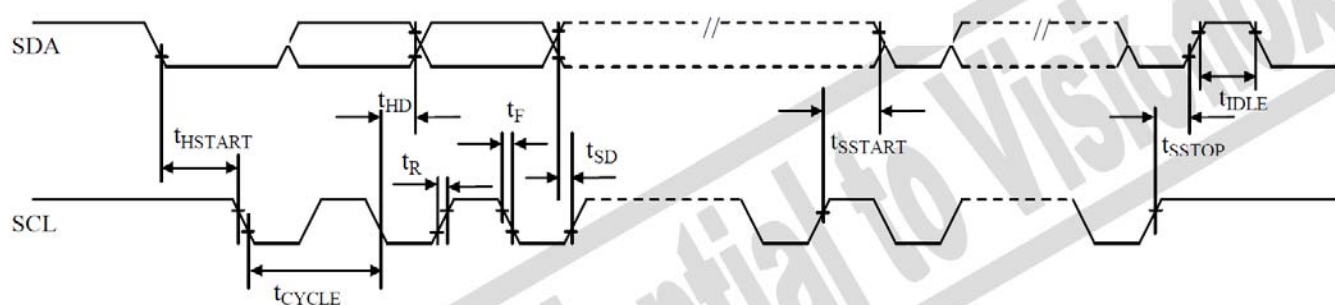
8.3 AC Electrical Characteristics

I²C Interface Timing Characteristics

(VDD - VSS = 1.65V to 3.3V, TA = 25°C)

Symbol	Parameter	Min	Typ	Max	Unit
t _{cycle}	Clock Cycle Time	2.5	-	-	us
t _{HSTART}	Start condition Hold Time	0.6	-	-	us
t _{HD}	Data Hold Time (for “SDA _{OUT} ” pin)	0	-	-	ns
	Data Hold Time (for “SDA _{IN} ” pin)	300	-	-	ns
t _{SD}	Data Setup Time	100	-	-	ns
t _{SSTART}	Start condition Setup Time (Only relevant for a repeated Start condition)	0.6	-	-	us
t _{SSTOP}	Stop condition Setup Time	0.6	-	-	us
t _R	Rise Time for data and clock pin	-	-	300	ns
t _F	Fall Time for data and clock pin	-	-	300	ns
t _{IDLE}	Idle Time before a new transmission can start	1.3	-	-	us

I²C interface characteristics

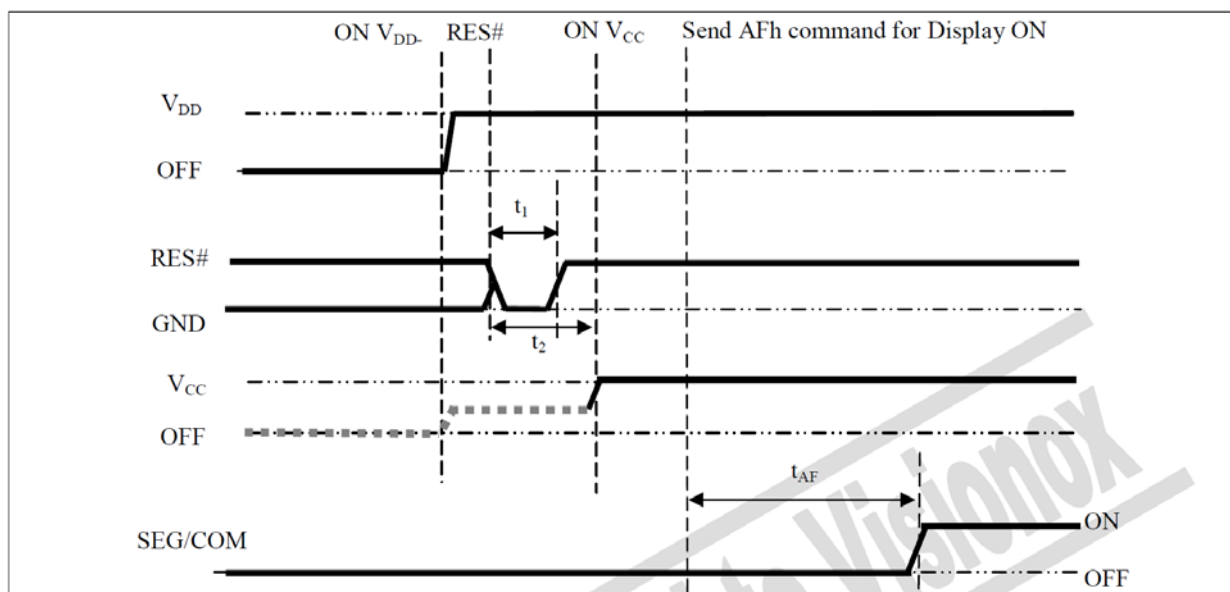


9 Functional Specification and Application Circuit

9.1 Power ON and Power OFF Sequence with External VCC

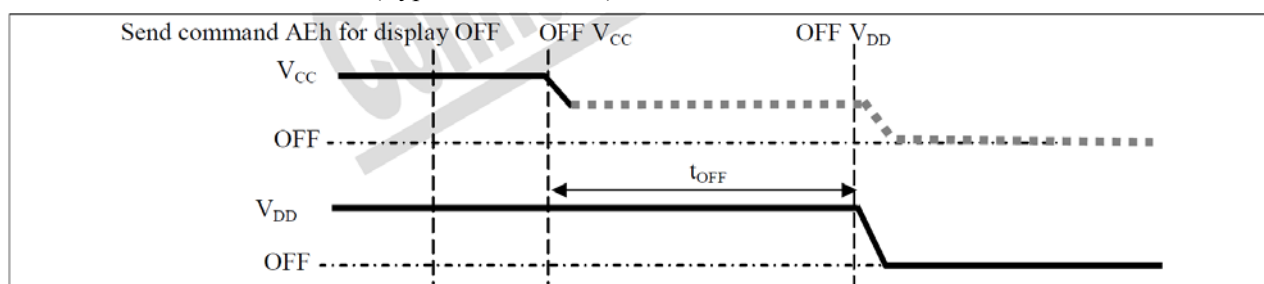
Power ON Sequence:

1. Power ON VDD
2. After VDD become stable, set RES# pin LOW (logic low) for at least $3\mu s$ (t_1)⁽⁴⁾ and then HIGH (logic high).
3. After set RES# pin LOW (logic low), wait for at least $3\mu s$ (t_2). Then Power ON VCC⁽¹⁾.
4. After VCC become stable, send command AFh for display ON. SEG/COM will be ON after $100ms$ (t_{AF}).



Power OFF Sequence:

1. Send command AEh for display OFF.
2. Power OFF VCC⁽¹⁾⁽²⁾⁽³⁾.
3. Power OFF VDD after t_{OFF} .⁽⁵⁾ (Typical $t_{OFF}=100ms$)



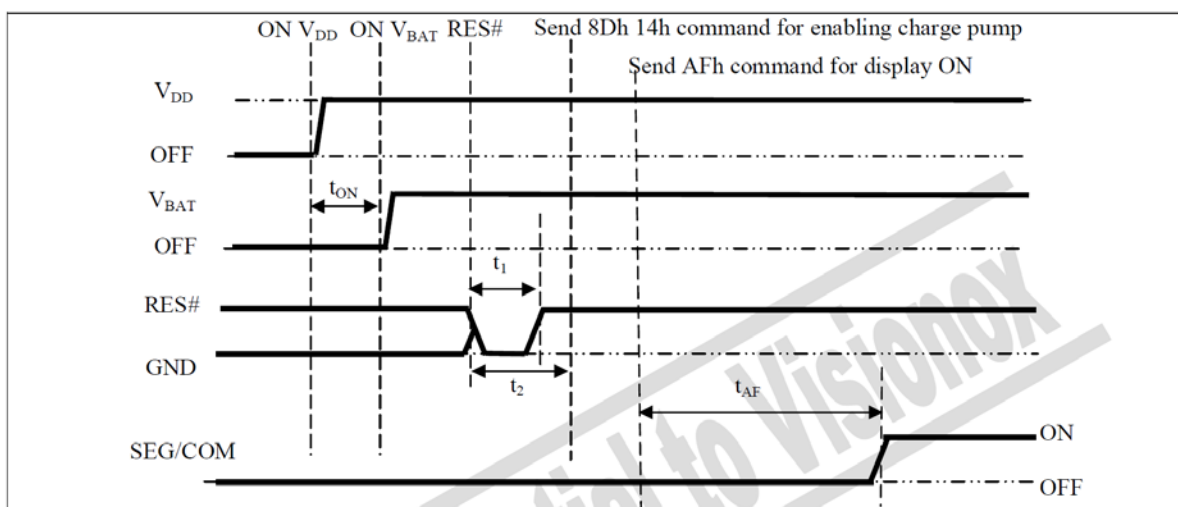
Note:

- (1) Since an ESD protection circuit is connected between VDD and VCC, VCC becomes lower than VDD whenever VDD is ON and VCC is OFF as shown in the dotted line of VCC in above figures.
- (2) VCC should be kept float (disable) when it is OFF.
- (3) Power Pins(VDD, VCC) can never be pulled to ground under any circumstance.
- (4) The register values are reset after t_1 .
- (5) VDD should not be Power OFF before VCC Power OFF.

9.2 Power ON and OFF sequence with Charge Pump Application

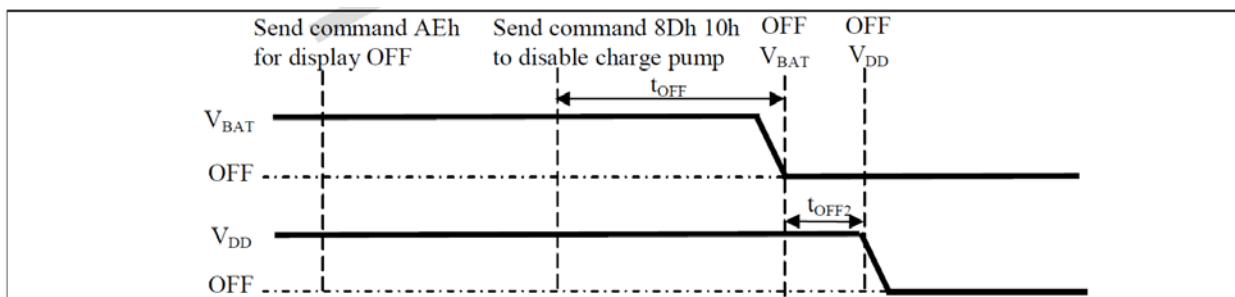
Power ON sequence:

1. Power ON VDD
2. Wait for t_{ON} . Power ON VBAT.^{(1), (2)} (where Minimum $t_{ON} = 0ms$)
3. After VBAT become stable, set RES# pin LOW (logic low) for at least 3us (t_1)⁽³⁾ and then HIGH (logic high).
4. After set RES# pin LOW (logic low), wait for at least 3us (t_2). Then input commands with below sequence:
 - a. 8Dh 14h for enabling charge pump at 9.0V mode
 - b. AFh for display ON
5. SEG/COM will be ON after 100ms (t_{AF}).



Power OFF sequence:

1. Send command AEh for display OFF
2. Send command 8Dh 10h to disable charge pump
3. Power OFF VBAT after t_{OFF} .^{(1), (2)} (Typical $t_{OFF} = 100ms$)
4. Power OFF VDD after t_{OFF2} . (where Minimum $t_{OFF2} = 0ms$)⁽⁴⁾, Typical $t_{OFF2} = 5ms$)

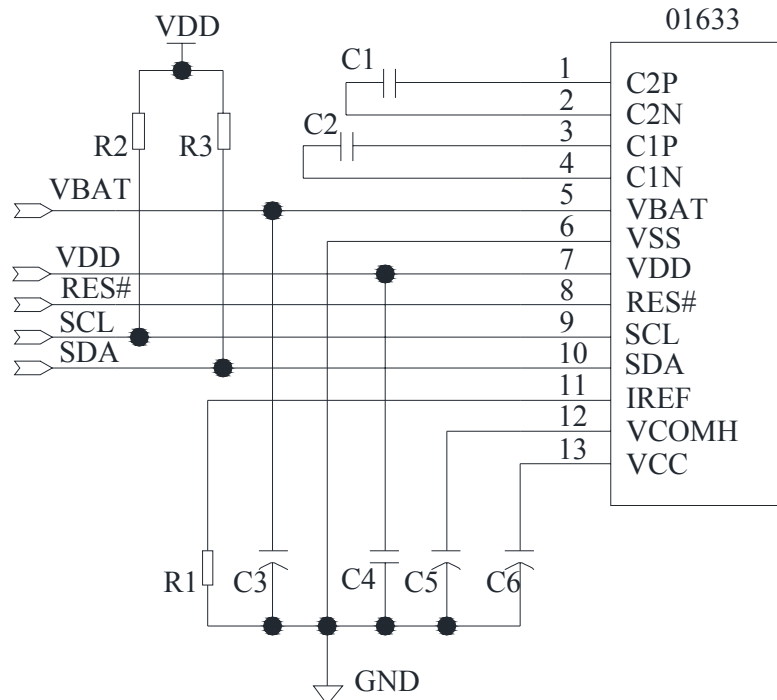


Note:

- (1) VBAT should be kept float (i.e. disable) when it is OFF.
- (2) Power Pins (VDD, VBAT) can never be pulled to ground under any circumstance.
- (3) The register values are reset after t_1 .
- (4) VDD should not be Power OFF before VBAT Power OFF

9.3 Application Circuit

1) Application Example of M01633 with Internal Charge Pump and I²C mode.



Pin connected to MCU interface: SCL, SDA, RES#

Under Internal DC/DC Mode, the Charge Pump Setting(8Dh) must be set as follow:

8Dh: Charge Pump Setting

14h; Enable Charge Pump at 9.0V mode

The I²C address is 0x78.

Recommended components

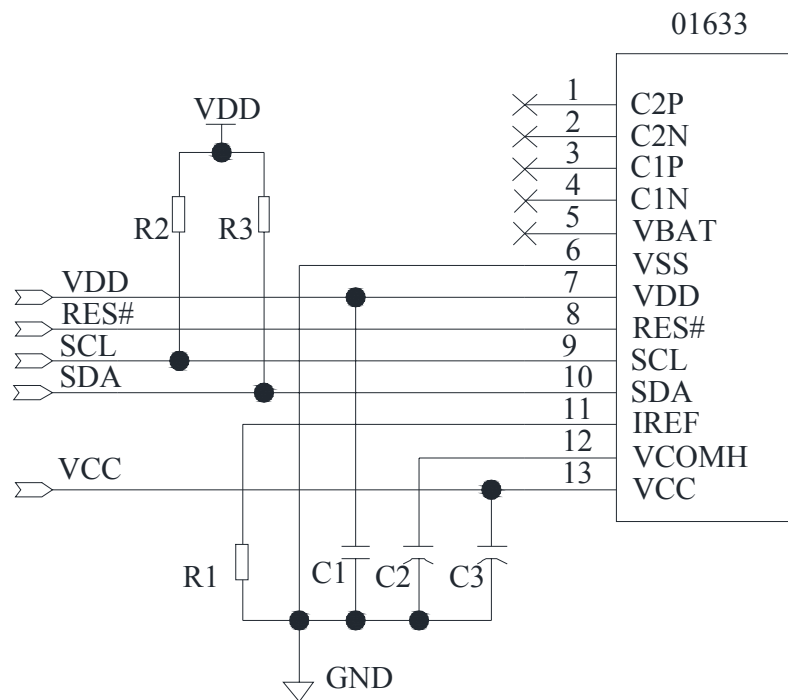
C3,C5,C6: 4.7μF/16V.RoHS (Tantalum Capacitors)

C1,C2,C4: 1uF-0603-X7R±10%.RoHS

R1: 0603 1/10W +/-5% 390Kohm.RoHS

R2,R3: 0603 1/10W +/-5% 10Kohm.RoHS

2) Application Example of M01633 with External VCC and I²C mode.



Pin connected to MCU interface: SCL, SDA, RES#

C1P, C1N, C2P, C2N, VBAT should be left open.

Under external VCC Mode, the Charge Pump Setting(8Dh) must be set as follow:

8Dh: Charge Pump Setting

10h; Disable Charge Pump

The I²C address is 0x78.

Recommended components

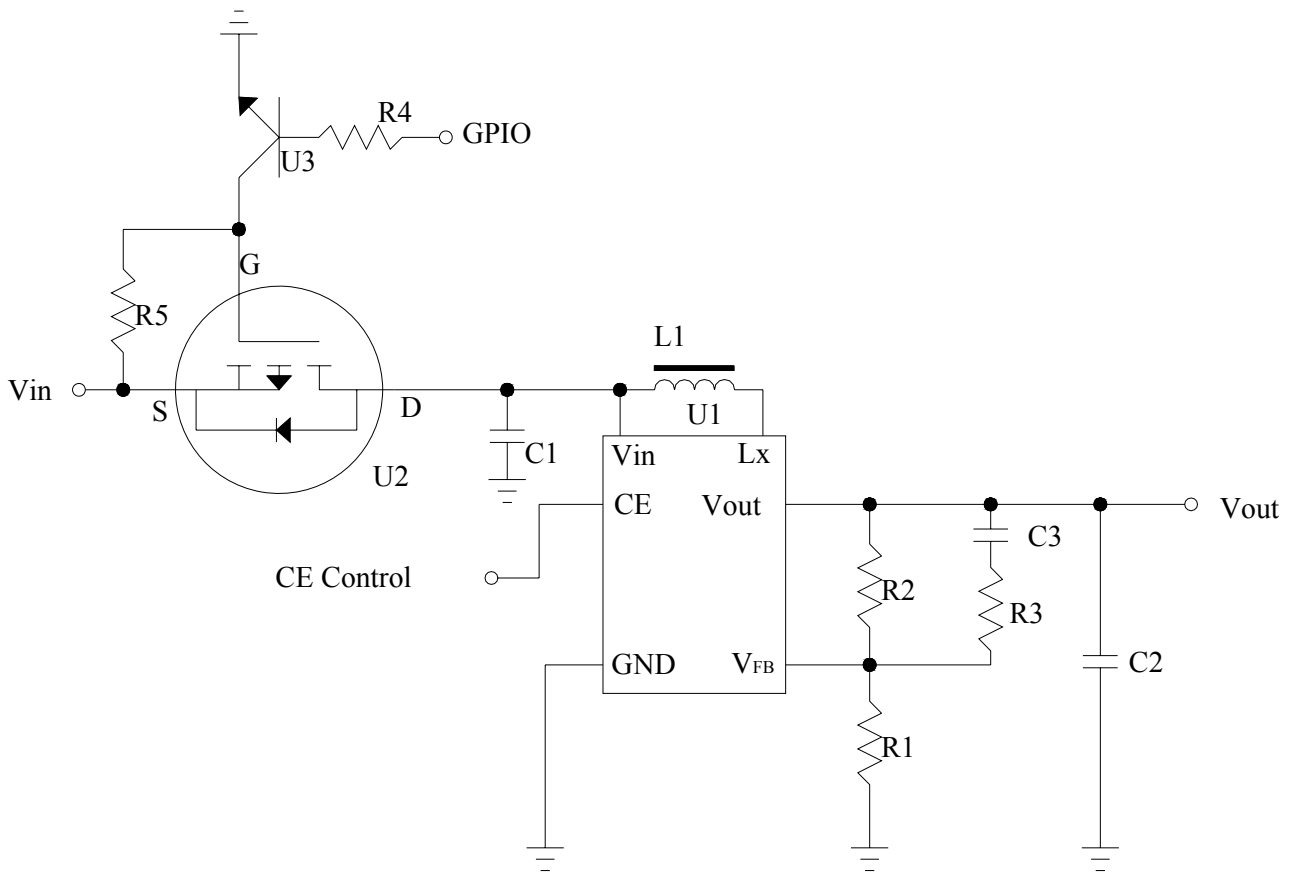
C2,C3: 4.7μF/16V.RoHS (Tantalum Capacitors)

C1: 1uF-0603-X7R±10%.RoHS

R1: 0603 1/10W +/-5% 390Kohm.RoHS

R2,R3: 0603 1/10W +/-5% 10Kohm.RoHS

9.4 External DC-DC application circuit



Recommend component

The C1	: 1 uF-0603-X7R±10%.RoHS
The C2	: 1 uF-0603-X7R±10%.RoHS
The C3	: 220pF-0603-X7R±10%.RoHS
The R1	: 0603 1/10W +/-5% 10Kohm.RoHS
The R2	: 0603 1/10W +/-1% 80Kohm.RoHS
The R3	: 0603 1/10W +/-5% 2Kohm.RoHS
The R4	: 0603 1/10W +/-5% 1Kohm.RoHS
The R5	: 0603 1/10W +/-5% 10Kohm.RoHS
The L1	: 22uH
The U1	: R1200
The U2	: FDN338P
The U3	: 8050

9.5 Display Control Instruction

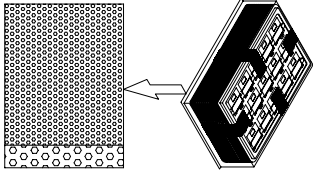
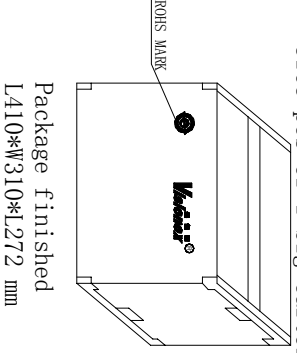
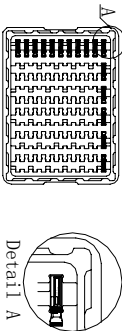
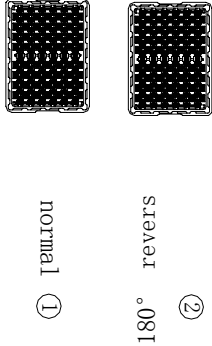
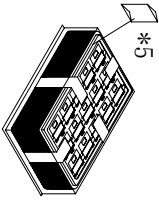
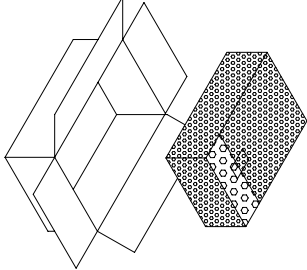
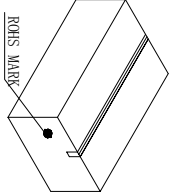
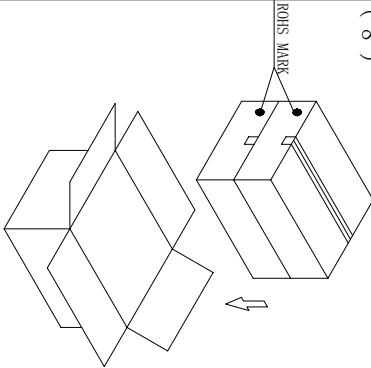
Refer to SSD1316 IC Specification.

9.6 Recommended Software Initialization

In order to ensure the reliability and stability of the module, the module must initialize use the following code, Malfunctioning of the module may occur and the reliability of the module may deteriorate if the module is used beyond the initialize code.

```
void Init_IC()
{
    Write_Command(0xAE);    //Set Display Off
    Write_Command(0xD5);    //Display divide ratio/osc. freq. mode
    Write_Command(0xC1);
    Write_Command(0xA8);    //Multiplex ration mode:
    Write_Command(0x1F);
    Write_Command(0xAD);    //External or Internal VCOMH IREF Selection
    Write_Command(0x00);    // Internal VCOMH/ External IREF
    Write_Command(0x20);    //Set Memory Addressing Mode
    Write_Command(0x02);    //Page Addressing Mode
    Write_Command(0xD3);    //Set Display Offset
    Write_Command(0x00);
    Write_Command(0x40);    //Set Display Start Line
    Write_Command(0x8D);    //DC-DC Control Mode Set
    Write_Command(0x14);    //DC-DC ON/OFF Mode Set
    Write_Command(0xA0);    //Segment Remap
    Write_Command(0xC8);    //Set COM Output Scan Direction
    Write_Command(0xDA);    // Set seg pads hardware
    Write_Command(0x12);
    Write_Command(0x81);    //Contrast control
    Write_Command(0x75);
    Write_Command(0xD9);    //Set pre-charge period
    Write_Command(0x22);
    Write_Command(0xDB);    //VCOM deselect level mode
    Write_Command(0x00);
    Write_Command(0xA4);    //Set Entire Display On/Off
    Write_Command(0xA6);    //Set Normal Display
    Clear_Screen();
    Write_Command(0xAF);    //Set Display On
}
```


10 Package Specification

Controlled Seal		Packing Process (1)~(9)			
(5) After tray be packaged, wrap the package in a bubble bag and seal with scotch tape. 	(9) 44 contained trays, 2 empty trays, Package quantity products: 3168 pcs of 1 big carton 	(1) TRAY Type:01632-MT6-A 	(2) 	(3) order ①、② ①、② fix trays with tape 1584 pcs of 1 small carton 1 tray contain 72 pcs 22 contained trays, 1 empty tray 	(4) package with plastic bags add five desiccants create a power vacuum *5 
		(6) 	(7) small carton package 	(8) 	2 small cartons in 1 big carton

NOTE:1、 The inner carton and master carton must be sealed with adhesive tape.

2、 Fill up the gap with EPE.

3、 If the customer has special needs with the RoHS making, the inner carton and master carton need adhesive new RoHS marking at .

11 Reliability

11.1 Reliability Test

NO.	ITEM	CONDITION	QUANTITY
1	High Temperature (Non-operation)	85°C,240hrs	4
2	Low Temperature (Non-operation)	-40°C,240hrs	4
3	High Temperature (Operation)	70°C,240hrs	4
4	Low Temperature (Operation)	-40°C,240hrs	4
5	High Temperature / High Humidity (Operation)	60°C,90%RH,240hrs	4
6	Thermal shock (Non-operation)	-40°C~85°C (-40°C/30min;transit/3min;85°C/30min;transit/3min) 1cycle: 66min,30cycles	4
7	Vibration	Frequency: 5~50Hz,0.5G Scan rate: 1 oct/min Time: 2 hrs/axis Test axis: X,Y, Z	1 Carton
8	Drop	Height: 100 cm Sequence: 1 angle, 3 edges and 6 faces	1 Carton

Test and measurement conditions

1. All measurements shall not be started until the specimens attain to temperature stability, the stable time is at least 15 minutes.
2. The degradation of polarizer is ignored for item 5.
3. The tolerance of temperature is $\pm 3^{\circ}\text{C}$, and the tolerance of relative humidity is $\pm 5\%$.

Evaluation criteria

1. The function test is OK.
2. No observable defects.
3. Luminance: $\geq 50\%$ of initial value.
4. Current consumption: within $\pm 50\%$ of initial value.

11.2 Lifetime

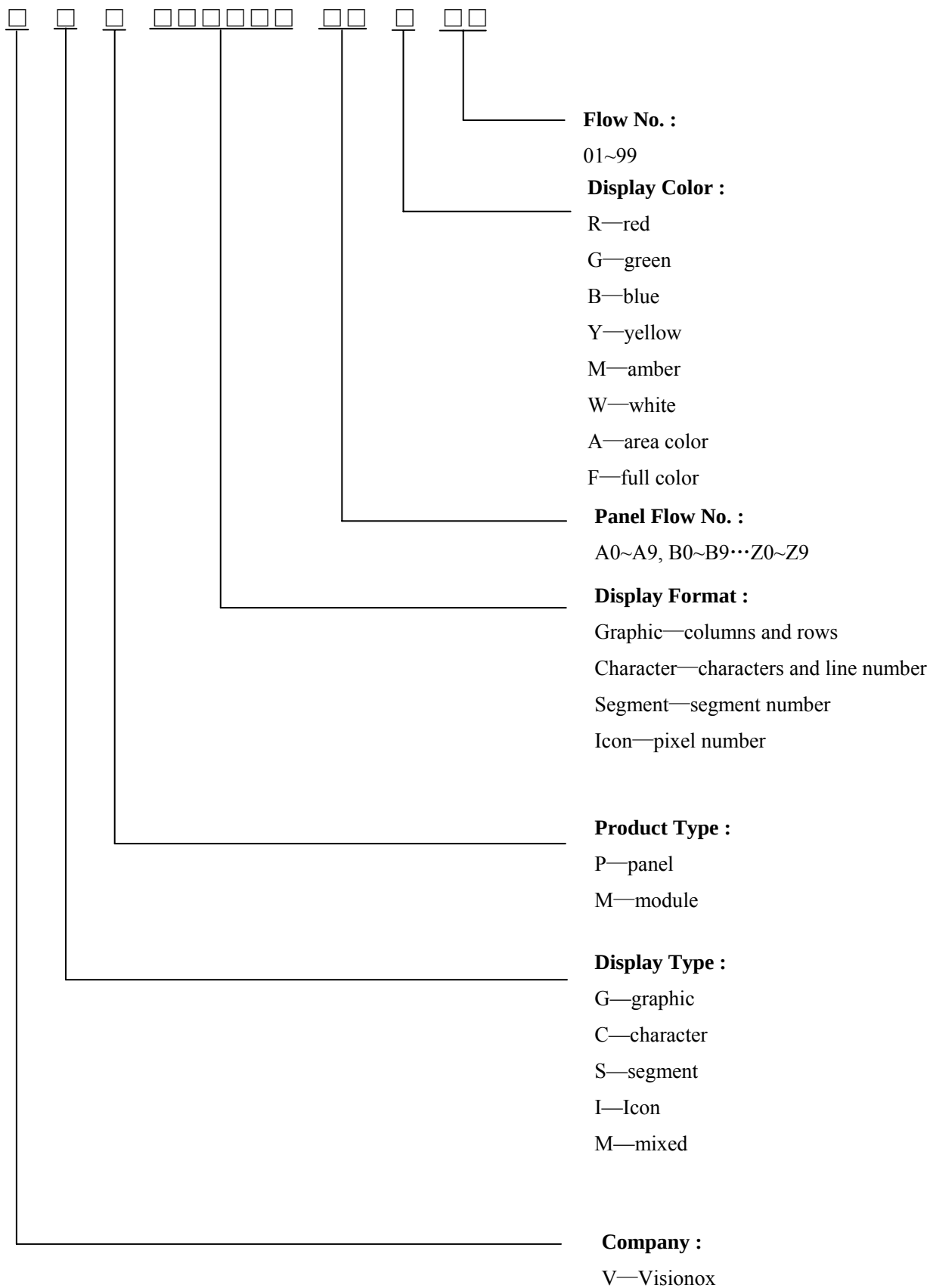
End of lifetime is specified as 50% of initial brightness and the test pattern at operating condition is 50% alternating checkerboard.

ITEM	MIN	MAX	UNIT	CONDITION
Operation Life Time	7,000	-	hrs	480 cd/m ² , 50% alternating checkerboard, 22 $\pm$ 3°C, 55 $\pm$ 15% RH

11.3 Failure Check Standard

After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 22 $\pm$ 3°C; 55 $\pm$ 15% RH.

11 Illustration of OLED Product Name



12 Outgoing Quality Control Specifications

12.1 Sampling Method

- (1) GB/T 2828.1-2003/ISO2859-1: 1999, inspection level II, normal inspection, single sample inspection
- (2) AQL: Major 0.65; Minor 1.0

12.2 Inspection Conditions

The environmental conditions for test and measurement are performed as follows.

Temperature: $22 \pm 3^{\circ}\text{C}$

Humidity: $55 \pm 15\% \text{R.H}$

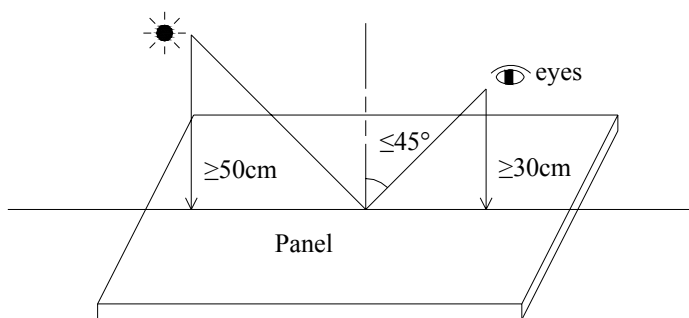
Fluorescent Lamp: 30W

Distance between the Panel & Lamp: $\geq 50\text{cm}$

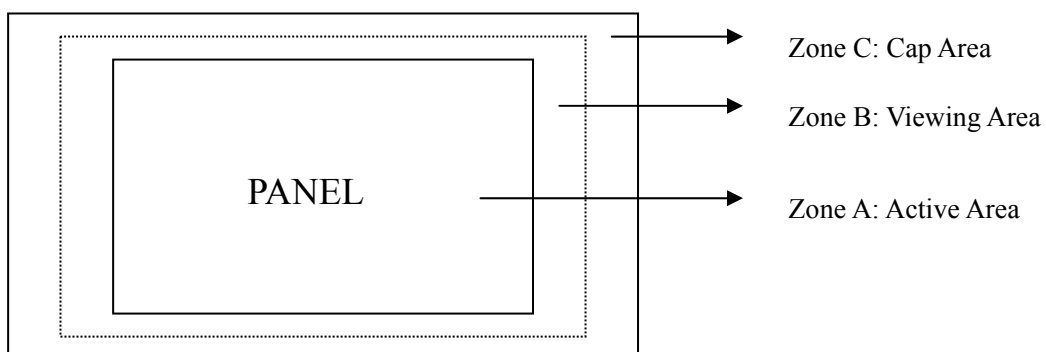
Distance between the Panel & Eyes: $\geq 30\text{cm}$

Viewing angle from the vertical in each direction: $\leq 45^{\circ}$

(See the sketch below)

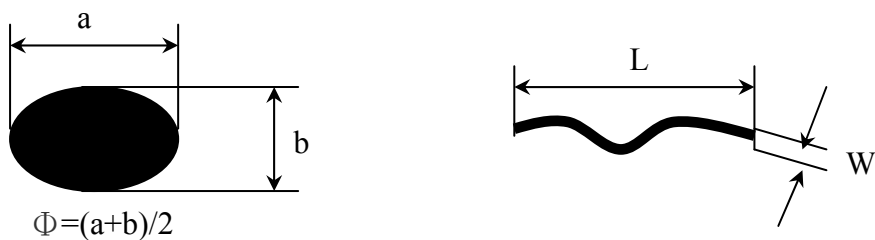


12.3 Quality Assurance Zones



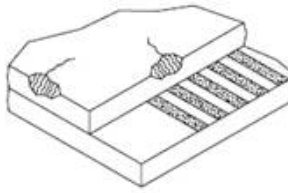
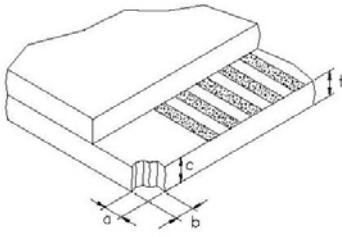
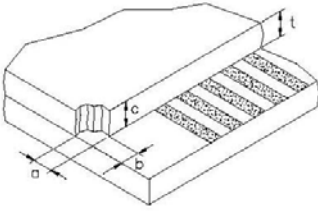
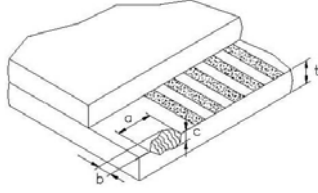
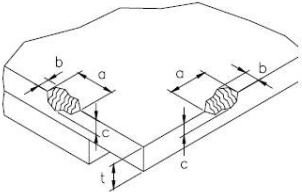
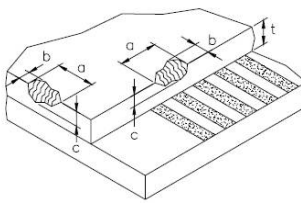
12.4 Inspection Standard

Definition of Φ & L & W (Unit: mm)



I . Appearance Defects

NO.	ITEM	CRITERIA				CLASSIFICATION																
1	Polarizer Black or White spot, Dirty spot, Foreign matter, Dent on the polarizer	<table><tr><td rowspan="2">Average Diameter (mm)</td><td colspan="2">Acceptable Number</td></tr><tr><td>Zone A,B</td><td>Zone C</td></tr><tr><td>$\Phi\leq0.15$</td><td>Ignore</td><td rowspan="3">Ignore</td></tr><tr><td>$0.15<\Phi\leq0.30$</td><td>3</td></tr><tr><td>$\Phi>0.30$</td><td>0</td></tr></table>				Average Diameter (mm)	Acceptable Number		Zone A,B	Zone C	$\Phi\leq0.15$	Ignore	Ignore	$0.15<\Phi\leq0.30$	3	$\Phi>0.30$	0	Minor				
Average Diameter (mm)	Acceptable Number																					
	Zone A,B	Zone C																				
$\Phi\leq0.15$	Ignore	Ignore																				
$0.15<\Phi\leq0.30$	3																					
$\Phi>0.30$	0																					
2	Scratch/line on the glass/Polarizer	<table><tr><td rowspan="2">Width (mm)</td><td rowspan="2">Length (mm)</td><td colspan="2">Acceptable Number</td></tr><tr><td>Zone A,B</td><td>Zone C</td></tr><tr><td>$W\leq0.03$</td><td>---</td><td>Ignore</td><td rowspan="3">Ignore</td></tr><tr><td>$0.03<W\leq0.08$</td><td>$L\leq5.0$</td><td>3</td></tr><tr><td>$W>0.08$</td><td>---</td><td>0</td></tr></table>				Width (mm)	Length (mm)	Acceptable Number		Zone A,B	Zone C	$W\leq0.03$	---	Ignore	Ignore	$0.03<W\leq0.08$	$L\leq5.0$	3	$W>0.08$	---	0	Minor
Width (mm)	Length (mm)	Acceptable Number																				
		Zone A,B	Zone C																			
$W\leq0.03$	---	Ignore	Ignore																			
$0.03<W\leq0.08$	$L\leq5.0$	3																				
$W>0.08$	---	0																				
3	Polarizer Bubble	<table><tr><td rowspan="2">Average Diameter (mm)</td><td colspan="2">Acceptable Number</td></tr><tr><td>Zone A,B</td><td>Zone C</td></tr><tr><td>$\Phi>0.5$</td><td>0</td><td rowspan="3">Ignore</td></tr><tr><td>$0.2<\Phi\leq0.5$</td><td>3</td></tr><tr><td>$\Phi\leq0.2$</td><td>Ignore</td></tr></table>				Average Diameter (mm)	Acceptable Number		Zone A,B	Zone C	$\Phi>0.5$	0	Ignore	$0.2<\Phi\leq0.5$	3	$\Phi\leq0.2$	Ignore	Minor				
Average Diameter (mm)	Acceptable Number																					
	Zone A,B	Zone C																				
$\Phi>0.5$	0	Ignore																				
$0.2<\Phi\leq0.5$	3																					
$\Phi\leq0.2$	Ignore																					
4	Any Dirt & Scratch on Polarizer’s Protective Film	Ignore for not affect the polarizer.				Minor																
5	Any Dirt on Cap Glass	<table><tr><td>Average Diameter (mm)</td><td colspan="2">Acceptable Number</td></tr><tr><td>$\Phi\leq0.5$</td><td colspan="2">Ignore</td></tr><tr><td>$0.5<\Phi\leq1.0$</td><td colspan="2">3</td></tr><tr><td>$\Phi>1.0$</td><td colspan="2">0</td></tr></table>				Average Diameter (mm)	Acceptable Number		$\Phi\leq0.5$	Ignore		$0.5<\Phi\leq1.0$	3		$\Phi>1.0$	0		Minor				
Average Diameter (mm)	Acceptable Number																					
$\Phi\leq0.5$	Ignore																					
$0.5<\Phi\leq1.0$	3																					
$\Phi>1.0$	0																					

6	Glass Crack	 Propagation crack is not acceptable.	Major
7	Corner Chip	 t = Glass thickness Accept $a \leq 2.0\text{mm}$ or $b \leq 2.0\text{mm}$, $c \leq t$	Minor
8	Corner Chip on Cap Glass	 t = Glass thickness Accept $a \leq 1.5\text{mm}$ or $b \leq 1.5\text{mm}$, $c \leq t$	Minor
9	Chip on Contact Pad	 t = Glass thickness Accept $a \leq 3.0\text{mm}$ or $b \leq 0.8\text{mm}$, $c \leq t$ (on the contact pin) $a \leq 3.0\text{mm}$ or $b \leq 1.5\text{mm}$, $c \leq t$ (outside of the contact pin)	Minor
10	Chip on Face of Display	 t = Glass thickness Accept $a \leq 1.5\text{mm}$ or $b \leq 1.5\text{mm}$, $c \leq t$	Minor
11	Chip on Cap Glass	 t = Glass thickness Accept $a \leq 3.0\text{mm}$ or $b \leq 3.0\text{mm}$, $c \leq t/2$ $a \leq 1.5\text{mm}$ or $b \leq 1.5\text{mm}$, $t/2 \leq c \leq t$	Minor
12	Stain on Surface	Stain removable by soft cloth or air blow is acceptable.	Minor
13	TCP/FPC Damage	(1) Crack, deep scratch, deep hole and deep pressure mark on the TCP/FPC are not acceptable. (2) Terminal lead twisted or broken is not allowable. (3) Copper exposed is not allowed by naked eye inspection.	Minor
14	Dimension Unconformity	Checking by mechanical drawing.	Major

II . Displaying Defects

NO.	ITEM	CRITERIA			CLASSIFICATION
1	Black/White spot Dirty spot Foreign matter	Average Diameter (mm)	Pieces Permitted		Minor
			Zone A,B	Zone C	
		Φ≤0.10	Ignore	Ignore	
		0.10<Φ≤0.20	3		
		Φ>0.20	0		
2	No Display	Not allowable.			Major
3	Irregular Display	Not allowable.			Major
4	Missing Line (row or column)	Not allowable.			Major
5	Short	Not allowable.			Major
6	Flicker	Not allowable.			Major
7	Abnormal Color	Refer to the SPEC.			Major
8	Luminance NG	Refer to the SPEC.			Major
9	Over Current	Refer to the SPEC.			Major

13 Precautions for operation and Storage

13.1 Precautions for Operation

- (1) Since OLED panel is made of glass, do not apply any mechanical shock or impact or excessive force to it when installing the OLED module. Any strong mechanical impact due to falling dropping etc. may cause damage (breakage or cracking).
- (2) The polarizer on the OLED surface is made of soft material and is easily scratched. Please take most care when handing. When the surface of the polarizer of OLED Module is contaminated, please wipe it off gently by using moisten soft cloth with isopropyl alcohol, do not use water, ketone or aromatics. If there is saliva or water on the OLED surface, please wipe it off immediately.
- (3) When handling OLED module, please be sure that the body and the tools are properly grounded. And do not touch I/O pins with bare hands or contaminate I/O pins, it will cause disconnection or defective insulation of terminals.
- (4) Do not attempt to disassemble or process the OLED module.
- (5) OLED module should be used under recommended operating conditions shown in the specification. Since the higher voltage leads to the shorter lifetime, be sure to use the specified operating voltage.
- (6) Foggy dew, moisture condensation or water droplets deposited on surface and contact terminals will cause polarizer stain or damage, the deteriorated display quality and electrochemical reaction then leads to shorter life time and permanent damage to the module probably. Please pay attention to the environmental temperature and humidity.
- (7) An afterimage is created by the difference in brightness between unused dot and the fixed dot, according to the decrease of brightness of the emitting time. Therefore, to avoid having an afterimage, the full set should be thoroughly used instead of using a fixed dot. When the fixed dot emits, an afterimage can be created.
- (8) Flicker could be come out at full on display. And it disappears when frame frequency increase, but brightness decreases too.

13.2 Soldering

- (1) Soldering should be performed only on the I/O terminals.
- (2) Use soldering irons with proper grounding and no leakage.
- (3) Iron: The temperature setting of electric iron is 350°C, but we suggest that during soldering, the temperature of iron tip should be no higher than 330°C and soldering be finished within 3~4 seconds.

13.3 Precautions for Storage

- (1) Please store OLED module in a dark place. Avoid exposure to sunlight, the light of fluorescent lamp or any ultraviolet ray.
- (2) Keep the environment temperature between 10°C and 35°C and the relative humidity less than 60%. Avoid high temperature and high humidity.
- (3) Keep the OLED modules stored in the container when shipped from supplier before using them is recommended.
- (4) Do not leave any article on the OLED module surface for an extended period of time.

13.4 Warranty period

Visionox warrants for a period of 12 months from the shipping date when stored or used under normal condition.



**Стандарт
Электрон
Связь**

Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

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С нами вы становитесь еще успешнее!

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