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FDMC86320

June 2014

N-Channel Power Trench[®] MOSFET 80 V, 22 A, 11.7 m Ω

Features

- Max $r_{DS(on)}$ = 11.7 m Ω at V_{GS} = 10 V, I_D = 10.7 A
- Max $r_{DS(on)}$ = 16 m Ω at V_{GS} = 8 V, I_D = 8.5 A
- MSL1 robust package design
- 100% UIL Tested
- RoHS Compliant

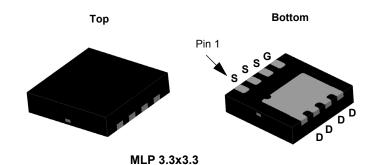


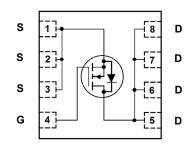
General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency and to minimize switch node ringing of DC/DC converters using either synchronous or conventional switching PWM controllers.It has been optimized for low gate charge, low $r_{\rm DS(on)},$ fast switching speed and body diode reverse recovery performance.

Applications

- Primary DC-DC Switch
- Motor Bridge Switch
- Synchronous Rectifier





MOSFET Maximum Ratings T_A = 25 °C unless otherwise noted

Symbol	Paramete	Parameter				
V_{DS}	Drain to Source Voltage			80	V	
V_{GS}	Gate to Source Voltage			±20	V	
	Drain Current -Continuous	T _C = 25 °C		22		
I_{D}	-Continuous	T _A = 25 °C	(Note 1a)	10.7	Α	
	-Pulsed			50		
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	60	mJ	
D	Power Dissipation T _C = 25 °C		40	W		
P_{D}	Power Dissipation	T _A = 25 °C	(Note 1a)	2.3	VV	
T _J , T _{STG}	Operating and Storage Junction Temperatu	re Range		-55 to +150	°C	

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case		3.1	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	53	C/VV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMC86320	FDMC86320	Power 33	13 "	12 mm	3000 units

Electrical Characteristics $T_J = 25 \, ^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	acteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	80			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, referenced to 25 °C		56		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 64 V, V _{GS} = 0 V			1	μΑ
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	2.4	3.5	4.5	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I_D = 250 μA, referenced to 25 °C		-11		mV/°C
		$V_{GS} = 10 \text{ V}, I_D = 10.7 \text{ A}$		9.7	11.7	
r _{DS(on)}	r _{DS(on)} Static Drain to Source On Resistance	$V_{GS} = 8 \text{ V}, I_D = 8.5 \text{ A}$		11.4	16	mΩ
	V_{GS} = 10 V, I_D = 10.7 A, T_J = 125 °C		15	18		
9 _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 10.7 A		20		S

Dynamic Characteristics

C _{iss}	Input Capacitance	., ., ., ., ., ., ., ., ., ., ., ., ., .		1985	2640	pF
C _{oss}	Output Capacitance	V _{DS} = 40 V, V _{GS} = 0 V, f = 1 MHz		353	469	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1 1/11/12		12	30	pF
R_g	Gate Resistance			0.5		Ω

Switching Characteristics

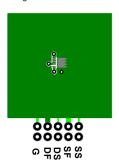
t _{d(on)}	Turn-On Delay Time		15	28	ns
t _r	Rise Time	V _{DD} = 40 V, I _D = 10.7 A,	8	16	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10 V, R_{GEN} = 6 Ω	20	35	ns
t _f	Fall Time		5	10	ns
$Q_{g(TOT)}$	Total Gate Charge	V _{GS} = 0 V to 10 V	29	41	nC
$Q_{g(TOT)}$		$V_{GS} = 0 \text{ V to 8 V}$ $V_{DD} = 40 \text{ V},$ $I_{D} = 10.7 \text{ A}$	24	34	nC
Q _{gs}	Total Gate Charge	1 _D - 10.7 A	10		nC
Q_{gd}	Gate to Drain "Miller" Charge		6.9		nC

Drain-Source Diode Characteristics

V	Vob Source to Drain Diode Forward Voltage H	$V_{GS} = 0 \text{ V}, I_S = 10.7 \text{ A}$ (Note 2)		0.84	1.3	٧
v _{SD}		$V_{GS} = 0 \text{ V}, I_S = 2 \text{ A}$ (Note 2)		0.75	1.2	
t _{rr}	Reverse Recovery Time			38	61	ns
Q _{rr}	Reverse Recovery Charge			27	43	nC

NOTES

1. R_{0JA} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



a. 53 °C/W when mounted on a 1 in² pad of 2 oz copper



b. 125 °C/W when mounted on a minimum pad of 2 oz copper

^{2.} Pulse Test: Pulse Width < 300 $\mu s,$ Duty cycle < 2.0%.

^{3.} Starting T $_J$ = 25 °C; N-ch: L = 0.3 mH, I $_{AS}$ = 20 A, V $_{DD}$ = 72 V, V $_{GS}$ = 10 V.

Typical Characteristics T_J = 25 °C unless otherwise noted

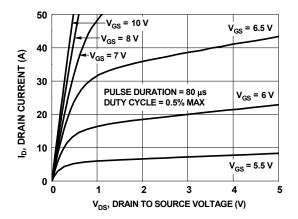


Figure 1. On Region Characteristics

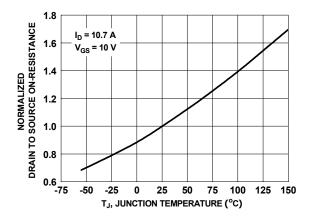


Figure 3. Normalized On Resistance vs. Junction Temperature

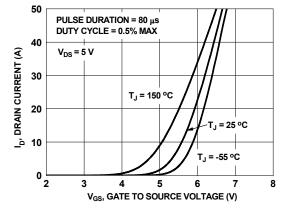


Figure 5. Transfer Characteristics

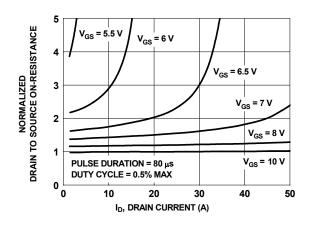


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

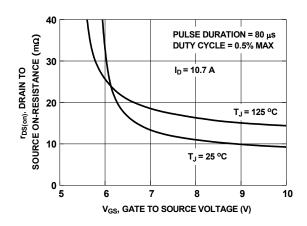


Figure 4. On-Resistance vs. Gate to Source Voltage

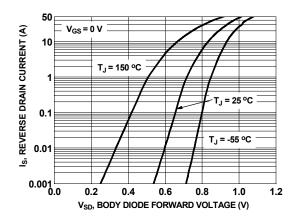


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

Typical Characteristics $T_J = 25$ °C unless otherwise noted

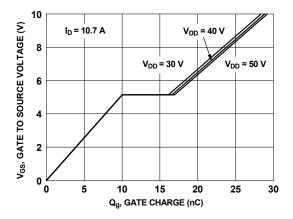


Figure 7. Gate Charge Characteristics

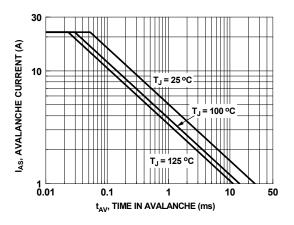


Figure 9. Unclamped Inductive Switching Capability

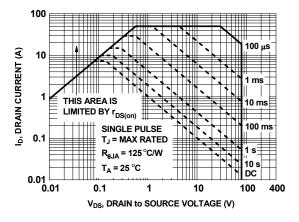


Figure 11. Forward Bias Safe Operating Area

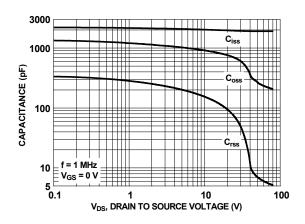


Figure 8. Capacitance vs. Drain to Source Voltage

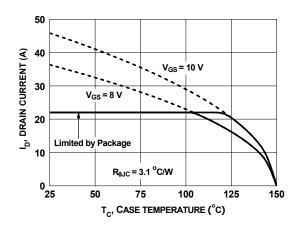


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

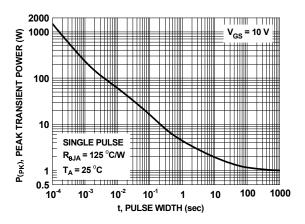


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics T_J = 25 °C unless otherwise noted

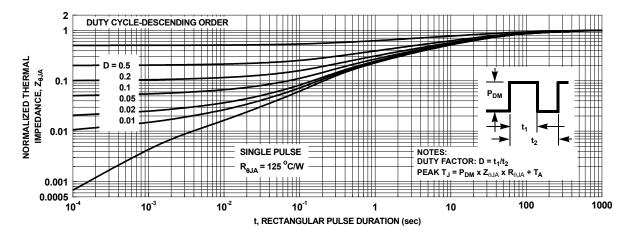
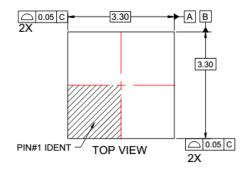
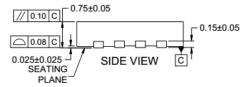
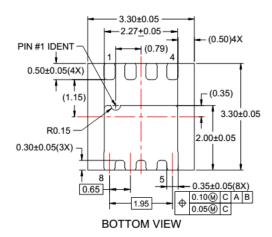


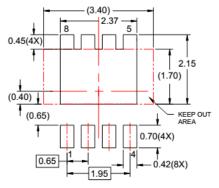
Figure 13. Junction-to-Ambient Transient Thermal Response Curve

Dimensional Outline and Pad Layout









RECOMMENDED LAND PATTERN

NOTES:

- A. DOES NOT CONFORM TO JEDEC REGISTRATION MO-229
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- D. LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.
- E. DRAWING FILENAME: MKT-MLP08Srev3.



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