

Evaluation Kit Description

Rev. 1.00 / November 2012

ZSPM4121

Under-Voltage Load Switch Evaluation Kit



Power and Precision



ZSPM4121

Evaluation Kit Description



The Analog Mixed Signal Company



Restrictions:

Zentrum Mikroelektronik Dresden AG's ZSPM4121 Evaluation Kit is designed for evaluation of the ZSPM4121, laboratory setup, and module development only. This kit must not be used for module production and production test setups.

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For more information, contact ZMDI via analog@zmdi.com.



1 Kit Contents

- ZSPM4121 Evaluation Board
- Kit Documentation

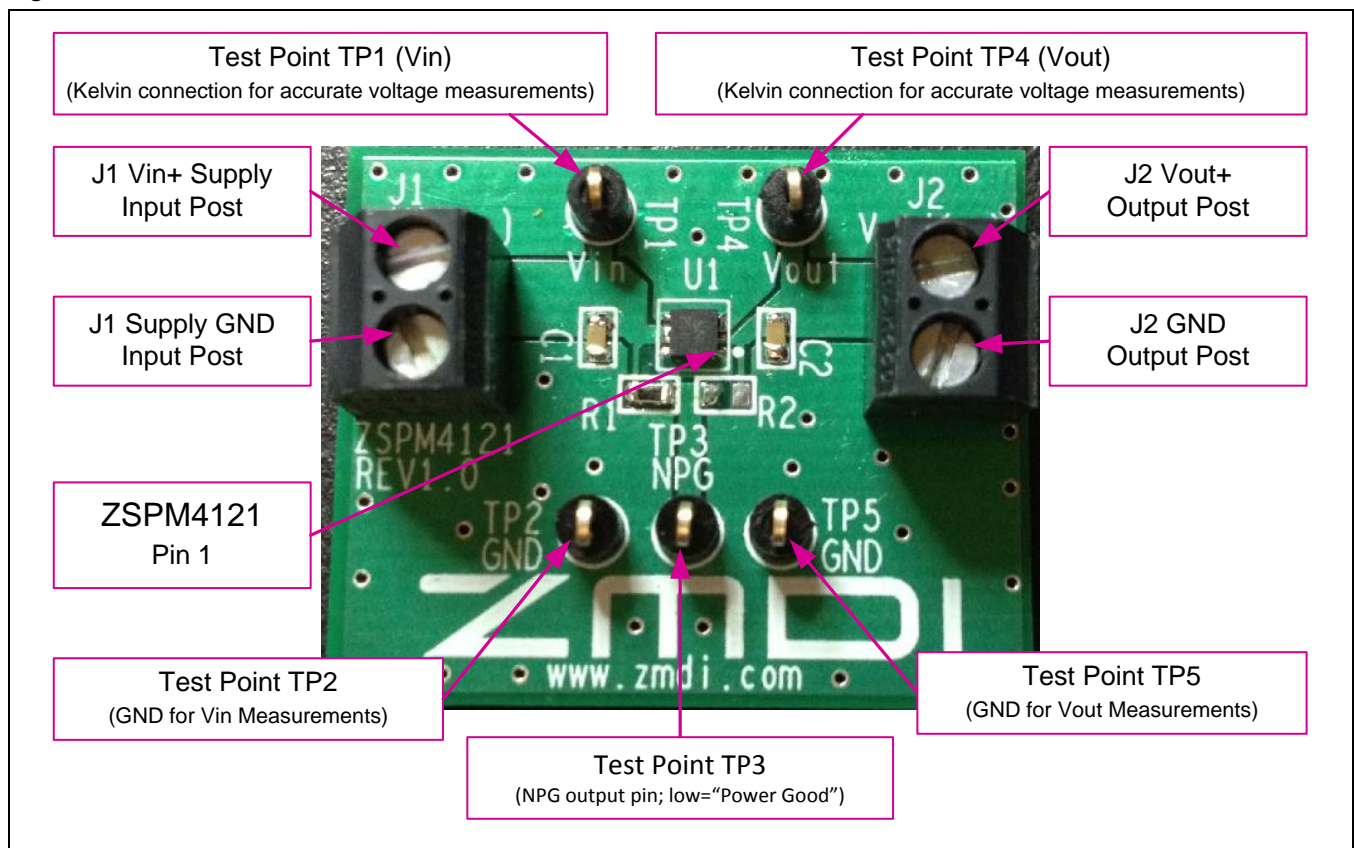
2 Introduction

The ZSPM4121 Evaluation Board is a compact system intended to facilitate measuring the performance and operating characteristics of the ZSPM4121 Under-Voltage Load Switch. The board contains a number of test points to allow evaluating the functions of the ZSPM4121 (see Figure 2.1 and section 4). It has a solid ground plane.

Note: The default part number for the ZSPM4121 Under-Voltage Load Switch (U1 in the schematic in Figure 6.1) is ZSPM4121AI1Wxx, where xx refers to the factory-configured voltage threshold which is determined by the version of the ZSPM4121 Evaluation Board ordered. Contact ZMDI for available board versions.

Note: The default configuration of the Evaluation Board is with the NPG pin pulled up to V_{in} via R1 and therefore R2 is unpopulated (NL designation in the schematic in Figure 6.1). If the application requires that NPG be pulled up to V_{out} , remove R1 and install R2.

Figure 2.1 ZSPM4121 Evaluation Board Connections





3 Connectors

The board contains the following connectors to external signals and supplies (see Figure 2.1):

- J1:** Screw terminal connection for Vin supply (Vin+ and GND)
- J2:** Screw terminal connection to Vout output (Vout+ and GND)

4 Probe and Test Points

The board contains the following test/probe points (see Figure 2.1):

- TP1/Vin:** Kelvin measurement test point for Vin main input
- TP4/Vout:** Kelvin measurement test point for Vout output
- TP3/NPG:** Measurement point for the ZSPM4121's open-drain N-channel NPG output pin 8 (low indicates "Power Good")
- TP2/GND:** Test point for ground; recommended ground for accurate measurements for Vin
- TP5/GND:** Test point for ground; recommended ground for accurate measurements for Vout

5 Setup

Connect the input voltage supply for the ZSPM4121 at the J1 screw terminal with the leads connected shown in Figure 2.1.

The output of the Evaluation Board can be measured at the J2 screw terminal.

Recommendation: Use J1 and J2 only for power connections. Use test points TP1 (Vin) and TP4 (Vout) as Kelvin connections for accurate voltage measurements (see Figure 2.1).



6 Evaluation Board Schematic

Figure 6.1 ZSPM4121 Evaluation Board Circuit

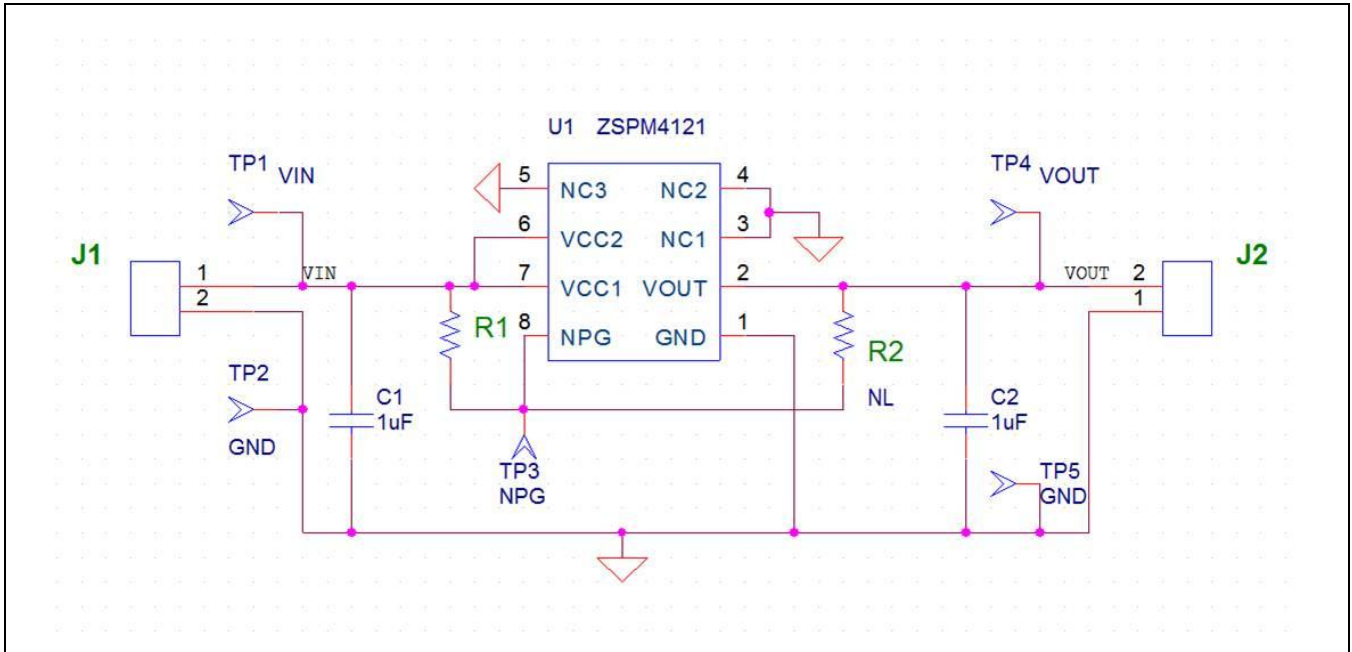


Table 6.1 ZSPM4121 Evaluation Board Bill of Materials (BOM)

QTY	Reference	Description	Manufacturer	Manufacturer P/N	Digikey P/N
1	U1	ZSPM4121	ZMDI	ZSPM4121A11 Wxx*	N/A
2	C1	CAP CER 1UF 25V 10% X5R 0603	TDK	C1608X5R1E105K	445-5146-2-ND
	C2	CAP CER 1UF 25V 10% X5R 0603	TDK	C1608X5R1E105K	445-5146-2-ND
1	R1	RES 1K OHM 1/10W 5% 0603 SMD	Stackpole	RMCF0603JT1K00	RMCF0603JT1K00CT-ND
0	R2	Do not populate			
5	TP1	Test Point	Keystone	5001K-ND	5001
	TP2	Test Point	Keystone	5001K-ND	5001
	TP3	Test Point	Keystone	5001K-ND	5001
	TP4	Test Point	Keystone	5001K-ND	5001
	TP5	Test Point	Keystone	5001K-ND	5001
2	J1, J2	TERMINAL BLOCK 3.5MM 2POS PCB	On Shore Technology Inc	ED555/2DS	ED1514-ND

* xx refers to the factory-configured voltage threshold, which is determined by the version of the ZSPM4121 Evaluation Board ordered. Contact ZMDI for available board versions.

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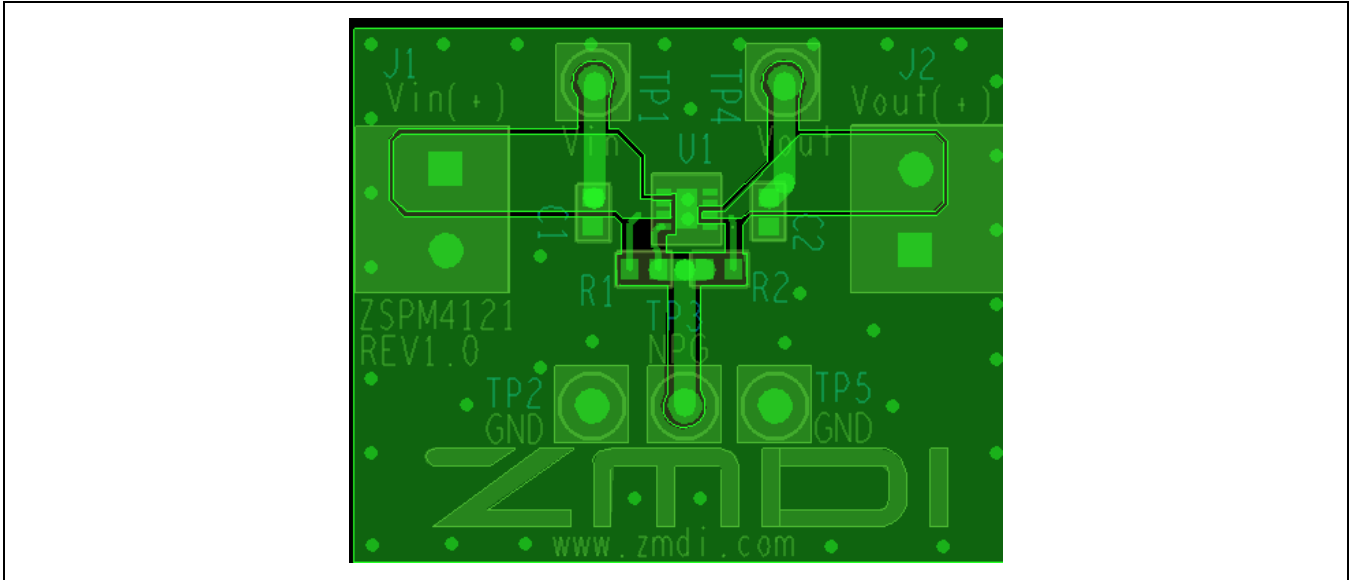


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7 Evaluation Board Layout

Figure 7.1 ZSPM4121 Evaluation Board Layout – Top View



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8 Related Documents

Note: X_xy refers to the latest version of the document.

File Name	File Name
ZSPM4121 Data Sheet	ZSPM4121_Data_Sheet_rev_X_xy.pdf
ZSPM4121 Feature Sheet	ZSPM4121_Feature_Sheet_rev_X_xy.pdf

Visit ZMDI's website www.zmdi.com or contact your nearest sales office for the latest version of these documents.

9 Document Revision History

Revision	Date	Description
1.00	November 14, 2012	First release.

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