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#### FAIRCHILD August 1998 SEMICONDUCTOR TM FDC6326L **Integrated Load Switch General Description** Features • $V_{\text{DROP}}$ =0.20V @ $V_{\text{IN}}$ =12V, I<sub>L</sub>=1.5A.R<sub>DS(ON)</sub> = 0.125 $\Omega$ This device is particularly suited for compact power management in portable electronic equipment where 3V to $V_{DROP}=0.20V @ V_{IN}=5V, I_{I}=1A.R_{DS(ON)}=0.20 \Omega.$ 20V input and 1.8A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOT<sup>TM</sup>-6 package. ■ SuperSOT<sup>TM</sup>-6 package design using copper lead frame for superior thermal and electrical capabilities. SOT-23 SuperSOT<sup>™</sup>-6 SOIC-16 SuperSOT<sup>™</sup>-8 SO-8 SOT-223 4 3 Vout,C1 Vin,R1 EQUIVALENT CIRCUIT

0 2

See Application Circuit

ON/OFF 5

R1,C1 6

2 |Vout,C1

R2

1

IN C

## Absolute Maximum Ratings T. = 25°C unless otherwise noted

Symbol	Parameter	FDC6326L	Units
/ <sub>IN</sub>	Input Voltage Range	3 - 20	V
V <sub>ON/OFF</sub>	On/Off Voltage Range	2.5 - 8	V
I <sub>L</sub>	Load Current - Continuous (Note 1)	1.8	A
	- Pulsed (Note 1 & 3)	5	
D	Maximum Power Dissipation (Note 2)	0.7	W
⁻」, <b>T</b> ₅тб	Operating and Storage Temperature Range	-55 to 150	°C
SD	Electrostatic Discharge Rating MIL-STD-883D Human Body Model (100pf/1500Ohm)	6	kV
HERMA	L CHARACTERISTICS		
۲ <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient (Note 2)	180	°C/W
۲ <sup>өлс</sup>	Thermal Resistance, Junction-to-Case (Note 2)	60	°C/W

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SuperSOT <sup>™</sup>-6

V DROP

ON/OFF 0-

⊸ оит

Electrical Characteristics (T <sub>A</sub> = 25°C unless otherwise noted)								
Symbol	Parameter	Conditions	Min	Тур	Max	Units		
OFF CHA	RACTERISTICS	· · · ·		•				
I <sub>FL</sub>	Forward Leakage Current	$V_{IN} = 20 \text{ V}, V_{ON/OFF} = 0 \text{ V}$			1	μA		
ON CHAR	ACTERISTICS (Note 3)							
V <sub>DROP</sub>	Conduction Voltage Drop	$V_{IN} = 12 \text{ V}, V_{ONOFF} = 3.3 \text{ V}, I_{L} = 1.5 \text{ A}$		0.15	0.2	V		
		$V_{IN} = 5 V, V_{ONOFF} = 3.3 V, I_{L} = 1 A$		0.14	0.2			
R <sub>DS(ON)</sub>	Q2 - Static On-Resistance	$V_{GS} = -12 \text{ V}, \ I_{D} = -1.9 \text{ A}$		0.095	0.125	Ω		
		$V_{GS} = -5 \text{ V}, \text{ I}_{D} = -1.5 \text{ A}$		0.14	0.2			
I <u>.</u>	Load Current	$V_{DROP} = 0.125 \text{ V}, V_{IN} = 12 \text{ V}, V_{ONOFF} = 3.3 \text{ V}$	1			А		
		$V_{DROP} = 0.20 V, V_{IN} = 5 V, V_{ONOFF} = 3.3 V$	1					

Notes:

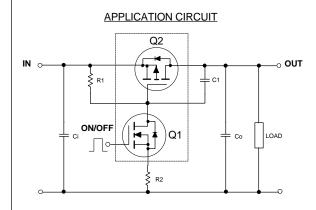
1.  $V_{IN}$ =20V,  $V_{ONOFF}$ =8V,  $T_A$ =25°C

2. R<sub>e,it</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface

of the drain pins.  $R_{_{AUC}}$  is guaranteed by design while  $R_{_{ACA}}$  is determined by the user's board design.

3. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2.0%.

## FDC6326L Load Switch Application



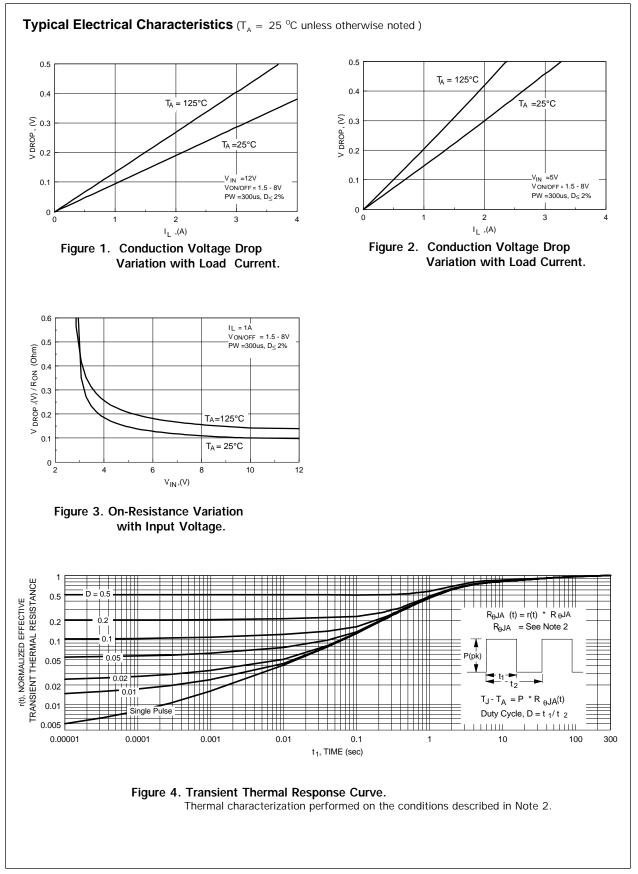
### **External Component Recommendation**

First select R2, 100 -  $1k\Omega$ , for Slew Rate control.

 $C1 \le 1000 pF$  can be added in addition to R2 for further In-rush current control.

Then select R1 such that R1/R2 ratio maintains between 10 - 100. R1 is required to turn Q2 off.

For SPICE simulation, users can download a "FDC6326L.MOD" Spice model from Fairchild Web Site at www.fairchildsemi.com



FDC6326L Rev.D1

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