



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

Monolithic Digital IC

LB1936V — 1-2 Phase Excitation Stepping Motor Driver

Overview

The LB1936V is a 2-phase bipolar drive stepping motor driver IC that supports low-voltage drive. The LB1936V's miniature package and minimal number of external components reduces the required mounting area. It also provides high-efficiency motor drive and can reduce circuit current consumption. Since it provides a current detection pin and supports PWM control input, it can be used to implement current chopper control at the system level. The LB1936V is optimal for the stepping motor drive in scanners, digital cameras, and printers.

Features

- Low saturation voltage forward/reverse motor driver ($V_O \text{ sat} = 0.25\text{V}$ at $I_O = 200\text{mA}$)
- Two H-bridge channels
- Wide usable voltage range (Allowable voltage range : 2.5V to 9.5V, absolute maximum rating : 10.5V)
- Supports PWM input (Low power consumption can be achieved in slow delay mode that uses IN1/IN2 = H/H logic.)
- Motor (coil) current detection pin
- Built-in thermal shutdown circuit
- Miniature package (SSOP16 : 225mil)

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	$V_{CC} \text{ max}$		-0.3 to +10.5	V
	$V_S \text{ max}$		-0.3 to +10.5	V
Maximum output voltage	$V_{OUT} \text{ max}$		$V_S + V_{SF}$	V
Maximum input voltage	$V_{IN} \text{ max}$		-0.3 to +8.0	V
Ground pin source current	$I_{GND} \text{ max}$	Per channel	800	mA
Allowable power dissipation	$P_d \text{ max}$	When mounted on a circuit board *	740	mW
Operating temperature	T_{opr}		-20 to +85	$^\circ\text{C}$
Storage temperature	T_{stg}		-40 to +150	$^\circ\text{C}$

* Specified circuit board : $114.3 \times 76.1 \times 1.6\text{mm}^3$, glass epoxy

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LB1936V

Allowable Operating Range at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC}		2.5 to 9.5	V
	V _S		2.5 to 9.5	
High-level input voltage	V _{IH}		2.0 to 7.5	V
Low-level input voltage	V _{IL}		-0.3 to 0.7	V

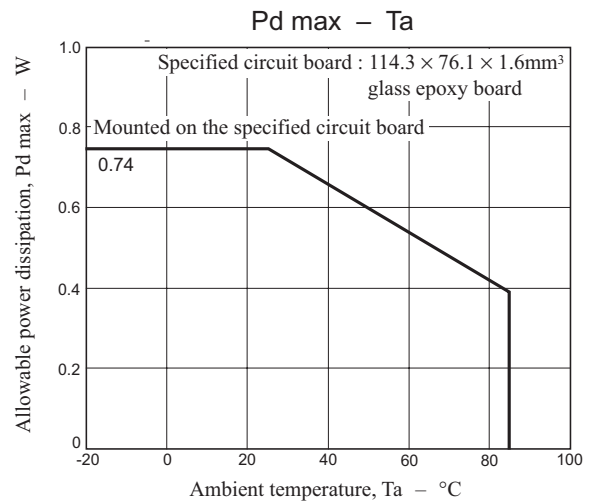
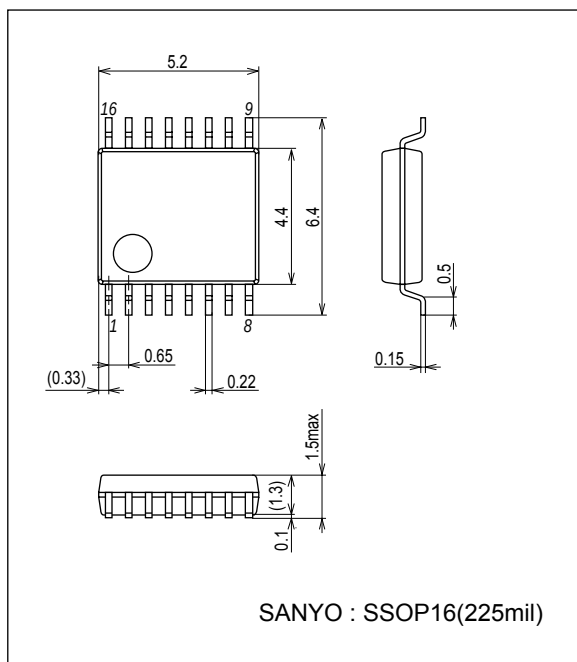
Electrical Characteristics at Ta = 25°C, V_{CC} = V_S = 5V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
V _{CC} system power supply current	I _{CC0}	IN1 = IN2 = IN3 = IN4 = 0V		0.1	1	μA
	I _{CC1}	IN1 = IN3 = 3V, IN2 = IN4 = 0V		10	16	mA
V _S system power supply current	I _{S0}	IN1 = IN2 = IN3 = IN4 = 0V		0.1	1	μA
	I _{S1}	IN1 = IN3 = 3V, IN2 = IN4 = 0V		12	19	mA
Output saturation voltage	V _{OUT1}	V _{CC} = V _S = 3V to 7.5V, V _{IN} = 3V or 0V, I _{OUT} = 200mA (High and low side)	-	0.25	0.4	V
	V _{OUT2}	V _{CC} = V _S = 4V to 7.5V, V _{IN} = 3V or 0V, I _{OUT} = 400mA (High and low side)	-	0.5	0.8	V
Input current	I _{IN}	V _{IN} = 5V		150	200	μA
Spark killer diode						
Reverse current	I _{S(leak)}				30	μA
Forward voltage	V _{SF}	I _{OUT} = 400mA			1.7	V

Package Dimensions

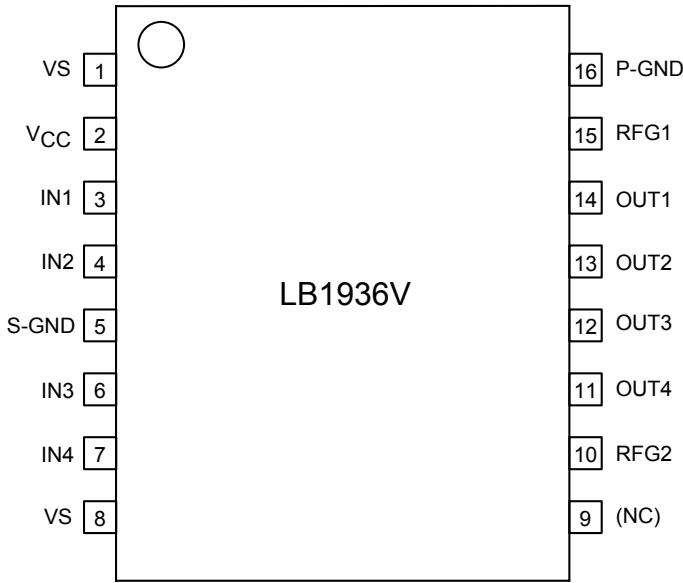
unit:mm (typ)

3178B



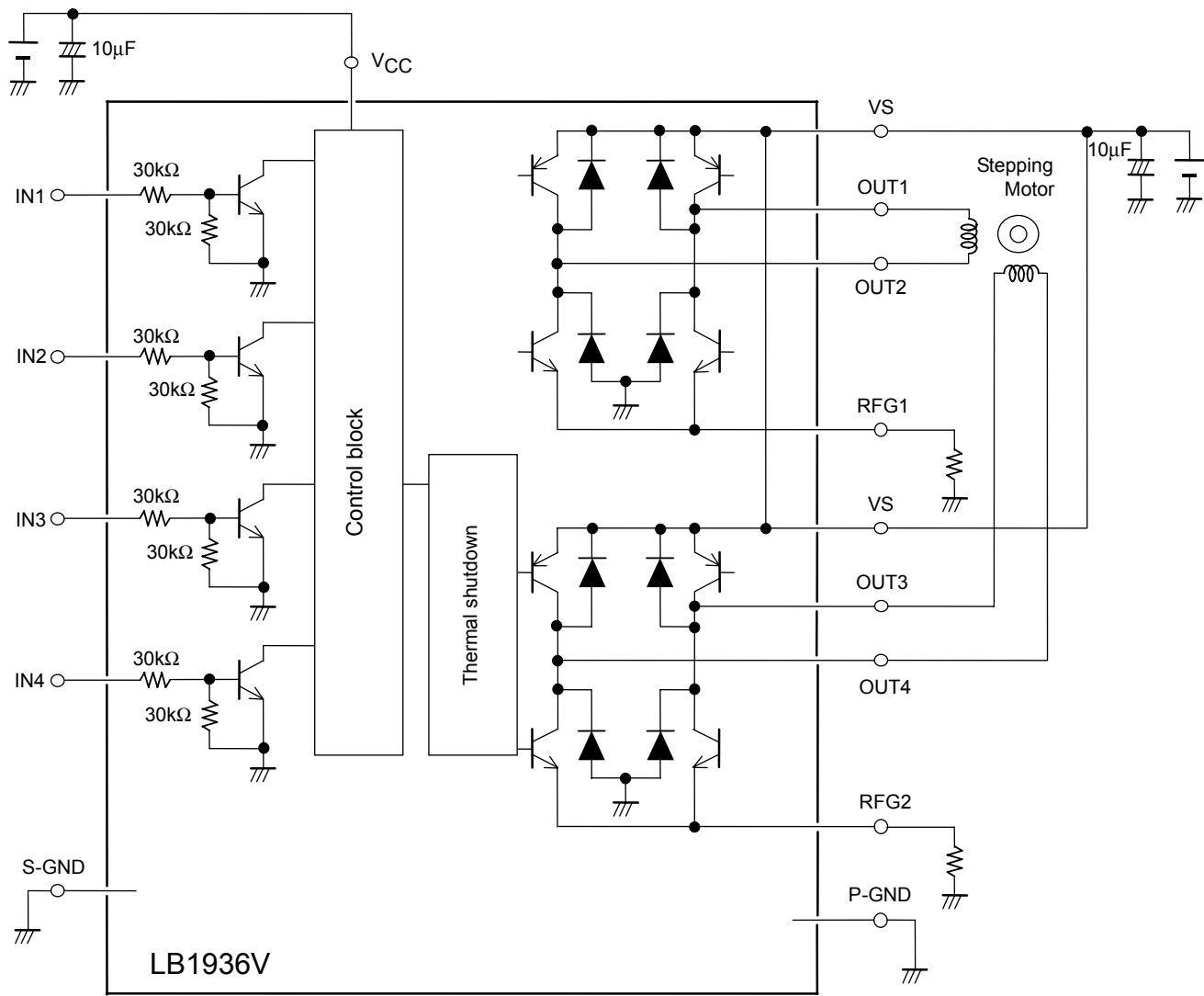
LB1936V

Pin Assignment



Top view

Block Diagram



Truth Table

IN1	IN2	IN3	IN4	OUT1	OUT2	OUT3	OUT4	Output mode
L	L	-	-	off	off	-	-	(1)
L	H			L	H			(2)
H	L			H	L			(3)
H(*)	H(*)			off	H			(4)
				H	off			(4)'
-	-	L	L	-	-	off	off	(5)
		L	H			L	H	(6)
		H	L			H	L	(7)
		H(*)	H(*)			off	H	(8)
						H	off	(8)'

L : low, H : high

*: The output logic mode when IN1/IN2 = H/H is determined by the immediately preceding IN1/IN2 mode.

The post-switching output modes will be as follows.

When switching from (2): (4)

When switching from (3): (4)'

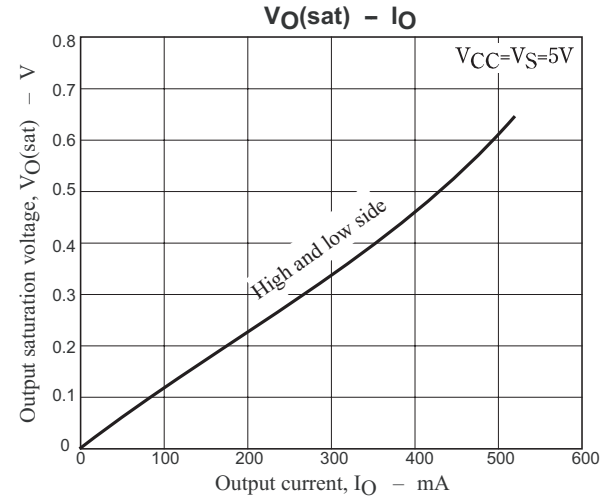
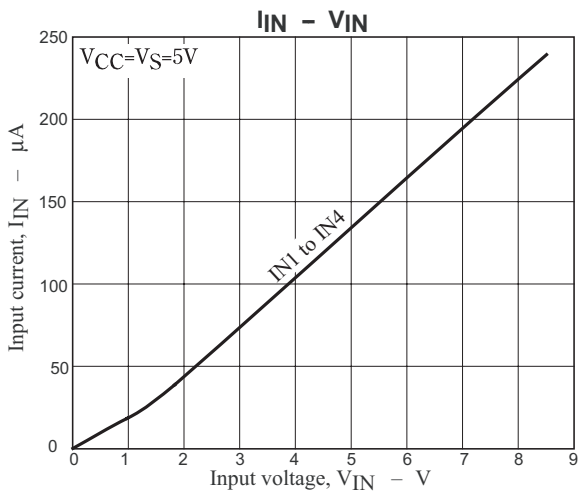
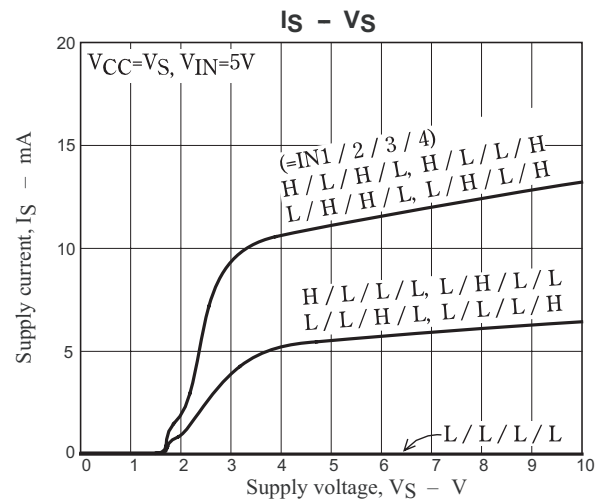
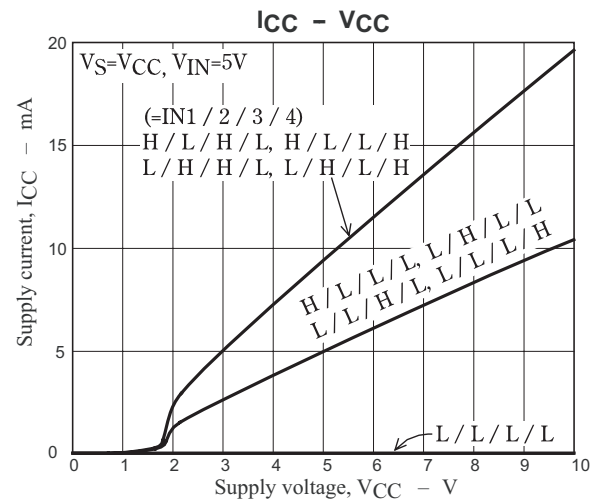
When switching from (1): Undefined (Either (4) or (4)')

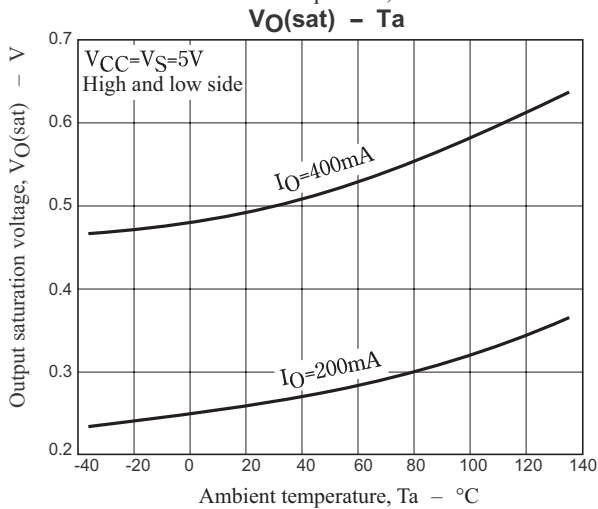
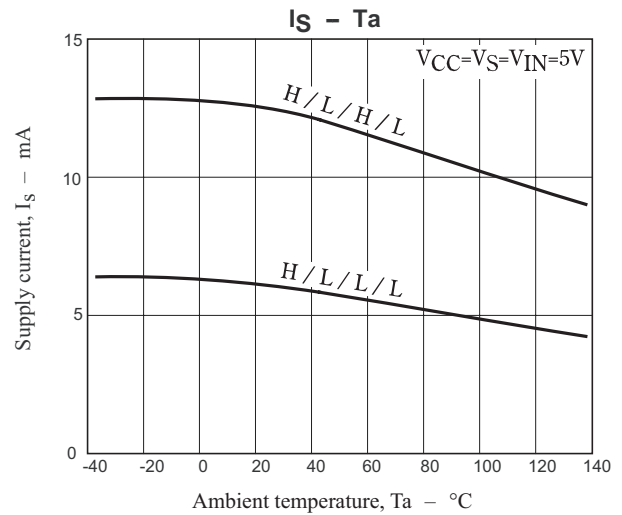
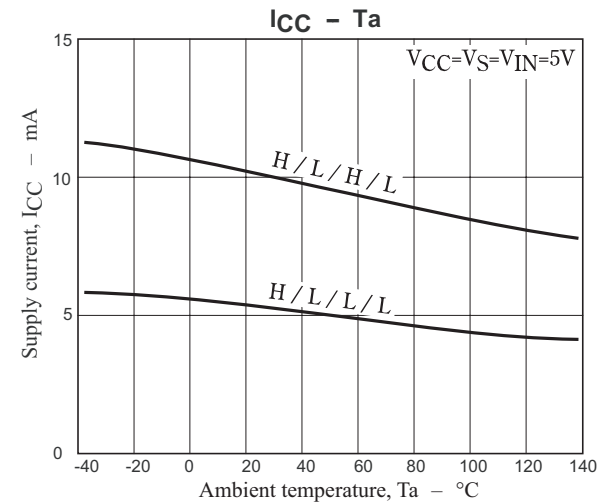
The modes when IN3/IN4 = H/H operate similarly as described below.

When switching from (6): (8)

When switching from (7): (8)'

When switching from (5): Undefined (Either (8) or (8)')





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