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November 2015

# FGD5T120SH 1200 V, 5 A FS Trench IGBT

#### **Features**

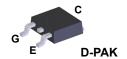
- FS Trench Technology, Positive Temperature Coefficient
- · High Speed Switching
- Low Saturation Voltage:  $V_{CE(sat)} = 2.9 V @ I_C = 5 A$
- 100% of the Parts tested for I<sub>I M</sub>(1)
- · High Input Impedance
- · RoHS Compliant

### **Applications**

- · Inrush current limitation
- · Lighting
- · Home appliances

# **General Description**

Using novel field stop IGBT technology, Fairchild's new series of field stop 3rd generation IGBTs offer the optimum performance for inrush current limitation, lighting and home appliance applications.





# Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Description		FGD5T120SH	Unit
V <sub>CES</sub>	Collector to Emitter Voltage		1200	V
$V_{GES}$	Gate to Emitter Voltage		±25	V
*GES	Transient Gate to Emitter Voltage		±30	V
la	Collector Current	@ T <sub>C</sub> = 25°C	10	A
l <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 100°C	5	A
I <sub>LM</sub> (1)	Clamped Inductive Load Current	@ T <sub>C</sub> = 25°C	12.5	Α
I <sub>CM</sub> (2)	Pulsed Collector Current		12.5	A
$P_{D}$	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	69	W
	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	28	W
T <sub>J</sub>	Operating Junction Temperature		-55 to +150	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
T <sub>L</sub>	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Notes: 1. Vcc = 600 V,V  $_{GE}$  = 15 V, I  $_{C}$  = 12.5 A, R  $_{G}$  = 50  $\Omega$  ,  $\,$  Inductive Load 2. Limited by Tjmax

### **Thermal Characteristics**

Symbol	Parameter	FGD5T120SH	Unit	
$R_{\theta JC}$ (IGBT)	Thermal Resistance, Junction to Case, Max.	1.8	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max. (3)	50	°C/W	

Notes: 3. Mounted on 1" squre PCB (FR4 or G-10 material)

# **Package Marking and Ordering Information**

Device Marking Device		Package	Reel Size	Tape Width	Qty per Tube
FGD5T120SH	GD5T120SH FGD5T120SH TO-252 A03 3		380 mm	16 mm	2500

# Electrical Characteristics of the IGBT $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 uA	1200	-	-	V
ΔBV <sub>CES</sub> / ΔT <sub>J</sub>	Temperature Coefficient of Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 uA	-	1.2	-	V/°C
I <sub>CES</sub>	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0 V$	-	-	250	μА
I <sub>GES</sub>	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0 V$	-	-	± 400	nA
On Charac	teristics					
V <sub>GE(th)</sub>	G-E Threshold Voltage	I <sub>C</sub> = 5 mA, V <sub>CE</sub> = V <sub>GE</sub>	2.5	3.5	4.5	V
OE(III)		I <sub>C</sub> = 5 A, V <sub>GE</sub> = 15 V	_	2.9	3.6	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 5 A, V <sub>GE</sub> = 15 V, T <sub>C</sub> = 150°C	-	4.5	-	V
Dynamic C	haracteristics					
C <sub>ies</sub>	Input Capacitance		-	209	-	pF
C <sub>oes</sub>	Output Capacitance	V <sub>CE</sub> = 30 V <sub>,</sub> V <sub>GE</sub> = 0 V, f = 1 MHz	-	11	-	pF
C <sub>res</sub>	Reverse Transfer Capacitance	1 - 1 1011 12	-	2	-	pF
	Characteristics			T	1	
T <sub>d(on)</sub>	Turn-On Delay Time		-	4.8	-	ns
T <sub>r</sub>	Rise Time		-	20.8	-	ns
T <sub>d(off)</sub>	Turn-Off Delay Time	$V_{CC} = 600 \text{ V}, I_{C} = 5 \text{ A},$	-	24.8	-	ns
T <sub>f</sub>	Fall Time	$R_G = 30 \Omega$ , $V_{GE} = 15 V$ , Inductive Load, $T_C = 25$ °C	-	104	-	ns
E <sub>on</sub>	Turn-On Switching Loss		-	247	-	uJ
E <sub>off</sub>	Turn-Off Switching Loss		-	94	-	uJ
E <sub>ts</sub>	Total Switching Loss		-	341	-	uJ
T <sub>d(on)</sub>	Turn-On Delay Time		-	4.8	-	ns
T <sub>r</sub>	Rise Time		-	40	-	ns
T <sub>d(off)</sub>	Turn-Off Delay Time	$V_{CC} = 600 \text{ V}, I_{C} = 5 \text{ A},$	-	25.6	-	ns
T <sub>f</sub>	Fall Time	$R_G = 30 \Omega$ , $V_{GE} = 15 V$ , Inductive Load, $T_C = 150^{\circ}$ C	-	134	-	ns
E <sub>on</sub>	Turn-On Switching Loss		-	393	-	uJ
E <sub>off</sub>	Turn-Off Switching Loss		-	114	-	uJ
E <sub>ts</sub>	Total Switching Loss		-	507	-	uJ
Qg	Total Gate Charge	$V_{CC} = 600 \text{ V}, I_{C} = 5 \text{ A},$	-	6.7	-	nC
Q <sub>ge</sub>	Gate to Emitter Charge	$V_{GF} = 15 \text{ V}$	-	1.8	-	nC
$Q_{gc}$	Gate to Emitter Charge	GL	-	2.6	-	nC

# **Typical Performance Characteristics**

Figure 1. Typical Output Characteristics

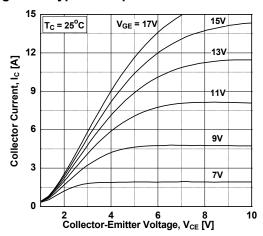


Figure 3. Typical Saturation Voltage Characteritics

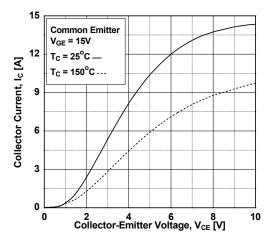


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level

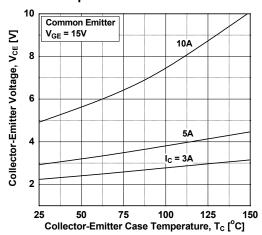


Figure 2. Typical Output Characteristics

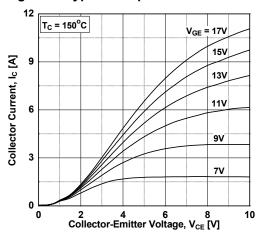


Figure 4. Transfer Characteristics

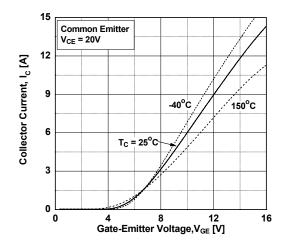
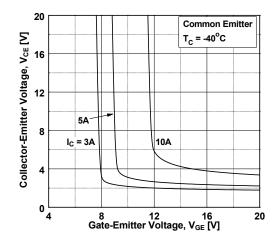


Figure 6. Saturation Voltage vs. VgE



# **Typical Performance Characteristics**

Figure 7. Saturation Voltage vs. VgE

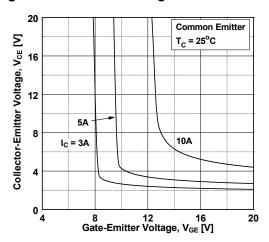


Figure 9. Capacitance Characteristics

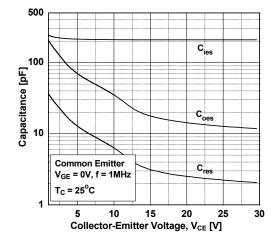


Figure 11. SOA Characteristics

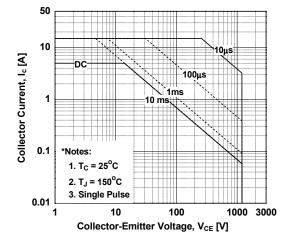


Figure 8. Saturation Voltage vs. VgE

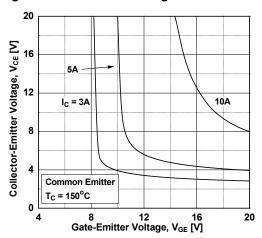


Figure 10. Gate Charge Characteristics

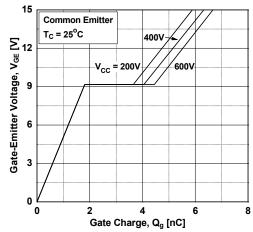
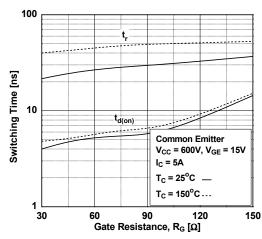


Figure 12. Turn-on Characteristics vs. Gate Resistance



# **Typical Performance Characteristics**

Figure 13. Turn-off Characteristics VS.
Gate Resistance

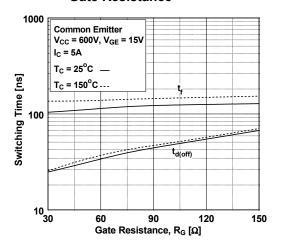


Figure 15.Turn-off Characteristics VS.
Collector Current

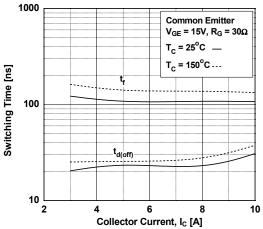


Figure 17. Switching Loss VS. Collector Current

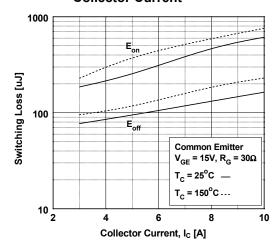


Figure 14.Turn-on Characteristics VS.
Collector Current

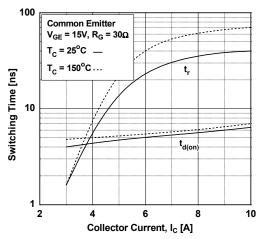


Figure 16.Switching Loss VS.
Gate Resistance

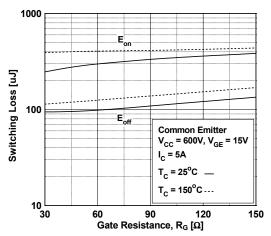


Figure 18. Current Derating

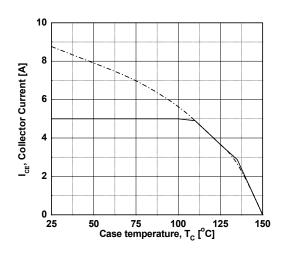


Figure 19. Load Current Vs. Frequency

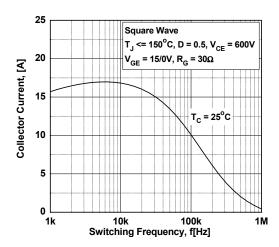
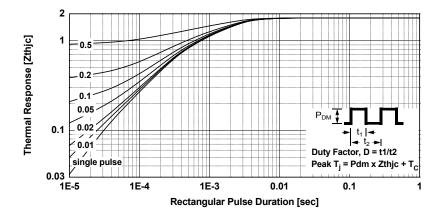


Figure 20. Transient Thermal Impedance of IGBT



# 6.00 MIN -6.50 MIN -1.02 MAX C 3.00 (0.59)1.40 MIN-2.30 ⊕ 0.25 M AM C 4.60 LAND PATTERN RECOMMENDATION SEE NOTE D MIN SEE 10.41 9.40 DETAIL A △ 0.10 B 0.51 GAGE PLANE NOTES: UNLESS OTHERWISE SPECIFIED UNLESS OTHERWISE SPECIFIED THIS PACKAGE CONFORMS TO JEDEC, TO-252, ISSUE C, VARIATION AA. ALL DIMENSIONS ARE IN MILLIMETERS. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994. HEAT SINK TOP EDGE COULD BE IN CHAMFERED CORNERS OR EDGE PROTRUSION. PRESENCE OF TRIMMED CENTER LEAD IS OPTIONAL A) B) C) (1.54)E) 0.127 MAX IS OPTIONAL. IS OPTIONAL. DIMENSIONS ARE EXCLUSSIVE OF BURSS, MOLD FLASH AND TIE BAR EXTRUSIONS. LAND PATTERN RECOMENDATION IS BASED ON IPC7351A STD T0220P1003X238—3N. DRAWING NUMBER AND REVISION: MKT—T0252A03REV8 SEATING PLANE -(2.90) DETAIL A (ROTATED -90°) SCALE: 12X

Figure 21. TO252 (D-PAK), Molded, 3-Lead, Option AA&AB

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**Mechanical dimensions** 





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