

PSMN3R3-80BS

N-channel 80 V, 3.5 m Ω standard level MOSFET in D2PAK Rev. 2 — 29 February 2012 Product data s

Product data sheet

1. **Product profile**

1.1 General description

Standard level N-channel MOSFET in D2PAK package qualified to 175C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

1.2 Features and benefits

- High efficiency due to low switching and conduction losses
- Suitable for standard level gate drive

1.3 Applications

- DC-to-DC converters
- Load switch

- Motor control
- Server power supplies

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	M	in Typ	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	80	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; see <u>Figure 1</u>	<u>[1]</u> -	-	120	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	306	W
Tj	junction temperature		-5	5 -	175	°C
Static charact	eristics					
R _{DSon}	drain-source on-state	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 ^{\circ}\text{C}; \text{ see } \frac{\text{Figure } 13}{}$	-	3	3.5	mΩ
	resistance	V_{GS} = 10 V; I_D = 25 A; T_j = 100 °C; see <u>Figure 12</u> ; see <u>Figure 13</u>		4.9	5 5.8	mΩ
Dynamic char	acteristics					
Q_{GD}	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 75 \text{ A}; V_{DS} = 40 \text{ V}; \text{ see } \frac{\text{Figure } 14}{\text{Figure } 14};$	-	28	-	nC
Q _{G(tot)}	total gate charge	see Figure 15		111	-	nC
Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 120 A; $V_{sup} \le$ 80 V; R_{GS} = 50 Ω ; unclamped	-	-	676	mJ

^[1] Continuous current is limited by package.



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain	mb	D
3	S	source		_G (EA)
mb	D	drain	\	
				mbb076 S
			∐ ∐ 1 3	
			SOT404 (D2PAK)	

3. Ordering information

Table 3. Ordering information

Type number Package			
	Name	Description	Version
PSMN3R3-80BS	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

4. Limiting values

Table 4. Limiting values

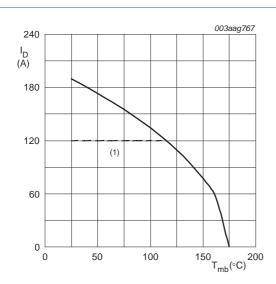
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Mi	n Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	80	V
V_{DGR}	drain-gate voltage	$T_j \ge 25$ °C; $T_j \le 175$ °C; $R_{GS} = 20 \text{ k}\Omega$	-	80	V
V_{GS}	gate-source voltage		-20	0 20	V
I_D	drain current	$V_{GS} = 10 \text{ V; } T_{mb} = 100 \text{ °C; see } Figure 1$	<u>[1]</u> _	120	Α
		$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C}; \text{ see } \frac{\text{Figure 1}}{\text{Model}}$	<u>[1]</u> _	120	Α
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 \text{ °C}$; see Figure 3	-	760	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	306	W
T _{stg}	storage temperature		-5	5 175	°C
Tj	junction temperature		-5	5 175	°C
T _{sld(M)}	peak soldering temperature		-	260	°C
Source-drain	diode				
Is	source current	T _{mb} = 25 °C	[1] -	120	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$	-	760	Α
Avalanche rug	gedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 120 A; V_{sup} ≤ 80 V; R_{GS} = 50 Ω; unclamped	-	676	mJ

^[1] Continuous current is limited by package.

PSMN3R3-80BS

All information provided in this document is subject to legal disclaimers.



 $V_{GS} \ge 10 \text{ V}$; (1) Capped at 120 A due to package

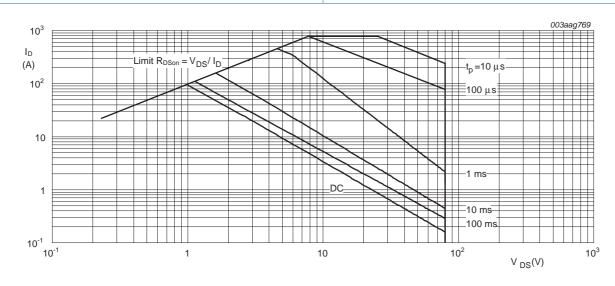
120
Pder (%)
80

40 $P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$

ig 2. Normalized total power dissipation as a

function of mounting base temperature





 $T_{mb} = 25$ °C; I_{DM} is a single pulse

Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	0.22	0.49	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	minimum footprint; mounted on a printed circuit board	-	50	-	K/W

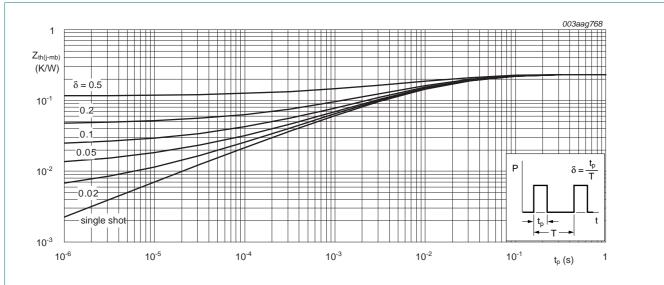


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Characteristics

Table 6 Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
V _{(BR)DSS}	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	73	-	-	V
	breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	80	-	-	V
()	gate-source threshold voltage	$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 175 \text{ °C}$; see Figure 10	1	-	-	V
		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = -55 \text{ °C}$; see Figure 10	-	-	4.6	V
		$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = 25$ °C; see Figure 10;see Figure 11	2	3	4	V
I _{DSS}	drain leakage current	$V_{DS} = 80 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	10	μΑ
		$V_{DS} = 80 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 ^{\circ}\text{C}$	-	-	500	μΑ
I_{GSS}	gate leakage current	$V_{GS} = -20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	10	100	nA
		$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	10	100	nA
R _{DSon} drain-source on-state resistance		V_{GS} = 10 V; I_D = 25 A; T_j = 175 °C; see <u>Figure 12</u> ;see <u>Figure 13</u>	-	7.1	8.4	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C};$ see Figure 13	-	3	3.5	mΩ
	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 100 \text{ °C};$ see Figure 12;see Figure 13	-	4.95	5.8	mΩ	
R_{G}	internal gate resistance (AC)	f = 1 MHz	-	0.9	-	Ω
Dynamic	characteristics					
Q _{G(tot)}	total gate charge	$I_D = 0 \text{ A}; V_{DS} = 0 \text{ V}; V_{GS} = 10 \text{ V}$	-	104	-	nC
		$I_D = 75 \text{ A}; V_{DS} = 40 \text{ V}; V_{GS} = 10 \text{ V};$	-	111	-	nC
Q_{GS}	gate-source charge	see Figure 14; see Figure 15	-	38	-	nC
$Q_{GS(th)}$	pre-threshold gate-source charge		-	24	-	nC
Q _{GS(th-pl)}	post-threshold gate-source charge		-	14	-	nC
Q_{GD}	gate-drain charge		-	28	-	nC
$V_{GS(pl)}$	gate-source plateau voltage	I _D = 75 A; V _{DS} = 40 V; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	6.1	-	V
C _{iss}	input capacitance	$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$	-	8161	-	pF
C _{oss}	output capacitance	T _j = 25 °C; see <u>Figure 16</u>	-	701	-	pF
C _{rss}	reverse transfer capacitance		-	337	-	pF
d(on)	turn-on delay time	$V_{DS} = 40 \text{ V}; R_L = 0.53 \Omega; V_{GS} = 10 \text{ V};$	-	38	-	ns
t _r	rise time	$R_{G(ext)} = 4.7 \Omega; I_D = 75 A$	-	29	-	ns
t _{d(off)}	turn-off delay time		-	94	-	ns
t _f	fall time		-	33	-	ns

Table 6. Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Source-drain diode						
V _{SD}	source-drain voltage	$I_S = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C};$ see <u>Figure 17</u>	-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_S = 25 \text{ A}$; $dI_S/dt = 100 \text{ A/}\mu\text{s}$; $V_{GS} = 0 \text{ V}$;	-	59	-	ns
Q _r	recovered charge	V _{DS} = 20 V	-	109	-	nC

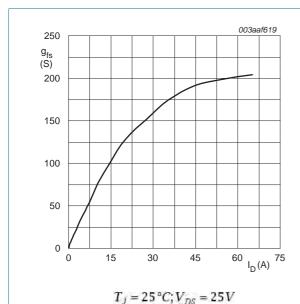


Fig 5. Forward transconductance as a function of drain current: typical values

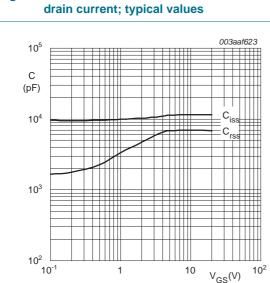
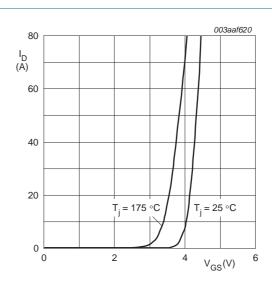


Fig 7. Input and reverse transfer capacitances as a function of gate-source voltage; typical values

 $V_{DS} = 0V; f = 1MHz$



 $V_{ extit{DS}}\!>\!I_{ extit{D}}\! imes\!R_{ extit{DSom}}$

Fig 6. Transfer characteristics: drain current as a function of gate-source voltage; typical values

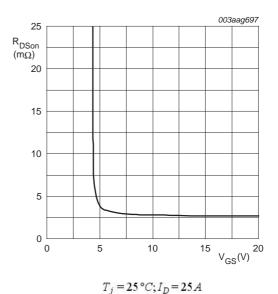
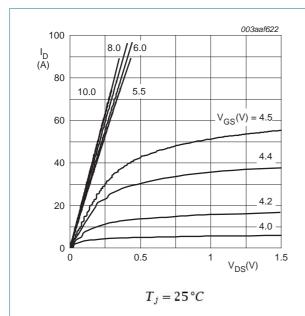


Fig 8. Drain-source on-state resistance as a function of gate-source voltage; typical values



Output characteristics: drain current as a Fig 9. function of drain-source voltage; typical values

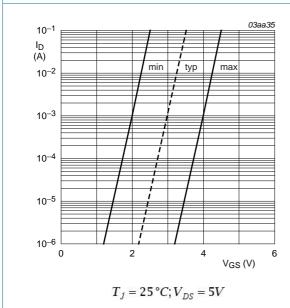
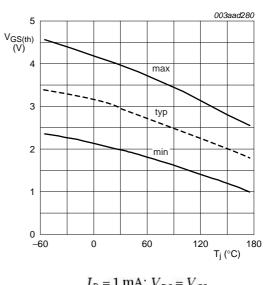


Fig 11. Sub-threshold drain current as a function of gate-source voltage



 $I_D = 1 \text{ mA}; V_{DS} = V_{GS}$

Fig 10. Gate-source threshold voltage as a function of junction temperature

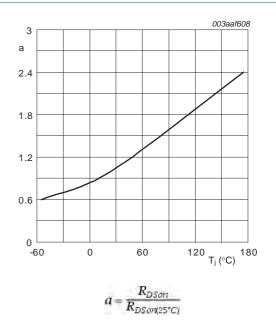


Fig 12. Normailzed drain-source on-state resistance factor as a function of junction temperature

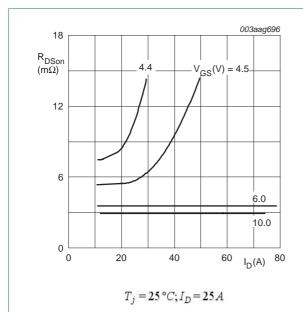


Fig 13. Drain-source on-state resistance as a function of drain current; typical values

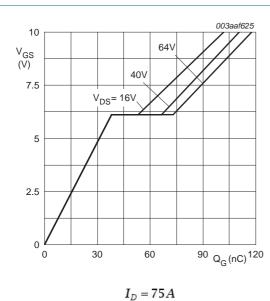


Fig 15. Gate-source voltage as a function of gate charge; typical values

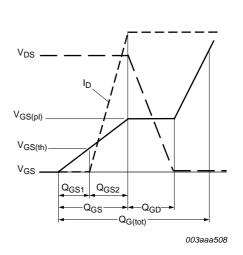
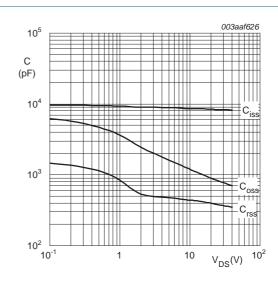
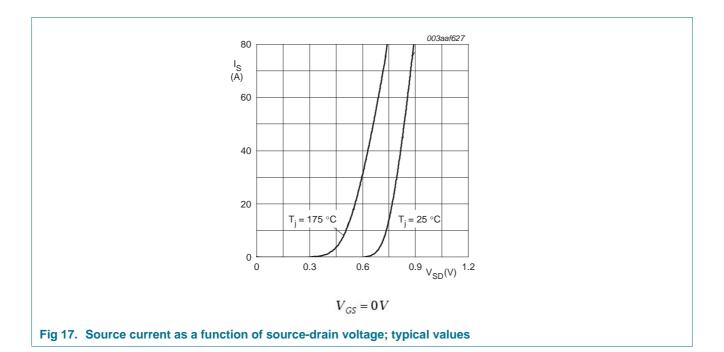


Fig 14. Gate charge waveform definitions



 $V_{GS} = 0V; f = 1MHz$

Fig 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



7. Package outline

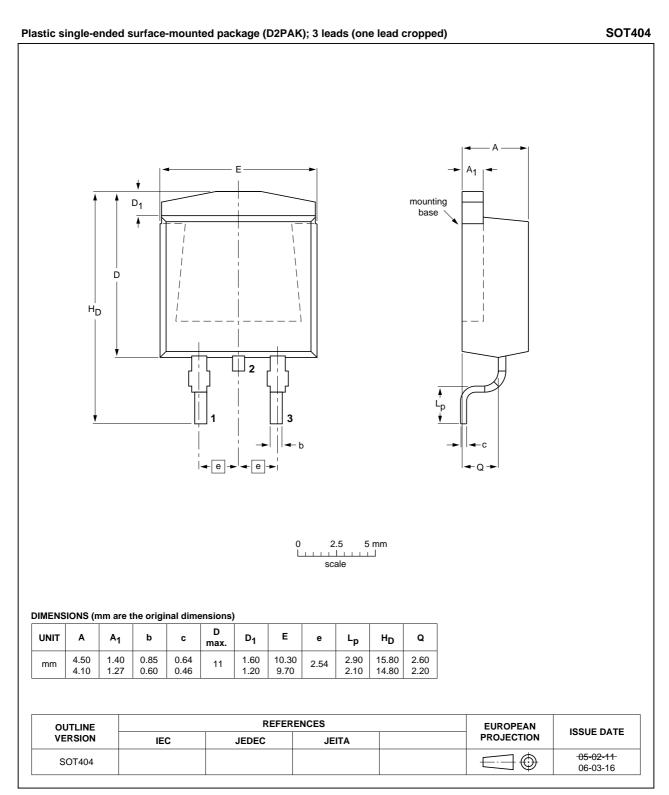


Fig 18. Package outline SOT404 (D2PAK)

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PSMN3R3-80BS v.2	20120229	Product data sheet	-	PSMN3R3-80BS v.1
Modifications:	Status changedVarious changes	from objective to product. s to content.		
PSMN3R3-80BS v.1	20110928	Objective data sheet	-	-

9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design
- [2] The term 'short data sheet' is explained in section "Definitions"
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

9.2 Definitions

Preview— The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Draft— The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet— A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification— The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet

9.3 Disclaimers

Limited warranty and liability— Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale*of Nexperia.

Right to make changes— Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use— Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data— The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications— Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values— Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

PSMN3R3-80BS

All information provided in this document is subject to legal disclaimers.

© Nexperia B.V. 2017. All rights reserved

Nexperia PSMN3R3-80BS

N-channel 80 V, 3.5 mΩ standard level MOSFET in D2PAK

Terms and conditions of commercial sale— Nexperia

products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license— Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control— This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products— Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations— A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

9.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

10. Contact information

For more information, please visit:http://www.nexperia.com

For sales office addresses, please send an email to:salesaddresses@nexperia.com

PSMN3R3-80BS

N-channel 80 V, 3.5 m Ω standard level MOSFET in D2PAK

11. Contents

1	Product profile
1.1	General description
1.2	Features and benefits
1.3	Applications
1.4	Quick reference data1
2	Pinning information
3	Ordering information
4	Limiting values
5	Thermal characteristics4
6	Characteristics
7	Package outline
8	Revision history11
9	Legal information12
9.1	Data sheet status
9.2	Definitions12
9.3	Disclaimers
9.4	Trademarks13
10	Contact information



Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

Благодаря сотрудничеству с мировыми поставщиками мы осуществляем комплексные и плановые поставки широчайшего спектра электронных компонентов.

Собственная эффективная логистика и склад в обеспечивает надежную поставку продукции в точно указанные сроки по всей России.

Мы осуществляем техническую поддержку нашим клиентам и предпродажную проверку качества продукции. На все поставляемые продукты мы предоставляем гарантию.

Осуществляем поставки продукции под контролем ВП МО РФ на предприятия военно-промышленного комплекса России, а также работаем в рамках 275 ФЗ с открытием отдельных счетов в уполномоченном банке. Система менеджмента качества компании соответствует требованиям ГОСТ ISO 9001.

Минимальные сроки поставки, гибкие цены, неограниченный ассортимент и индивидуальный подход к клиентам являются основой для выстраивания долгосрочного и эффективного сотрудничества с предприятиями радиоэлектронной промышленности, предприятиями ВПК и научноисследовательскими институтами России.

С нами вы становитесь еще успешнее!

Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург,

Промышленная ул, дом № 19, литера Н,

помещение 100-Н Офис 331