

# **PSMN015-60BS**

# N-channel 60 V 14.8 m $\Omega$ standard level MOSFET in D2PAK Rev. 2 — 1 March 2012 Product data s

Product data sheet

#### **Product profile** 1.

### 1.1 General description

Standard level N-channel MOSFET in D2PAK package qualified to 175 °C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

### 1.2 Features and benefits

- High efficiency due to low switching and conduction losses
- Suitable for standard level gate drive sources

### 1.3 Applications

- DC-to-DC converters
- Load switching

- Motor control
- Server power supplies

### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C	-	-	60	V
I <sub>D</sub>	drain current	$T_{mb} = 25  ^{\circ}C;  V_{GS} = 10  V;  see  \underline{Figure  1}$	-	-	50	А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; see <u>Figure 2</u>	-	-	86	W
Tj	junction temperature		-55	-	175	°C
Static chara	cteristics					
DOON	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A}; T_j = 100 °C;$ see Figure 12	-	-	23.7	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A}; T_j = 25 \text{ °C};$ see Figure 13	-	12.6	14.8	mΩ
Dynamic ch	aracteristics					
$Q_{GD}$	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; V_{DS} = 30 \text{ V};$	-	4.7	-	nC
Q <sub>G(tot)</sub>	total gate charge	see <u>Figure 14</u> ; see <u>Figure 15</u>	-	20.9	-	nC
Avalanche ruggedness						
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; $I_D$ = 50 A; $V_{sup}$ ≤ 60 V; $R_{GS}$ = 50 $\Omega$ ; unclamped	-	-	44	mJ



# 2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain[1]	mb	D
3	S	source		
mb	D	mounting base; connected to drain		mbb076 S
			SOT404 (D2PAK)	

<sup>[1]</sup> It is not possible to make connection to pin 2

# 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PSMN015-60BS	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

# 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C	-	60	V
$V_{DGR}$	drain-gate voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}; R_{GS} = 20 \text{ k}\Omega$	-	60	V
$V_{GS}$	gate-source voltage		-20	20	V
I <sub>D</sub>	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 100 \text{ °C}; \text{ see } \frac{\text{Figure 1}}{\text{Model}}$	-	36	Α
		$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C}; \text{ see } \frac{\text{Figure 1}}{\text{Model}}$	-	50	Α
I <sub>DM</sub>	peak drain current	pulsed; $t_p \le 10 \mu s$ ; $T_{mb} = 25 \text{ °C}$ ; see Figure 3	-	201	Α
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; see <u>Figure 2</u>	-	86	W
T <sub>stg</sub>	storage temperature		-55	175	°C
T <sub>j</sub>	junction temperature		-55	175	°C
$T_{sld(M)}$	peak soldering temperature		-	260	°C
Source-dra	in diode				
I <sub>S</sub>	source current	T <sub>mb</sub> = 25 °C	-	50	Α
I <sub>SM</sub>	peak source current	pulsed; $t_p \le 10 \mu s$ ; $T_{mb} = 25  ^{\circ}C$	-	201	Α
Avalanche	ruggedness				
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; $I_D$ = 50 A; $V_{sup}$ ≤ 60 V; $R_{GS}$ = 50 $\Omega$ ; unclamped	-	44	mJ

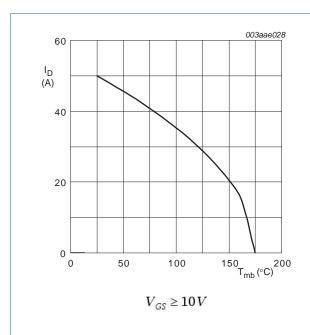


Fig 1. Continuous drain current as a function of mounting base temperature

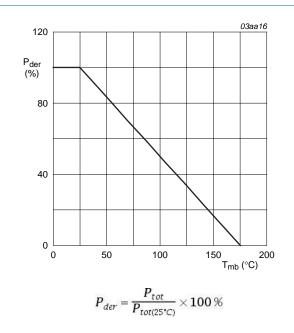
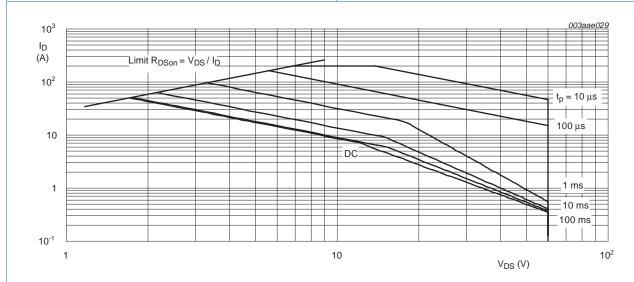


Fig 2. Normalized total power dissipation as a function of mounting base temperature



 $T_{mb} = 25$ °C;  $I_{DM \text{ is single pulse}}$ 

Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

### 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	1	1.74	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	Minimum footprint; mounted on a printed circuit board	-	60	-	K/W

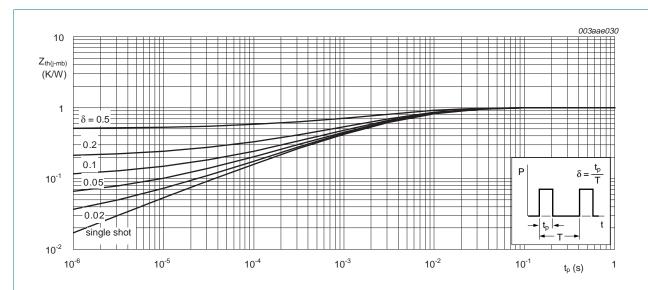


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration; typical values

# 6. Characteristics

Table 6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
$V_{(BR)DSS}$	drain-source breakdown	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	54	-	-	V
	voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	60	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1$ mA; $V_{DS} = V_{GS}$ ; $T_j = 25$ °C; see <u>Figure 10</u> ; see <u>Figure 11</u>	2	3	4	V
$V_{GSth}$	gate-source threshold voltage	$I_D$ = 1 mA; $V_{DS}$ = $V_{GS}$ ; $T_j$ = -55 °C; see <u>Figure 11</u>	-	-	4.8	V
		$I_D$ = 1 mA; $V_{DS}$ = $V_{GS}$ ; $T_j$ = 175 °C; see <u>Figure 11</u>	1	-	-	V
$I_{DSS}$	drain leakage current	$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.03	2	μΑ
		$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 \text{ °C}$	-	-	30	μΑ
I <sub>GSS</sub>	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	10	100	nΑ
		$V_{GS} = -20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	10	100	nΑ
20011	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A}; T_j = 175 ^{\circ}\text{C};$ see Figure 12	-	28.9	34	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A}; T_j = 100 \text{ °C};$ see Figure 12	-	-	23.7	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A}; T_j = 25 \text{ °C};$ see Figure 13	-	12.6	14.8	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz	-	1.3	-	Ω
Dynamic	characteristics					
$Q_{G(tot)}$	total gate charge	$I_D = 25 \text{ A}$ ; $V_{DS} = 30 \text{ V}$ ; $V_{GS} = 10 \text{ V}$ ; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	20.9	-	nC
		$I_D = 0 A; V_{DS} = 0 V; V_{GS} = 10 V$	-	17	-	nC
$Q_{GS}$	gate-source charge	$I_D = 25 \text{ A}$ ; $V_{DS} = 30 \text{ V}$ ; $V_{GS} = 10 \text{ V}$ ; see Figure 14; see Figure 15	-	6.2	-	nC
Q <sub>GS(th)</sub>	pre-threshold gate-source charge	$I_D = 25 \text{ A}; V_{DS} = 30 \text{ V}; V_{GS} = 10 \text{ V};$ see Figure 14	-	3.7	-	nC
Q <sub>GS(th-pl)</sub>	post-threshold gate-source charge		-	2.4	-	nC
$Q_{GD}$	gate-drain charge	$I_D = 25 \text{ A}$ ; $V_{DS} = 30 \text{ V}$ ; $V_{GS} = 10 \text{ V}$ ; see Figure 14; see Figure 15	-	4.7	-	nC
$V_{GS(pl)}$	gate-source plateau voltage	V <sub>DS</sub> = 30 V; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	4.8	-	V
C <sub>iss</sub>	input capacitance	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$	-	1220	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C; see <u>Figure 16</u>	-	169	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	95	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1.2 \Omega; V_{GS} = 10 \text{ V};$	-	12	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 4.7 \Omega$	-	13	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	27	-	ns
t <sub>f</sub>	fall time			7		ns

Table 6. Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Source-drain diode						
$V_{SD}$	source-drain voltage	$I_S = 15 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.8	1.2	V
t <sub>rr</sub>	reverse recovery time	$I_S = 25 \text{ A}$ ; $dI_S/dt = -100 \text{ A/}\mu\text{s}$ ;	-	31	-	ns
Q <sub>r</sub>	recovered charge	$V_{GS} = 0 \text{ V}; V_{DS} = 30 \text{ V}$	-	28.5	-	nC

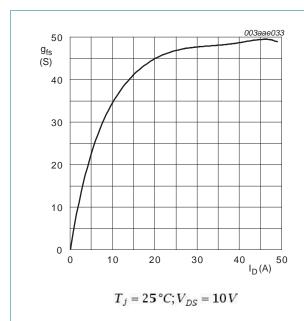


Fig 5. Forward transconductance as a function of drain current; typical values

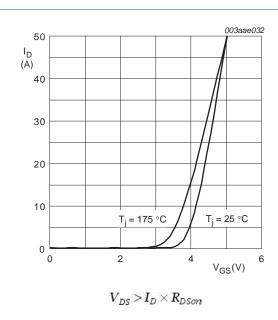


Fig 6. Transfer characteristics: drain current as a function of gate-source voltage; typical values

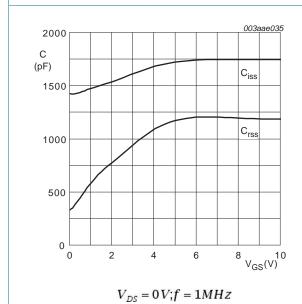


Fig 7. Input and reverse transfer capacitances as a function of gate-source voltage; typical values

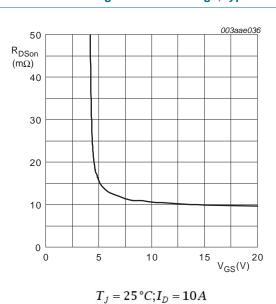


Fig 8. Drain-source on-state resistance as a function of gate-source voltage; typical values

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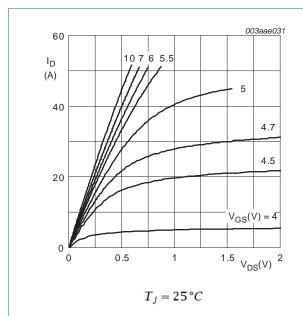


Fig 9. Output characteristics: drain current as a function of drain-source voltage; typical values

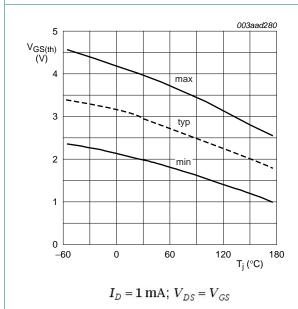
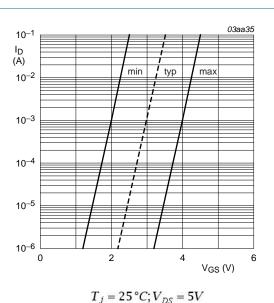


Fig 11. Gate-source threshold voltage as a function of junction temperature



 $T_j = 25 \, \mathrm{C}; V_{DS} = 5V$ 

Fig 10. Sub-threshold drain current as a function of gate-source voltage

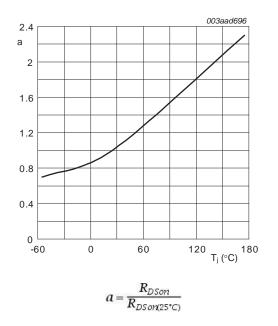


Fig 12. Normalized drain-source on-state resistance factor as a function of junction temperature.

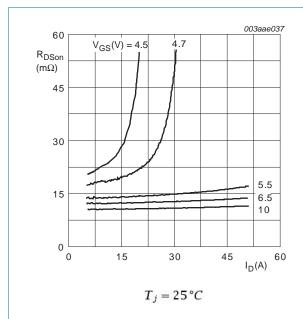
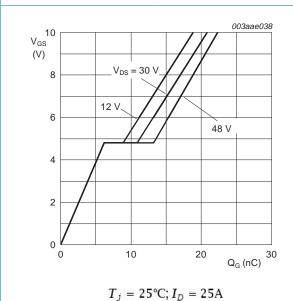


Fig 13. Drain-source on-state resistance as a function of drain current; typical values

Fig 14. Gate charge waveform definitions



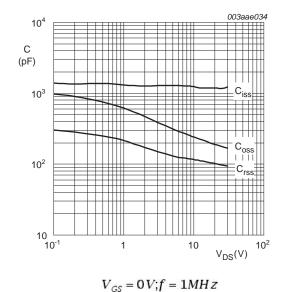


Fig 15. Gate-source voltage as a function of gate charge; typical values

Fig 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

# 7. Package outline

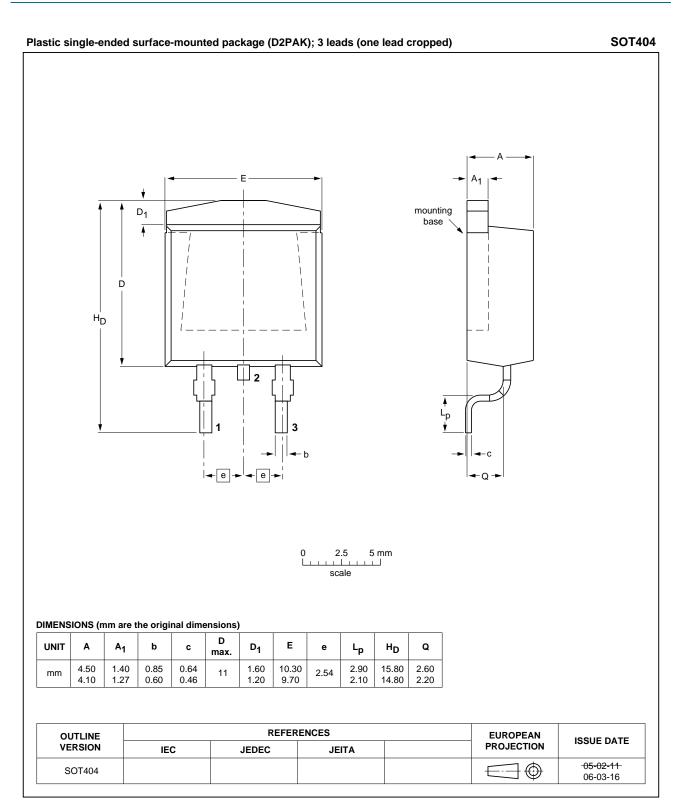


Fig 17. Package outline SOT404 (D2PAK)

# 8. Revision history

### Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PSMN015-60BS v.2	20120301	Product data sheet	-	PSMN015-60BS v.1
Modifications:	<ul> <li>Status changed f</li> </ul>	rom objective to product.		
	<ul> <li>Various changes</li> </ul>	to content.		
PSMN015-60BS v.1	20111021	Objective data sheet	-	-

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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# **PSMN015-60BS**

# **Nexperia**

N-channel 60 V 14.8 mΩ standard level MOSFET in D2PAK

# 11. Contents

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