



High-performance 32-bit RISC microcontrollers with a USB device controller capable of operating at up to 200 MHz

Features

ARM926EJ-STM CPU Core

·Operating voltage:

Internal: 1.4 to 1.6 V I/O:1.7 to 1.9 V, 3.0 to 3.6 V

·Minimum instruction execution time: 5 ns (200 MHz internal, 0 to 70°C)

6.67 ns (150 MHz internal, -20 to 85°C)

·Data cache: 16 Kbytes ·Instruction cache: 16 Kbytes ·Internal ROM: 16 Kbytes (Boot) ·Internal RAM: 32 Kbytes ·External data bus width: Up to 16 bits

On-chip Functions

·Color LCD controller (24-bit TFT/STN) : 1 channel

·LCD data process accelerator

·Memory controller

Static memory SDR SDRAM

LVCMOS DDR SDRAM

·NAND Flash controller

·CMOS image sensor interface

·USB (High-speed) device controller

·DMA controller

·SSP (SPI/MicroWire mode)

·RTC

: 2 channels

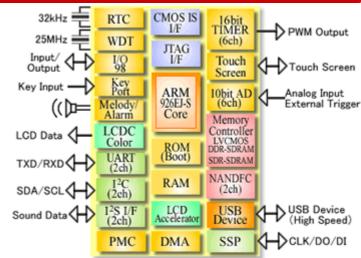
: 1 channel

: 1 channel

:8 channels

: 2 channels

: 1 channel



·UART 2 channels $\cdot I^2C$ 2 channels ·I2S interface 2 channels

·10-bit AD converter 6 channels ·16-bit timer 6 channels ·Touch-screen interface 1 channel

·JTAG interface

·Power management circuit (PMC)

Package Information

Pin	A scior	ments
- I III	Assigi	шспь

A1	A2	АЗ	A4	A5	Ав	A7	A8	A9	A10	A11	A12	A13	A14	A15	A16	A17	A18	A19
В1	B2	ВЗ	В4	B5	В6	В7	В8	В9	B10	B11	B12	B13	B14	B15	B16	B17	B18	B19
C1	C2	СЗ	C4	C5	С6	C7	С8	C9	C10	C11	C12	C13	C14	C15	C16	C17	C18	C19
D1	D2	DЗ	D4	D5	D6	D7	D8	D9	D10	D11	D12	D13	D14	D15	D16	D17	D18	D19
E1	E2	E3	E4	E5	E6	E7	E8	E9	E10	E11	E12	E13	E14	E15	E16	E17	E18	E19
F1	F2	F3	F4	F5	F6	F7	F8	F9	F10	F11	F12	F13	F14	F15	F16	F17	F18	F19
G1	G2	GЗ	G4	G5	G6	G7	G8	G9	G10	G11	G12	G13	G14	G15	G16	G17	G18	G19
Н1	H2	НЗ	Н4	H5	Н6	H7	Н8	Н9	H10	H11	H12	H13	H14	H15	H16	H17	H18	H19
J1	J2	JЗ	34	5	J6	5	9	9	J10	J11	J12	J13	J14	J15	J16	J17	J18	J19
K1	K2	ΚЗ	K4	K5	K6	K7	K8	K9	K10	K11	K12	K13	K14	K15	K16	K17	K18	K19
L1	L2	L3	L4	15	L6	17	2	2	L10	L11	L12	L13	L14	L15	L16	L17	L18	L19
M1	M2	МЗ	М4	М5	M6	M7	М8	М9	M10	M11	M12	M13	M14	M15	M16	M17	M18	M19
N1	N2	N3	N4	N5	N6	N7	N8	N9	N10	N11	N12	N13	N14	N15	N16	N17	N18	N19
P1	P2	РЗ	Р4	P5	P6	P7	Р8	Р9	P10	P11	P12	P13	P14	P15	P16	P17	P18	P19
R1	R2	R3	R4	R5	R6	R7	R8	R9	R10	R11	R12	R13	R14	R15	R16	R17	R18	R19
T1	T2	ТЗ	T4	T5	T6	T7	T8	Т9	T10	T 11	T12	T13	T14	T15	T16	T 17	T18	T19
/ U1	U2	U3	U4	U5	U6	U7	U8	U9	U10	U11	U12	U13	U14	U15	U16	U17	U18	U19
V1	V2	νз	٧4	∨5	V6	5	8	9	V10	V11	V12	V13	V14	V15	V16	V17	V18	V19
W1	VV2	W3	W4	W5	VV6	W7	98	909	W10	W11	W12	W13	W14	W15	W16	W17	W18	W19

·Package name:

FBGA361-P-1616-0.80AZ

Top View

• Pin Numbers and Names (1/3)

	nu Names (1/3)				
Ball No.	Pin name	Ball No.	Pin name	Ball No	Pin name
C3	SPD/TCK	L6	PC5/MLDALMh/INT8	T6	SW2/NDALE
D4	SP1/TMS	K2	DVCC3I04	N8	SW1/NDWEn
K8	DVSSCOM1	L5	PC6/I2C0CL	U6	SV3/NDD3
D3	SP2/TDI	L1	PC2/PWE	W3	DVCC3109
B1	DVCC3I01	L4	PT5/U1RXD	N9	SV6/NDD6
E 5	SP5/TDO	КЗ	DVSSCOM7	R7	SV2/NDD2
E4	SP4/RTCK	N7	PT4/U1TXD	N10	SW0/NDREn
F5	SP3/TRSTn	M1	PC7/I2C0DA/INT9	R8	PP2/INT2
G6	DVCC3CMS1	T1	SM4/RESETn	V4	DVCC1A3
G5	PF6/I2C1CL	P7	TEST05	17	SV5/NDD5
F4	PF7/I2C1DAINTC	L2	DVCC3I05	√5	SV1/NDD1
H7	PF1/CMSHSY	N6	TEST04	P9	PP1/INT1
J7	PE7/CMSD7	L3	TEST03	L9	DVSSCOM11
E3	PF2/CMSHBK	M2	TEST02	P10	SV4/NDD4
D2	PF3/CMSVSY	M8	DVSSCOM8	U7	SV0/NDD0
K8	DVSSCOM2	P1	TEST07	R9	PPO/INTO
F3	PFD/CMSPCK	M4	TEST15	W4	DVCC3I010
L7	PE6/CMSD6	M3	TEST06	M10	DVSSCOM12
C2	PE3/CMSD3	M5	TEST01	V6	SN2/SELJTAG
G4	PE4/CMSD4	N2	DVCC3106	T8	SM6/AMD
E2	PE5/CMSD5	N5	TEST00	U8	SM7/AM1
K/B	DVCC3CMS2	R2	DVCC1A2	VV5	DVCC3I011
C1	PED/CMSD0	P6	TEST16	R10	SN0/SELMBMC
H5	PE1/CMSD1	R1	TEST14	T10	SN1/SELDVCCM
G3	PE2/CMSD2	R6	TEST12	VV6	DVCC1C
J8	DVSSC0M3	P2	TEST11	W7	SMD/X1
D1	PT2/SP0D0	P4	DVSSCOM9	V7	DVSS1C
H4	PTD/SP0FSS	N3	TEST10	VV8	SM1/X2
E1	DVCC3I02	N1	TEST17	V8	DVCC1C
Н6	PN1/U0RXD/SIR0IN	N4	TEST13	U9	PT7/X1USB
F2	PN0/U0TXD/SIR0OUT	P 3	DVCC3I07	Т9	DVCC1A4
J5	PT3/SP0DI	T2	VREFH	К9	DVSSCOM13
F1	DVCC1A1	U2	VREFL	V9	AVDD3C
J8	DVSSCOM4	U1	AVSS3AD	VV9	SR4/VSENSE
H3	PT1/SP0CLK	VI	AVCC3AD	V10	SR3/REXT
K5	PT6/U1CTSn	P5	PD5/AN5/MY	W10	AVSS3C
G2	PN4/U0DSRn/INTD	R3	PD4/AN4MX	U10	AVDD3T1
MB	PN7/U0RTSn/INTG	R5	PD3/AN3	V11	AVSS3T3
G1	DVCC3103	R4	PD2/AN2		AVSS3T2
J4	PN3/U0DCDn	T5	PD1/AN1	W11	SR1/DM
H2	PN5/U0RIn/INTE	T3	PD0/AN0	W12	SRO/DP
K4	PN2/U0CTSn	U3	PD6/PX/INTA(TSI)	V12	AVSS3T1
H1	PN6/U0DTRn/INTF	T4	PD7/PY/INTB	U12	AVSS3T1
L8	DVSSCOM5	V2	D/CC3108	W13	AVSS3T0
J1	SM2/XT1	V3	SW6/NDRB	V13	AMDD3T0
K1	SM3/XT2	U4	SW4/NDCE0n	J10	DVSSC0M14
M9	DVSSC0M6	VV2	SW5/NDCE1n	V14	DVCC1A6
J3	PC4/FSOUT/PWM2OUT	U5	SW3/NDCLE	T11	PMD/I2S1W/S
M7	DVCC1B1	L9	DVSSCOM10	V15	PM2/I2S1DATO
J2	PC3/MLDALM/PW/MDOUT	P8	SV7/NDD7	R11	PL2/I2S0DATI/SP1D0

• Pin Numbers and Names (2/3)

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Ball No.	Pin name	Ball No.	Pin name	Ball No	Pin name
W14	PL4/I2SSCLK	M14	TEST33	E19	DVCC1A9
T12	DVCC3l2S1	R18	SB1/D9	H14	TEST36
U13	PL1/I2S0CLK/SP1CLK	M15	TEST32	F18	SE4/A4
P11	PM3/I2S1MCLK	T19	DVCCM6	G15	SG4/A20
W16	PM1/I2S1CLK	M16	SB7/D15	J11	D√SSCOM26
N11	PL3/I2S0MCLK/SP1DI	P18	SB4/D12	G16	SG2/A18
L10	DVSSCOM15	L11	DVSSCOM21	G17	SF4/A12
R12	PLD/I2SDW/S/SP1FSS	L12	PP3/INT3	F15	SG5/A21
U14	DVCC3l2S2	M17	PP4/INT4	D19	DVCCM9
T13	PR0/RESETOUTn	L13	PP5/INT5	F16	SG3/A19
W15	PR1/SMCW/Pn/FCOUT	R19	PP6/INT6	E18	SE5/A5
W17	DVCC1A6	L14	PP7/INT7	F17	SF5/A13
U15	DVCCM1	N18	PA0/KI0	G14	DVSSCOM27
R13	TEST23	P19	DVCC3I012	E15	SG6/A22
V16	SA0/D0	N19	PA1/KI1	D18	SB8/A6
P12	TEST26	L15	PA2/KI2	C19	SE7/A7
M11	DVSSCOM16	L17	PA3/KI3	E16	DVCCM10
P13	TEST25	L16	PA4/KI4	C18	SF1/A9
W18	SL6/DMCCLKIN	M18	PA6/KI5	E17	SF6/A14
P14	TEST24	L11	DVSSCOM22	D17	SF7/A15
U15	DVCCM2	M19	PA6/KI6	H13	DVSSCOM28
T14	TEST21	K16	PA7/KI7	B19	SF0/A8
V17	SA1/D1	L19	DVCC1A8	C17	SG0/A16
V18	SA2/D2	K15	PB0/K00	D16	DVSSCOM29
M11	DVSSCOM17	L18	PB1/K01	B18	TEST43
R14	TEST22	K13	PB2/K02	A18	TEST42
R15	TEST20	K17	SM5/TEST0n	D15	DVCCM11
U16	SA3/D3	K19	DVCC3I013	C16	SL2/DMCAP
T15	DVCCMB	K18	PB3/K03	E14	SG7/A23
T16	SA6/D5	K14	PB4/K04	D14	DVSSCOM30
U17	SL4/DMCDDQS0	J19	PB5/K05	C15	SJ4/DMCBA0
V19	SL5/DMCDDQS1	K11	DVSSCOM23	C14	SJ5/DMCBA1
N13	DVSSCOM18	J18	PB6/K06	F14	DVSSCOM31
T17	SA6/D6	J15	PB7/K07	F13	SH1/A25
R17	SB0/D8	H19	PCD/KO8	E13	DVCC1A10
R16	SA7/D7	J16	PC1/K09	B17	SKD/DMCSDQMD/DMCDDMD
P15	DVCCM4	K11	DVSSCOM24	B16	DVCCM12
N14	TEST30	J14	DVCC182	G13	SH0/A24
U18	SA4/D4	J17	SF2/A10		DVSSCOM32
P16	SB2/D10	J13	TEST37	D13	TEST47
M12	DVSSCOM19	H18	SE2/A2	C13	SJ2/DMCRASn
N15	TEST27	K12	DVCCM7	B13	DVSSC0M33
P17	SB3/D11	G19	SED/A0	D12	SK5/SMCBEIn
U19	DVCC1A7	H16	SG1/A17	H12	PR2/INTH
N12	TEST31	G18	SE3/A3	B15	DVCCM13
T18	DVCCM5	J12	DVSSCOM25	F12	SH2/SMCBEOn
M13	TEST34	H17	SF3/A11	C12	TEST46
N17	SB6/D14	H15	TEST35	E12	SK4/SMCWEn
N16	SB5/D13	F19	SE1/A1	A14	DVSSC0M34
K10	DVSSCOM20	K12	DVCCM8	A16	SLD/DMCDCLKP
KID	D 000 COMED	1114	DVCCIIIO	_ A10	SEADING DO ENT

Pin Numbers and Names (3/3)

Ball No. Pin name No. Pin name No. Add DVC3LCD4	iics (c	,,,,,	T I	
### ### ##############################	Ball No.	Pin name	Ball No.	Pin name
B14	A15	SL1/DMCDCLKN	A4	DVCC3LCD4
D11 SJ7/SMCANDN E7 SU2/LCILE F10 DVCCM14 C6 DVSSCOM43 G12 SJ1/DMCMEN G8 PJ5/LD13 A13 SL3/SMCCLK A3 PJ4/LD12 F11 DVCC1A11 D6 PJ3/LD11 B11 DVSSCOM36 A2 DVCC3LCD5 E11 SJ0/SMCOEN C4 SU1/LCIAC B12 SJ3/DMCCASN E6 PJ7/LD15 A12 DVSSCOM37 D5 PJ6/LD14 B10 SH6/SMCCS2N H9 DVSSCOM44 E10 SH6/SMCCS3N C5 SU3/LCIEP A12 DVSSCOM38 F7 SU4/LCILP D10 SH3/SMCCS0N F6 PK7/LD23 A11 SJ6/DMCCKE B2 DVCC1A14 F10 DVCCM15 C10 SH4/SMCCS1N C11 SH7/DMCCSN A10 DVSSCOM39 G11 SL7/SMCMATTN A9 DVCC1A12 G10 PK1/LD17 B9 PK0/LD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4/LD20 F9 PK3/LD19 A7 PK2/LD18 B8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCICP E8 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8 C7 PJ0/LD8	A14		J6	
F10	B14	SK1/DMCSDQM1/DMCDDM1	B3	PJ1/LD9
G12 SJI/DMCOVEN G8 PJ5/LD13 A13 SL3/SMCCLK A3 PJ4/LD12 F11 DVCC1A11 D6 PJ3/LD11 B11 DVSSCOM36 A2 DVCC3LCD5 E11 SJJO/SMCCA5h C4 SU1/LCLAC B12 SJJOMCCA5h B6 PJ7/LD15 A12 DVSSCOM37 D5 PJ6/LD14 B10 SH6/SMCCS2h H9 DVSSCOM44 E10 SH6/SMCCS2h H9 DVSSCOM44 E10 SH6/SMCCS3h C5 SU3/LCLFP A12 DVSSCOM38 F7 SU4/LCLLP D10 SH3/SMCCS0h F6 PK7ALD23 A11 SJ6/DMCCKE B2 DVCC1A14 F10 DVCCM15 DVCC1A14 C10 SH4/SMCCS1h C11 SH7/DMCCSh A11 SH7/DMCCSh A A12 DYCC1A12 D G10 PK1A.D17 B9 PK0A.D16 D9 DYCC3LCD1				
A13	F10	DVCCM14	C6	DVSSC 0M43
F11 DVCC1A11 D6 PJ3/LD11 B11 DVSSCOM36 A2 DVCC3LCD5 E11 SJ0/SMC0Eh C4 SU1/LCLAC B12 SJ3/DMCCASh D6 PJ7/LD15 A12 DVSSCOM87 D5 PJ6/LD14 B10 SH5/SMCCS2h H9 DVSSCOM44 E10 SH6/SMCCS3h C5 SU3/LCLFP A12 DVSSCOM38 F7 SU4/LCLP D10 SH3/SMCCS0h F6 PK7/LD23 A11 SJ6/DMCCKE B2 DVCC1A14 F10 DVCCM15 DVCC1A14 C10 SH4/SMCCS1h C11 C11 SH7/DMCCSh A0 A10 DVSSCOM39 G11 G11 SL7/SMCWATh A0 A9 DVCC1A12 G10 G10 PK1/LD17 B9 B9 PK0/LD16 D D9 DVCC3LCD1 D C9 ST2/LD2 B B7	G12	SJ1/DMCWEn	G8	PJ5/LD13
B11 DVSSCOM86 A2 DVCC3LCD5 E11 SJ0/SMC0En C4 SU1/LCLAC B12 SJ3/DMCCASn E8 PJ7/LD15 A12 DVSSCOM87 D5 PJ6/LD14 B10 SH5/SMCCS2n H9 DVSSCOM44 E10 SH6/SMCCS3n C5 SU3/LCLEP A12 DVSSCOM38 F7 SU4/LCLLP D10 SH3/SMCCS0n F6 PK7/LD23 A11 SJ6/DMCCKE B2 DVCC1AI4 F10 DVCCM15 C10 SH4/SMCCS1n C11 SH7/DMCCSN A10 DVSSCOM39 G11 SL7/SMCWAITN A9 DVCC1AI2 G10 PK1/LD17 B9 PK0/LD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4/LD20 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41	A13	SL3/SMCCLK	A3	PJ4/LD12
E11 SJJVSMCOBN C4 SUT/LCIAC B12 SJJVDMCCASN B6 PJ7/LD15 A12 DVSSCOM37 D5 PJ8/LD14 B10 SH5/SMCCS2N H9 DVSSCOM44 E10 SH6/SMCCS3N C5 SUJ/LCIFP A12 DVSSCOM38 F7 SUJ/LCIFP A12 DVSSCOM38 F7 SUJ/LCIFP A12 DVSSCOM38 F7 SUJ/LCIFP A11 SJR/MCCS0N F6 PK7/LD23 A11 SJR/MCCSIN C10 SH4/SMCCSIN C10 SH4/SMCCSIN C11 SH7/DMCCSN A10 DVSSCOM39 G11 SL7/SMCWAITN A9 DVCC1A12 G10 PK1A.D17 B9 PK0A.D16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4A.D20 PK2A.D18 D D8 DVCC3LCD2 B F7	F11	DVCC1A11	D6	PJ3/LD11
B12 SJ3/DMCCASn E8 PJ7/LD15 A12 DVSSCOM37 D5 PJ6/LD14 B10 SH6/SMCCS2n H9 DVSSCOM44 E10 SH6/SMCCS3n C5 SU3/LCLFP A12 DVSSCOM38 F7 SU4/LCLLP D10 SH3/SMCCS0n F6 PK7/LD23 A11 SJ6/DMCCKE B2 DVCC1A14 F10 DVCCM15 DVCC1A14 C10 SH4/SMCCS1n C11 SH7/DMCCSn A10 DVSSCOM39 G11 SL7/SMCWAITh A9 DVCC1A12 G10 PK1ALD17 B9 PK0ALD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4/LD20 PK2/LD18 D D8 DVCC3LCD2 B7 ST6/LD5 G9 ST4/LD4 C3 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP B8 </td <td>B11</td> <td>DVSSCOM36</td> <td>A2</td> <td>DVCC3LCD5</td>	B11	DVSSCOM36	A2	DVCC3LCD5
A12 DVSSCOM87 D5 PJ6/LD14 B10 SH6/SMCCS2h H9 DVSSCOM44 E10 SH6/SMCCS3h C5 SU3/LCLFP A12 DVSSCOM38 F7 SU4/LCLLP D10 SH3/SMCCS0h F6 PK7/LD23 A11 SJ6/DMCCKE B2 DVCC1A14 F10 DVCCM15 C10 SH4/SMCCS1h C11 SH7/DMCCSh A10 DVSSCOM39 G11 SL7/SMCWAITh A9 DVCC1A12 G10 PK1ALD17 B9 PK0ALD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4ALD20 F9 PK3ALD19 A7 PK2ALD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP B8 PK6ALD21 DVCC3LCD3 C7 <td>E11</td> <td>SJØ/SMCOEn</td> <td>C4</td> <td>SU1/LCLAC</td>	E11	SJØ/SMCOEn	C4	SU1/LCLAC
B10	B12		B6	
E10 SH6/SMCCS3h C5 SU3/LCLFP A12 DVSSCOM38 F7 SU4/LCLLP D10 SH3/SMCCS0h F6 PK7/LD23 A11 SJ&DMCCKE B2 DVCC1A14 F10 DVCCM15 C10 SH4/SMCCS1h C11 SH7/DMCCSh A10 DVSSCOM39 G11 SL7/SMCWAITh A9 DVCC1A12 G10 PK1/LD17 B9 PK0/LD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4/LD20 F9 PK3/LD19 A7 PK2/LD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP B8 PK6/LD21 D7 DVCC3LCD1 D7 DVCC3LCD1 D7 DVCC3LCD3 C7 PJ0/LD8	A12		D5	
### ### ##############################	B10	SH5/SMCCS2n	H9	DVSSCOM44
D10	E10	SH6/SMCCS3n	C5	SU3/LCLFP
A11 SJ&DMCCKE B2 DVCC1AI4 F10 DVCCM15 C10 SH4/SMCCSIn C11 SH7/DMCCSN A10 DVSSCOM39 G11 SL7/SMCWAITN A9 DVCC1AI2 G10 PK1/LD17 B9 PK0/LD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4/LD20 F9 PK3/LD19 A7 PK2/LD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C3 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP B8 PK6/LD21 B7 DVCC3LCD1 C7 PJ0/LD8	A12	DVSSCOM38	F7	SU4/LCLLP
F10	D10	SH3/SMCCS0n	F6	PK7/LD23
C10 SH4/SMCCS1n C11 SH7/DMCCSn A10 DVSSCOM39 G11 SL7/SMCWAITn A9 DVCC1A12 G10 PK1A.D17 B9 PK0A.D16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4A.D20 F9 PK3A.D19 A7 PK2A.D18 D8 DVCC3LCD2 B7 ST6/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP E8 PK6A.D22 B6 PK5A.D21 D7 DVCC3LCD3 C7 PJ0/LD8	A11	SJ6/DMCCKE	B2	DVCC1A14
C11 SH7/DMCCSn A10 DVSSCOM39 G11 SL7/SMCWAITn A9 DVCC1A12 G10 PK1A.D17 B9 PK0A.D16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4A.D20 F9 PK3A.D19 A7 PK2A.D18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCCP B8 PK6A.D22 B6 PK6A.D22 B6 PK5A.D21 D7 DVCC3LCD3 C7 PJ0/LD8	F10	DVCCM15		
A10 DVSSCOM39 G11 SL7/SMCWAITh A9 DVCC1A12 G10 PK1/LD17 B9 PK0/LD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSCOM40 B8 PK4/LD20 F9 PK3/LD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP B8 PK6/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	C10	SH4/SMCCS1n	\Box	
G11 SL7/SMCWAITh A9 DVCC1AI2 G10 PK1AD17 B9 PK0AD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSC0M40 B8 PK4AD20 F9 PK3AD19 A7 PK2AD18 D8 DVCC3LCD2 B7 ST6/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSC0M41 A6 SU0/LCLCP B8 PK6AD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	C11	SH7/DMCCSn	\Box	
## A9 DVCC1#12 G10	A10	DVSSCOM39	\Box	
G10 PK1AD17 B9 PK0AD16 D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSC0M40 B8 PK4ALD20 F9 PK3AD19 A7 PK2AD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSC0M41 A6 SU0/LCLCP B8 PK6ALD22 B6 PK5ALD2 B7 PK5ALD2 B7 PK5ALD22 B7 PK6ALD22 B7 PK6ALD22 B8 PK6ALD22 B9 PK6ALD22 B9 PK5ALD21 D7 DVCC3LCD3	G11	SL7/SMCW/AITn	\vdash	
B9	A9	DVCC1A12	\vdash	
D9 DVCC3LCD1 C9 ST2/LD2 E9 ST1/LD1 A8 STD/LD0 H11 DVSSC0M40 B8 PK4ALD20 F9 PK3ALD19 A7 PK2ALD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C3 ST3/LD3 H10 DVSSC0M41 A6 SU0/LCLCP E8 PK6ALD22 B6 PK5ALD21 D7 DVCC3LCD3 C7 PJ0/LD8	G10		\vdash	
C9 ST2/LD2 E9 ST1/LD1 A8 ST0/LD0 H11 DVSSC0M40 B8 PK4/LD20 F9 PK3/LD19 A7 PK2/LD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSC0M41 A6 SU0/LCLCP B8 PK6/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	B9	PKD/LD16		
B9	D9	DVCC3LCD1	\Box	
A8 STD/LD0 H11 DVSSCOM40 B8 PK4/LD20 F9 PK3/LD19 A7 PK2/LD18 D8 DVCC3/LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP B8 PK6/LD22 B6 PK5/LD21 D7 DVCC3/LCD3 C7 PJ0/LD8	C9	ST2/LD2	\Box	
H11 DVSSCOM40 B8 PK4LD20 F9 PK3LD19 A7 PK2LD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP B8 PK6LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	E9	ST1/LD1	\Box	
B8	A8	STO/LD0	\Box	
F9 PK3/LD19 A7 PK2/LD18 D8 DVCC3LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP E8 PK6/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	H11	DVSSC OM40	\Box	
A7 PK2/LD18 D8 DVCC3/LCD2 B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP B8 PK6/LD22 B6 PK5/LD21 D7 DVCC3/LCD3 C7 PJ0/LD8	B8	PK4/LD20	\Box	
D8 DVCC3LCD2 B7 ST6/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSC0M41 A6 SU0/LCLCP E8 PK6/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	F9	PK3/LD19	\Box	
B7 ST5/LD5 G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP E8 PK8/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	A7	PK2/LD18	\Box	
G9 ST4/LD4 C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP E8 PK6/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	D8	DVCC3LCD2	\Box	
C8 ST3/LD3 H10 DVSSCOM41 A6 SU0/LCLCP E8 PK8/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	B7	ST5/LD5	\Box	
H10 DVSSCOM41 A6 SU0/LCLCP B3 PK8/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	G9	ST4/LD4	\Box	
A6 SU0/LCLCP E8 PK6/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	C8	ST3/LD3	\Box	
B8 PK6/LD22 B6 PK5/LD21 D7 DVCC3LCD3 C7 PJ0/LD8	H10	DVSSC0M41	\Box	
B6	A6	SUDVLCLCP	\Box	
D7 DVCC3LCD3 C7 PJ0/LD8	B8	PK6/LD22		
C7 PJ0/LD8	B6		\Box	
	D7	DACC3TCD3		
H8 ST7/LD7	C7	PJ0/LD8		
	H8	ST7/LD7	\Box	
95 ST6/LD6	B5	ST6/LD6	\Box	
J9 DVSSCOM42	J9	DVSSC 0M42	\Box	
A6 DVCC1A13	Aδ	DVCC1A13	\Box	
F8 SU7/LPRG2	F8	SU7/LPRG2	\Box	
B4 SU6/LPRG1	B4	SU6/LPRG1	\Box	
K7 SU5/LPRG0				

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