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October 2013

RS1A - RS1M Fast Rectifiers

Features

- · Glass-Passivated Junction
- For Surface Mounted Applications
- · Built-in Strain Relief, Ideal for Automated Placement
- UL Certified: Certificate # E326243



Ordering Information

Part Number	Marking	Package	Packing Method
RS1A	RS1A		
RS1B	RS1B		
RS1D	RS1D		
RS1G	RS1G	DO-214AC	Tape and Reel
RS1J	RS1J		
RS1K	RS1K		
RS1M	RS1M		

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value							Units
Symbol	raiametei		1B	1D	1G	1J	1K	1M	Ullits
V_{RRM}	Maximum Repetitive Reverse Voltage		100	200	400	600	800	1000	V
I _{F(AV)}	Average Rectified Forward Current at T _A = 100°C		1.0						
I _{FSM}	Non-Repetitive Peak Forward Surge Current: 8.3 ms Single Half-Sine Wave		30						Α
T _{STG}	Storage Temperature Range		-55 to +150						°C
TJ	Operating Junction Temperature		-55 to +150						°C

Thermal Characteristics(1)

Symbol	Parameter	Value	Units
P _D	Power Dissipation	1.19	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽¹⁾	105	°C/W
$R_{\theta JL}$	Thermal Resistance, Junction to Lead ⁽¹⁾	32	°C/W

Note:

1. Device mounted on FR-4 PCB 0.013 mm.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Teat Conditions	Value						Units	
Symbol			1A	1B	1D	1G	1J	1K	1M	Units
V _F	Forward Voltage	1.0 A				1.3				V
t _{rr}		$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A},$ $I_{rr} = 0.25 \text{ A}$		15	50		250	50	00	ns
I _R	Reverse Current at	T _A =25°C		5.0					μΑ	
Rated V _R		T _A =125°C	50					μΑ		
СТ	Total Capacitance $V_R = 4.0 \text{ V},$ $f = 1.0 \text{ MHz}$				pF					

Typical Performance Characteristics

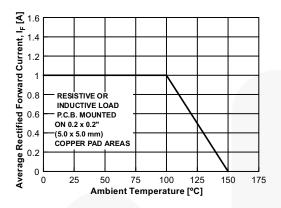


Figure 1. Forward Current Derating Curve

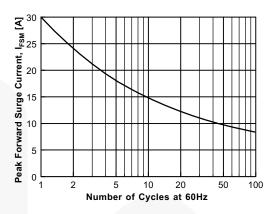


Figure 2. Non-Repetitive Surge Current

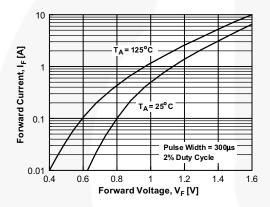


Figure 3. Forward Voltage Characteristics

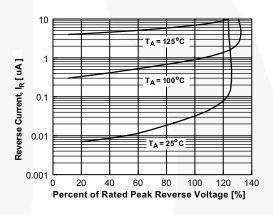


Figure 4. Reverse Current vs. Reverse Voltage

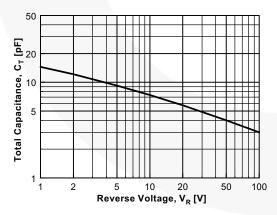


Figure 5. Total Capacitance

Physical Dimension

DO-214AC

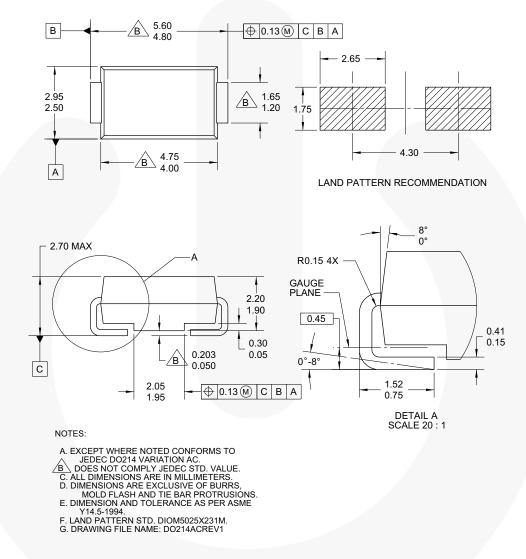


Figure 6. 2-LEAD, SMA, JEDEC DO-214, VARIATION AC (ACTIVE)

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Current Transfer Logic™



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Datasheet Identification		Definition					
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.					
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