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November 2013

FQD13N10

N-Channel QFET® MOSFET

100 V, 10 A, 180 mΩ

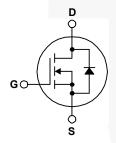
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce • Low Gate Charge (Typ. 12 nC) on-state resistance, and to provide superior switching • Low Crss (Typ. 20 pF) performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, • 100% Avalanche Tested audio amplifier, DC motor control, and variable switching power applications.

Features

- 10 A, 100 V, $R_{DS(on)}$ = 180 m Ω (Max.) @ V_{GS} = 10 V, $I_D = 5 A$





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter	FQD13N10TM	Unit
V_{DSS}	Drain-Source Voltage	100	V
I _D	Drain Current - Continuous (T _C = 25°C)	10	Α
	- Continuous (T _C = 100°C)	6.3	Α
I _{DM}	Drain Current - Pulsed (Note	1) 40	Α
V _{GSS}	Gate-Source Voltage	± 25	V
E _{AS}	Single Pulsed Avalanche Energy (Note	2) 95	mJ
I _{AR}	Avalanche Current (Note	1) 10	А
E _{AR}	Repetitive Avalanche Energy (Note	1) 4.0	mJ
dv/dt	Peak Diode Recovery dv/dt (Note	3) 6.0	V/ns
P_{D}	Power Dissipation (T _A = 25°C) *	2.5	W
	Power Dissipation (T _C = 25°C)	40	W
- Derate above 25°C		0.32	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
T _L	Maximum lead temperature for soldering, .1/8" from case for 5 seconds	300	°C

Thermal Characteristics

R _{θJC} Thermal Resistance, Junction to Case, Max. 3.13	<u>.</u>
Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max. 110	°C/W
R _{0JA} Thermal Resistance, Junction to Ambient (*1 in ² Pad of 2-oz Copper), Max. 50	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQD13N10TM	FQD13N10	DPAK	Tape and Reel	330 mm	16 mm	2500 units

Electrical Characteristics

T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.09		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100 V, V _{GS} = 0 V			1	μΑ
		V _{DS} = 80 V, T _C = 125°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 25 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -25 V, V _{DS} = 0 V			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 5.0 A		0.142	0.18	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 5.0 A		6.3		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,	-	345	450	pF
Coss	Output Capacitance	f = 1.0 MHz	\	100	130	pF
C _{rss}	Reverse Transfer Capacitance			20	25	pF

Switching Characteristics

t _{d(on)}	Turn-On Delay Time	V _{DD} = 50 V, I _D = 12.8 A,		 5	20	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		 55	120	ns
t _{d(off)}	Turn-Off Delay Time	1.0		 20	50	ns
t _f	Turn-Off Fall Time		(Note 4)	 25	60	ns
Q_g	Total Gate Charge	V _{DS} = 80 V, I _D = 12.8 A,		 12	16	nC
$\frac{Q_g}{Q_gs}$	Gate-Source Charge	V _{GS} = 10 V		 2.5		nC
Q _{gd}	Gate-Drain Charge		(Note 4)	 5.1		nC

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current		 	10	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		 -	40	Α
V_{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 10 A	 	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 12.8 A,	 72		ns
Q _{rr}	Reverse Recovery Charge	dI _F / dt = 100 A/μs	 0.17	/	μС

- $\label{eq:Notes:1} \begin{array}{l} \textbf{Notes:} \\ \textbf{1. Repetitive rating: pulse-width limited by maximum junction temperature.} \\ \textbf{2. L} = \textbf{1.43 mH, I}_{AS} = \textbf{10 A, V}_{DD} = \textbf{25 V, R}_{G} = \textbf{25 }\Omega, \text{ starting } \ T_{J} = \textbf{25}^{\circ}C. \end{array}$
- 3. $_{SD}$ = 12.8 A, di/dt \leq 300 A/ $_{\mu S}$, V_{DD} \leq BV $_{DSS}$, starting T_{J} = 25°C. 4. Essentially independent of operating temperature.

Typical Characteristics

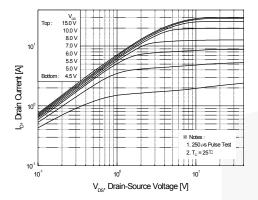


Figure 1. On-Region Characteristics

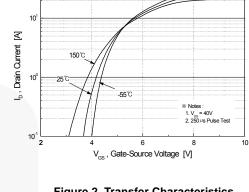


Figure 2. Transfer Characteristics

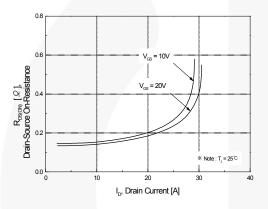


Figure 3. On-Resistance Variation vs. **Drain Current and Gate Voltage**

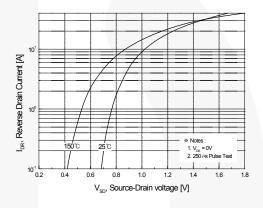


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

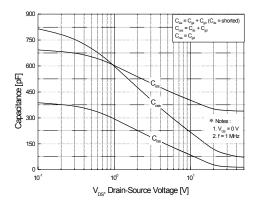


Figure 5. Capacitance Characteristics

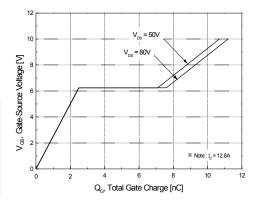


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

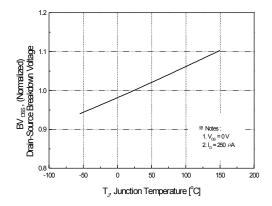
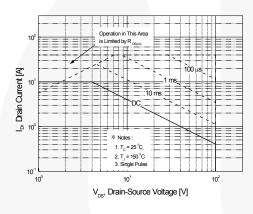


Figure 7. Breakdown Voltage Variation vs. Temperature





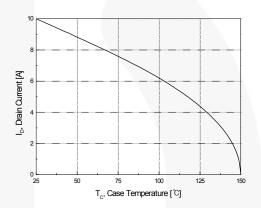


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

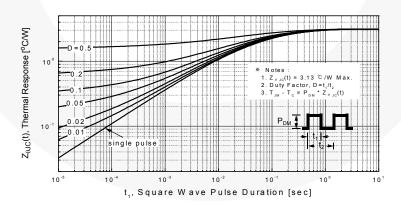


Figure 11. Transient Thermal Response Curve

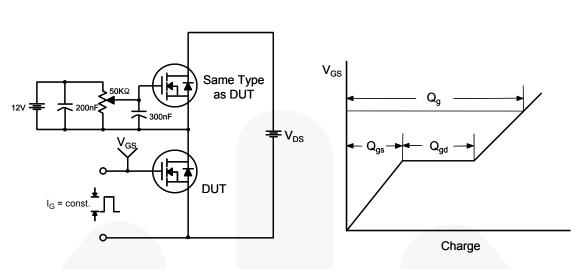


Figure 12. Gate Charge Test Circuit & Waveform

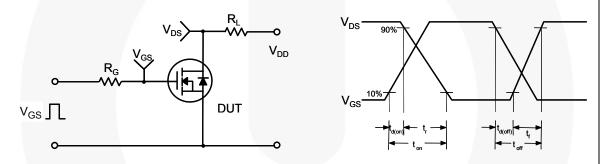


Figure 13. Resistive Switching Test Circuit & Waveforms

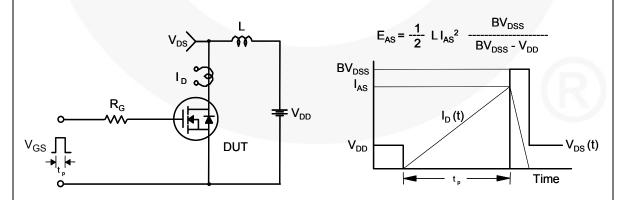
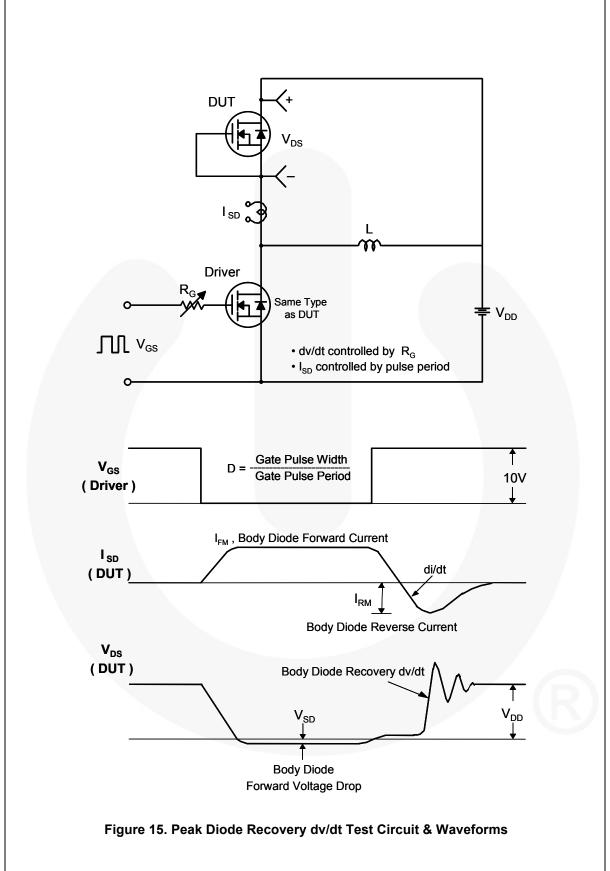


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

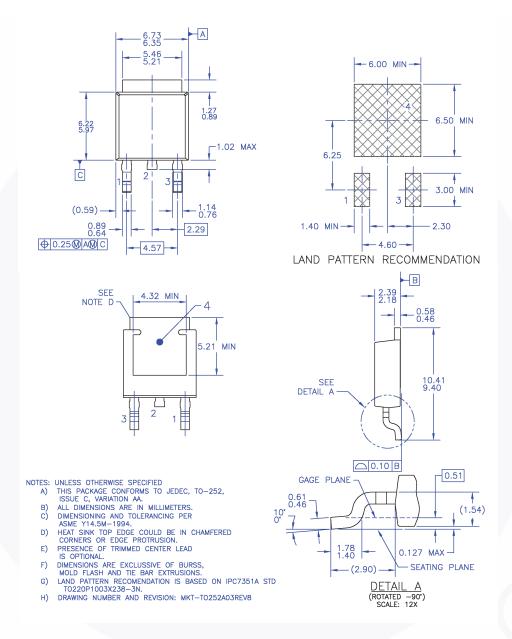


Figure 16. TO252 (D-PAK), Molded, 3-Lead, Option AA&AB

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