**Product data sheet** 

## 1. General description

Planar passivated Silicon Controlled Rectifier (SCR) in a TO263 (D2PAK) plastic package intended for use in applications requiring high thermal cycling performance and high junction temperature capability ( $T_{i(max)} = 150$  °C).

### 2. Features and benefits

- High junction operating temperature capability
- High thermal cycling performance
- High voltage capability
- · Planar passivated for voltage ruggedness and reliability
- · High bidirectional blocking voltage capability
- Surface mountable package
- Very high current surge capability

## 3. Applications

- Ignition circuits
- Motor control
- Protection circuits e.g. SMPS inrush current
- Voltage regulation
- Crowbar protection

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{DRM}$	repetitive peak off- state voltage		-	-	600	V
$V_{RRM}$	repetitive peak reverse voltage		-	-	600	V
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 10 ms; Fig. 4; Fig. 5	-	-	180	А
		half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 8.3 ms	_	-	198	Α
T <sub>j</sub>	junction temperature		-	-	150	°C
$I_{T(AV)}$	average on-state current	half sine wave; T <sub>mb</sub> ≤ 133 °C; <u>Fig. 1</u>	_	-	10.2	Α
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; $T_{mb} \le 133 \text{ °C}$ ; Fig. 2; Fig. 3	-	-	16	Α

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Static characte	Static characteristics							
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C}; Fig. 7$		1.5	-	6	mA	
Dynamic chara	Dynamic characteristics							
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 125 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit		300	-	-	V/µs	

# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		A K
2	А	anode	r	G sym037
3	G	gate		Symosi
mb	mb	mounting base; connected to anode		
			D2PAK (TO263N)	

# 6. Ordering information

#### **Table 3. Ordering information**

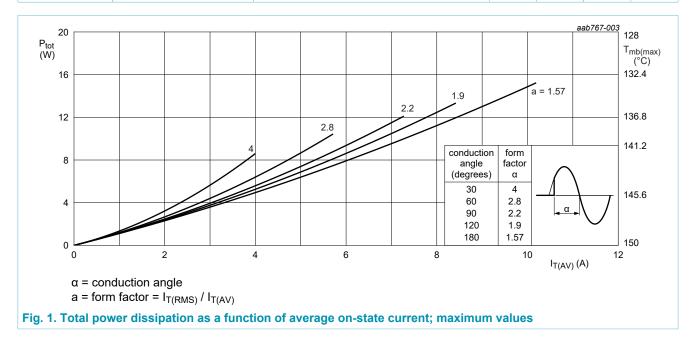
Type number	Package				
	Name	Description	Version		
TYN16B-600CT	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	TO263N		

## 7. Limiting values

## **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	600	V
$V_{RRM}$	repetitive peak reverse voltage		-	600	V
I <sub>T(AV)</sub>	average on-state current	half sine wave; T <sub>mb</sub> ≤ 133 °C; <u>Fig. 1</u>	-	10.2	Α
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; $T_{mb} \le 133 ^{\circ}\text{C}$ ; $\overline{\text{Fig. 2}}$ ; $\overline{\text{Fig. 3}}$	-	16	Α
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 10 ms; Fig. 4; Fig. 5	-	180	Α
		half sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 8.3 ms	-	198	Α
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; SIN	-	162	A²s
dl <sub>T</sub> /dt	rate of rise of on-state current	I <sub>G</sub> = 30 mA	-	50	A/µs
I <sub>GM</sub>	peak gate current		-	4	Α
$V_{RGM}$	peak reverse gate voltage		-	5	V
P <sub>GM</sub>	peak gate power		-	10	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	1	W
T <sub>stg</sub>	storage temperature		-40	150	°C
T <sub>j</sub>	junction temperature		-	150	°C



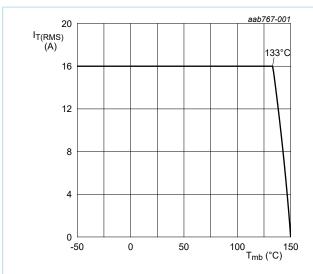


Fig. 2. RMS on-state current as a function of mounting base temperature; maximum values

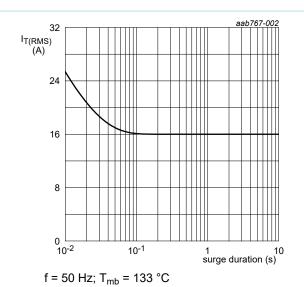


Fig. 3. RMS on-state current as a function of surge duration; maximum values

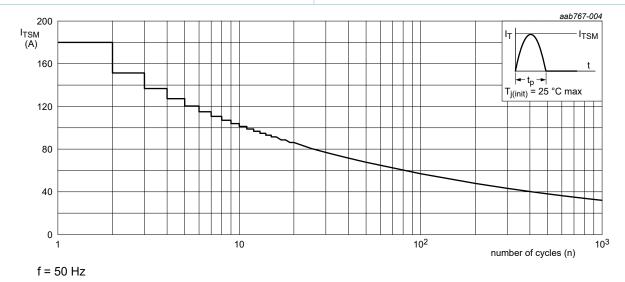
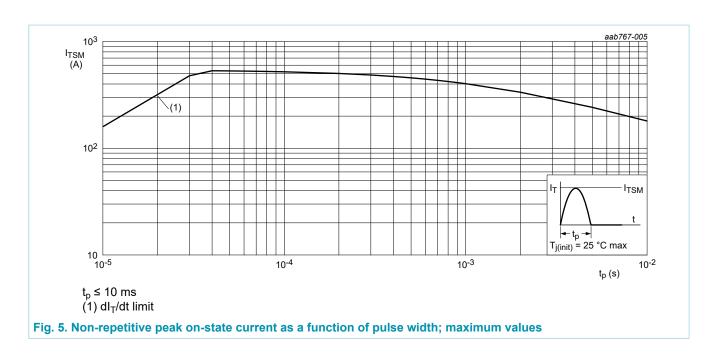


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



## 8. Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	<u>Fig. 6</u>	-	-	1.1	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	mounted on a minimum footprint FR4 board	-	55	-	K/W

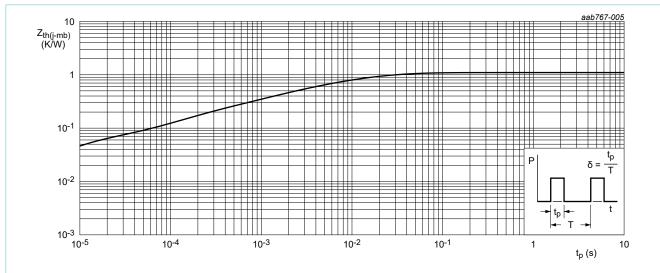


Fig. 6. Transient thermal impedance from junction to mounting base as a function of pulse duration

### 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics		'			
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C}; Fig. 7$	1.5	-	6	mA
IL	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}; Fig. 8$	-	-	60	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	-	40	mA
$V_{T}$	on-state voltage	I <sub>T</sub> = 32 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.2	1.6	V
V <sub>GT</sub>	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.7	1.3	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 150 \text{ °C};$ Fig. 11	0.2	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 600 V; T <sub>j</sub> = 150 °C	-	-	1	mA
I <sub>R</sub>	reverse current	V <sub>R</sub> = 600 V; T <sub>j</sub> = 150 °C	-	-	1	mA
Dynamic ch	naracteristics		'			
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 125 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit	300	-	-	V/µs
		$V_{DM}$ = 402 V; $T_j$ = 150 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit	100	-	-	V/µs

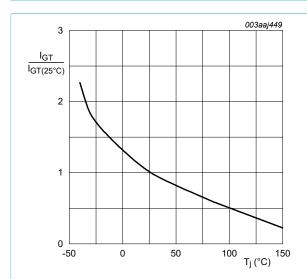


Fig. 7. Normalized gate trigger current as a function of junction temperature

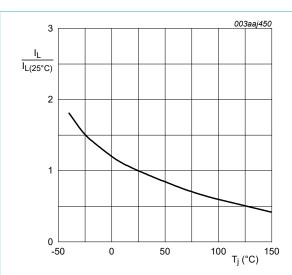


Fig. 8. Normalized latching current as a function of junction temperature

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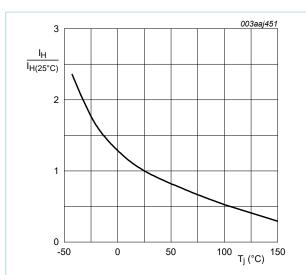
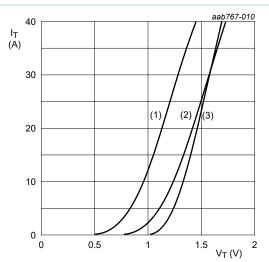


Fig. 9. Normalized holding current as a function of junction temperature



 $V_o = 1.071 \text{ V}; R_s = 0.0169 \Omega$ 

(1) T<sub>j</sub> = 150 °C; typical values (2) T<sub>j</sub> = 150 °C; maximum values

(3)  $T_j = 25$  °C; maximum values

Fig. 10. On-state current as a function of on-state voltage

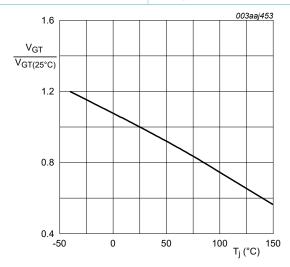
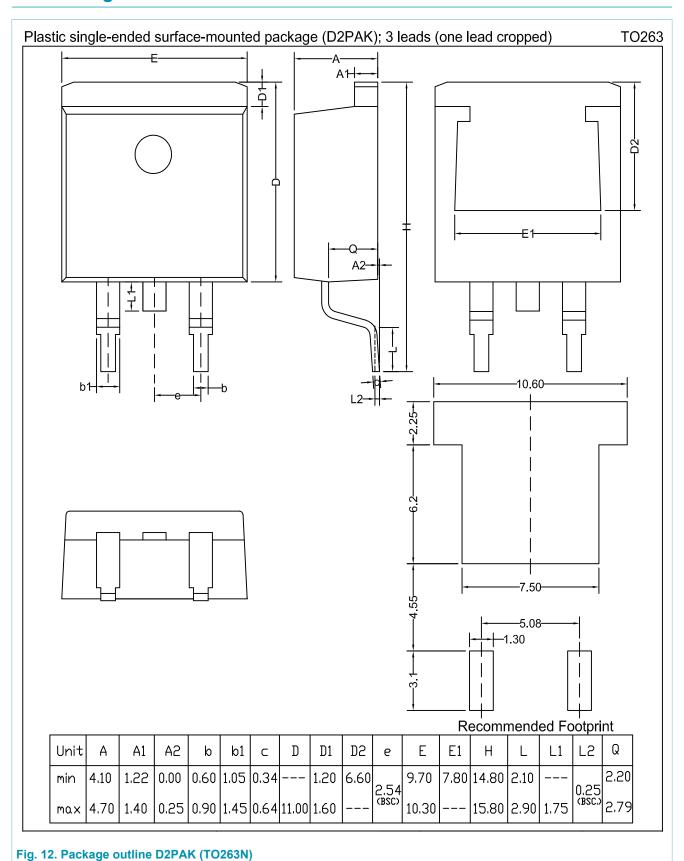


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

## 10. Package outline



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