R1524N033B-EV

200 mA 36 V Input Ultra Low Supply Current VR Evaluation **Board**

No. EEV-332-N033B-181225

R1524N033B-EV is the evaluation board for R1524 which has the below features, benefits and specifications.

OUTLINE

The R1524N is an ultra-low supply current voltage regulator featuring 200 mA output current and 36 V input voltage. This device consists of an Output Short-circuit Protection Circuit, an Over-current Protection Circuit, and a Thermal Shutdown Circuit in addition to the basic regulator circuits. The operating temperature range is from -40°C to 105°C, and the maximum input voltage is 36 V. All these features allow the R1524N to become an ideal power source of electric home appliances. The output voltages are internally fixed at either of the following: 1.8 V, 2.5 V, 2.8 V, 3.0 V, 3.3 V, 3.4 V, 5.0 V, 5.5 V, 6.0 V, 6.4 V, 8.0 V, 8.5 V and 9.0 V. The output voltage accuracy is ±0.6%.

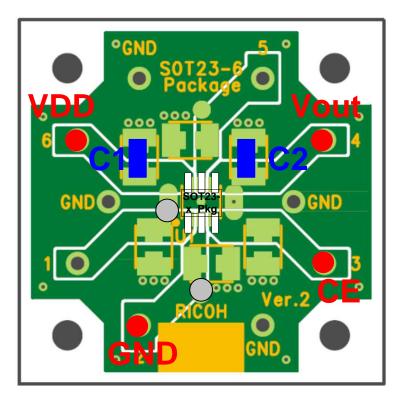
KEY SPECIFICATIONS

•	Input Voltage Range (Maximum Rating) ······	3.5 V to 36 V (50 V)
•	Operating Temperature Range ·······	
•	Supply Current	
•	Standby Current ·····	Typ. 0.1 μA
•	Dropout Voltage ······	Typ. 0.6 V (I _{OUT} = 200 mA, V _{OUT} = 5.0 V)
•	Output Voltage Range ······	1.8 V / 2.5 V / 2.8 V / 3.0 V / 3.3 V / 3.4V /
		5.0 V / 5.5 V / 6.0 V / 6.4 V / 8.0 V / 8.5 V / 9.0 V
		*Contact Ricoh sales representatives for other voltages.
•	Output Voltage Accuracy ······	±0.6% (Ta = 25°C)
•	Output Voltage Temperature-Drift Coefficient ········	Typ. ±60 ppm/°C
•	Line Regulation ·····	Typ. $0.01\%/V$ (V _{SET} + 1 V \leq V _{IN} \leq 36 V)
•	Built-in Output Short-circuit Protection Circuit ·······	Typ. 80 mA
•	Built-in Over-current Protection Circuit ······	Typ. 350 mA
•	Built-in Thermal Shutdown Circuit ······	Thermal Shutdown Temperature: Typ. 160°C
•	Ceramic capacitors are recommended	
	to be used with this device	$C_{OUT} = 0.1 \mu F$ or more
•	Package ·····	SOT-23-5
•	For more details on R1524 IC, please refer to	

https://www.e-devices.ricoh.co.jp/en/products/power/vr_ldo/r1524/r1524-ea.pdf.

PCB LAYOUT

R1524N (Package: SOT-23-5) PCB Layout



:Jumper

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

Symbol	Item		Rating	Unit
VIN	Input Voltage		-0.3 to 50	V
VIN	Peak Input Voltage ⁽¹⁾		60	V
Vce	Input Voltage (CE Pin)		-0.3 to 50	V
Vout	Output Voltage		-0.3 to $V_{IN} + 0.3 \le 50$	V
Іоит	Output Current		300	mA
P _D	Power Dissipation ⁽²⁾ (JEDEC STD.51-7 Test Land Pattern)	SOT-23-5	660	mW
Tj	Junction Temperature Range		-40 to 125	°C
Tstg	Storage Temperature Range		-55 to 125	°C

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

RECOMMENDED OPERATING CONDITIONS

Recommended Operating Conditions

Symbol	ltem	Rating	Unit
V _{IN} Input Voltage		3.5 to 36	V
Ta Operating Temperature Range		-40 to 105	°C

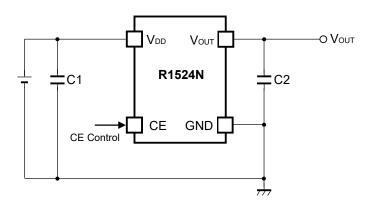
RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

⁽¹⁾ Duration time: 200 ms

⁽²⁾ Refer to POWER DISSIPATION for detailed information.

TYPICAL APPLICATION



R1524N Typical Application

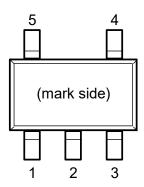
%Although C2 operates even at 0.1 μ F, C2 = 10 μ F is recommended to improve transient characteristics.

Recommended External Components*1

Symbol	Value
C1	0.1 µF
C2	10 µF

^{*1} The bill of materials will be attached on the shipment of each purchased evaluation board.

PIN DESCRIPTION



SOT-23-5 Pin Configuration

SOT-23-5 Pin Description

Pin No.	Symbol	Description
1	GND ⁽¹⁾	Ground Pin
2	GND ⁽¹⁾	Ground Pin
3	CE	Chip Enable Pin (Active-high)
4	Vouт	Output Pin
5	V _{DD}	Input Pin

⁽¹⁾ The GND pin must be wired together when it is mounted on board.

TECHNICAL NOTES

Phase Compensation

In the R1524N, phase compensation is provided to secure stable operation even when the load current is varied. For this purpose, make sure to use $0.1 \, \mu F$ or more of a capacitor (C2).

In case of using a tantalum type capacitor and the ESR (Equivalent Series Resistance) value of the capacitor is large, the output might be unstable. Evaluate the circuit including consideration of frequency characteristics. Connect 0.1 μ F or more of a capacitor (C1) between V_{DD} and GND, and as close as possible to the pins.

PCB Layout

For SOT-23-5 package type, wire the following GND pins together: No. 1 and No. 2.



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Sales & Support Offices

Ricoh Electronic Devices Co., Ltd.

Shin-Yokohama Office (International Sales)
2-3, Shin-Yokohama 3-chome, Kohoku-ku, Yokohama-shi, Kanagawa, 222-8530, Japan
Phone: +81-50-3814-7687 Fax: +81-45-474-0074

Ricoh Americas Holdings, Inc

way, Suite 200 Campbell, CA 95008, U.S.A. 675 Campbell Technology Part Phone: +1-408-610-3105

Ricoh Europe (Netherlands) B.V.

Semiconductor Support Centre

Prof. W.H. Keesomlaan 1, 1183 DJ Amstelveen, The Netherlands Phone: +31-20-5474-309

Ricoh International B.V. - German Branch

Semiconductor Sales and Support Centre Oberrather Strasse 6, 40472 Düsseldorf, Germany

Phone: +49-211-6546-0

Ricoh Electronic Devices Korea Co., Ltd.

3F, Haesung Bldg, 504, Teheran-ro, Gangnam-gu, Seoul, 135-725, Korea Phone: +82-2-2135-5700 Fax: +82-2-2051-5713

Ricoh Electronic Devices Shanghai Co., Ltd.

Room 403, No.2 Building, No.690 Bibo Road, Pu Dong New District, Shanghai 201203, People's Republic of China

Phone: +86-21-5027-3200 Fax: +86-21-5027-3299

Ricoh Electronic Devices Shanghai Co., Ltd. Shenzhen Branch

1205, Block D(Jinlong Building), Kingkey 100, Hongbao Road, Luohu District,

Shenzhen, China Phone: +86-755-8348-7600 Ext 225

Ricoh Electronic Devices Co., Ltd.

Taipei office
Room 109, 10F-1, No.51, Hengyang Rd., Taipei City, Taiwan
Phone: +886-2-2313-1621/1622 Fax: +886-2-2313-1623

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Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург,

Промышленная ул, дом № 19, литера Н,

помещение 100-Н Офис 331