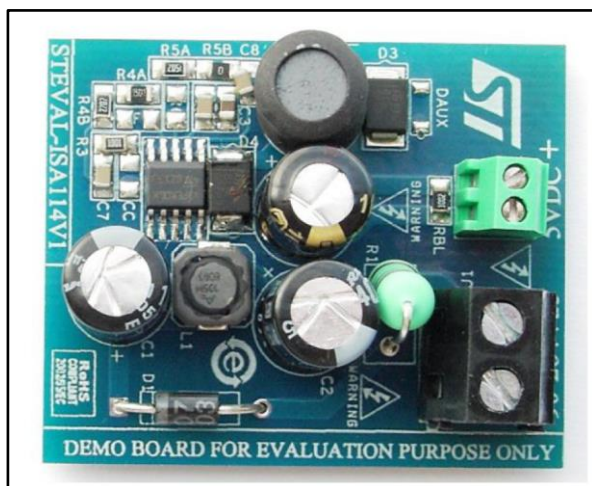


5 V, 160 mA non-isolated buck converter using VIPer™ Plus - VIPER06XS

Data brief



Description

The STEVAL-ISA114V1 evaluation board describes a 5 V- 0.15 A power supply set in buck topology with the VIPer06XS, a new off-line high voltage converter by STMicroelectronics, specifically developed for non-isolated SMPS.

The features of the device include an 800 V avalanche rugged power section, PWM operation at 30 kHz with frequency jittering for lower EMI, limiting current with adjustable set point, on-board soft-start, safe auto-restart after a fault condition and low standby power consumption.

The available protection includes a thermal shutdown with hysteresis, delayed overload protection and open loop failure protection. All protection is auto-restart mode.

Features

- Universal input mains range:
 - input voltage: 90 - 264 V_{AC}
 - frequency: 45 - 65 Hz
- Single-output voltage:
 - 5 V @ 0.16 A continuous operation
- Fully protected against faults (overload, feedback disconnection and overheating)
- EMI: according to EN55022-Class-B
- RoHS compliant

1 Adapter features

The electrical specifications are given in [Table 1: "Electrical specifications"](#), the schematic in [Figure 1: "Circuit schematic"](#), and the bill of material in [Table 2: "Bill of material"](#).

Table 1: Electrical specifications

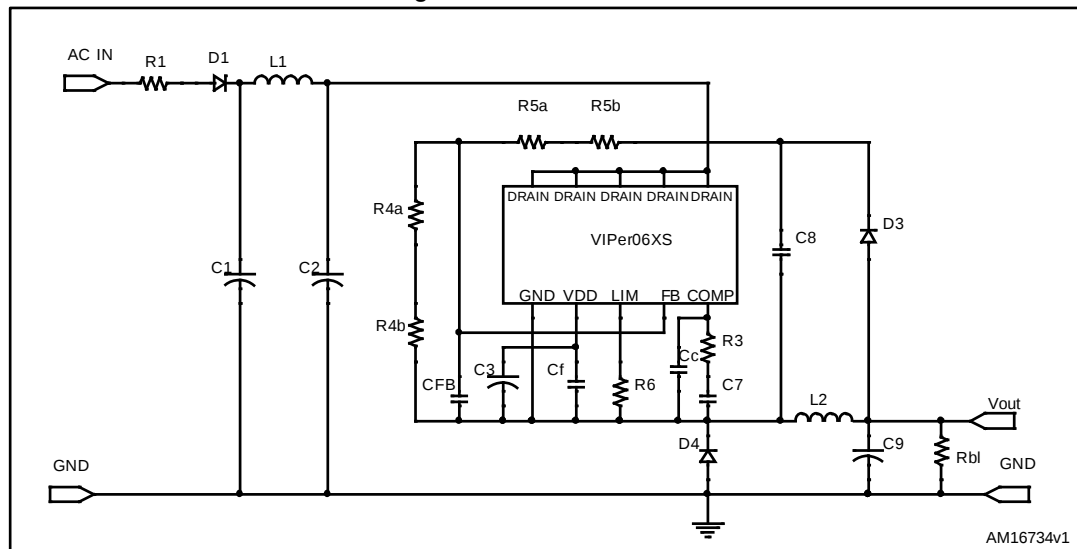
Parameter	Symbol	Value
Input voltage range	V_{IN}	[80 V _{AC} ; 265 V _{AC}]
Output voltage	V_{OUT}	5 V
Max. output current	I_{OUT}	0.16 A
Precision of output regulation	ΔV_{OUT_LF}	± 5%
High frequency output voltage ripple	ΔV_{OUT_HF}	50 mV
Max. ambient operating temperature	T_{AMB}	60 °C

Table 2: Bill of material

Name	Value	Description	Footprint	Manufacturer
C1	2.2 μ F, 400 V	Electrolytic capacitor		Saxon
C2	2.2 μ F, 400 V	Electrolytic capacitor		Saxon
C3	2.2 μ F, 25 V	Ceramic capacitor	SMD: 0805	Murata
CFB	Not mounted	Ceramic capacitor	SMD: 0805	
Cf	100 nF, 50 V	Ceramic capacitor	SMD: 0805	Murata
Cc	Not mounted	Ceramic capacitor	SMD: 0805	
C7	22 nF, 25 V	Ceramic capacitor	SMD: 0805	Murata
C8	100 nF, 50 V	Ceramic capacitor	SMD: 0805	Murata
C9	100 μ F, 25 V	Electrolytic capacitor		Rubycon, ZL series
D1	1N4007	High voltage rectifier	DO-41	Fairchild
D3	STTH1L06	High voltage ultra fast rectifier	SMB (SOD87)	ST
D4	STTH1L06	High voltage ultra fast rectifier	SMB (SOD87)	ST
Daux	Not mounted	Small signal diode		
IC	VIPER06XS	High voltage converter	SSO-10	ST
L1	1 mH	Input filter inductor	SMD	Epcos
L2	RFB0810-681	0.68 mH power inductor		Coilcraft
R1	22 ohm	1 W resistor		Panasonic
R3	1 kohm, 1%	1/4 W resistor	SMD: 0805	Panasonic
R4a	1.5 kohm, 1%	1/4 W resistor	SMD: 0805	Panasonic
R4b	22 kohm	1/4 W resistor	SMD: 0805	
R5a	15 kohm	1/4 W resistor	SMD: 0805	
R5b	0 ohm, 1%	1/4 W resistor	SMD: 0805	Panasonic

Name	Value	Description	Footprint	Manufacturer
R6	Not mounted	1/4 W resistor	SMD: 0805	
Rbl	10 kohm, 1%	1/4 W resistor	SMD: 0805	Panasonic

Figure 1: Circuit schematic



The image shows the PCB layout for the STEVAL-ISA114V1. The board is populated with various components including resistors (R1, R3, R4A, R4B, R5A, R5B), capacitors (C1, C2, C3, C4, C7), inductors (L1, L2), and a diode (D1). A large integrated circuit (IC) is mounted in the center. The layout includes a power input section at the top with a 90-264Vac transformer and a 5VDC+ output. A DAUX connector is located on the left side. The board is labeled with 'ST' (STMicroelectronics) and 'STEVAL-ISA114V1'. A RoHS compliance logo is present in the bottom right corner. The text 'DEMO BOARD FOR EVALUATION PURPOSE ONLY' is printed vertically along the right edge.

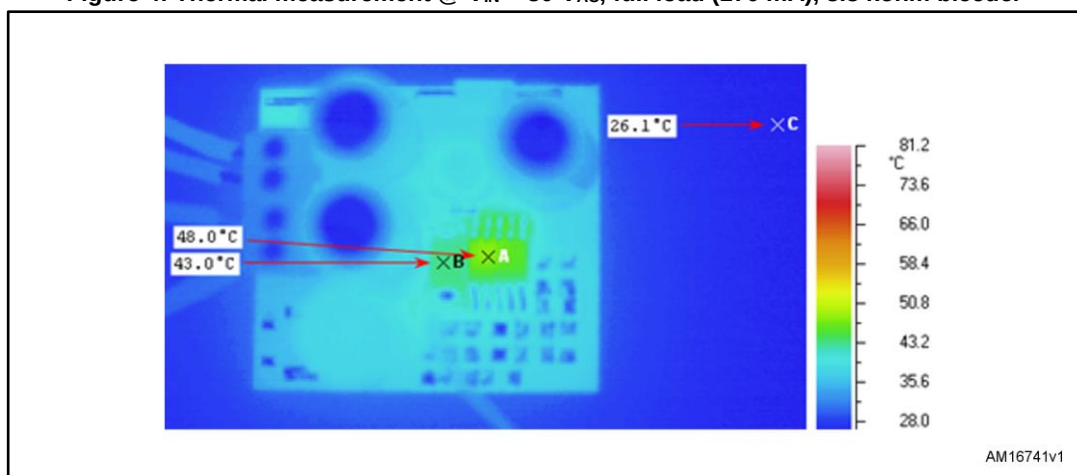
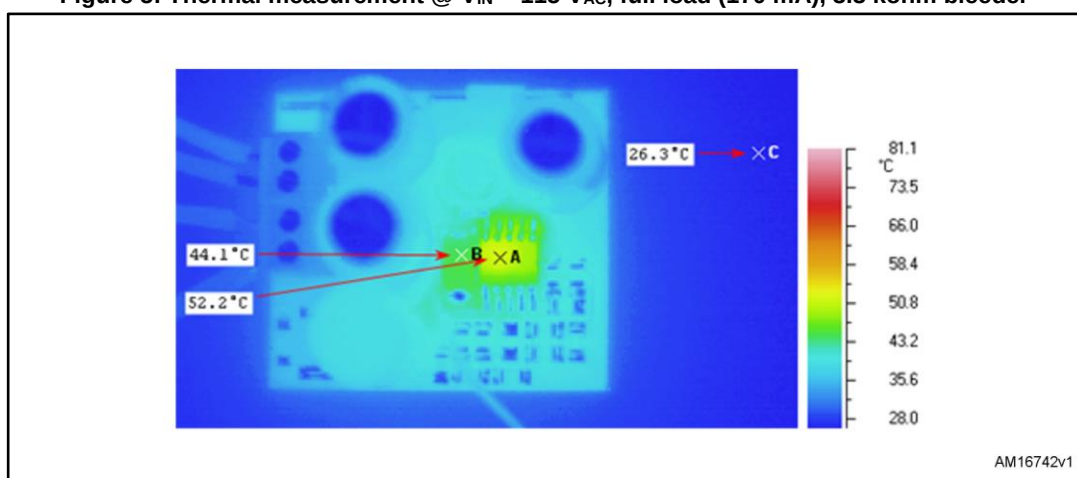
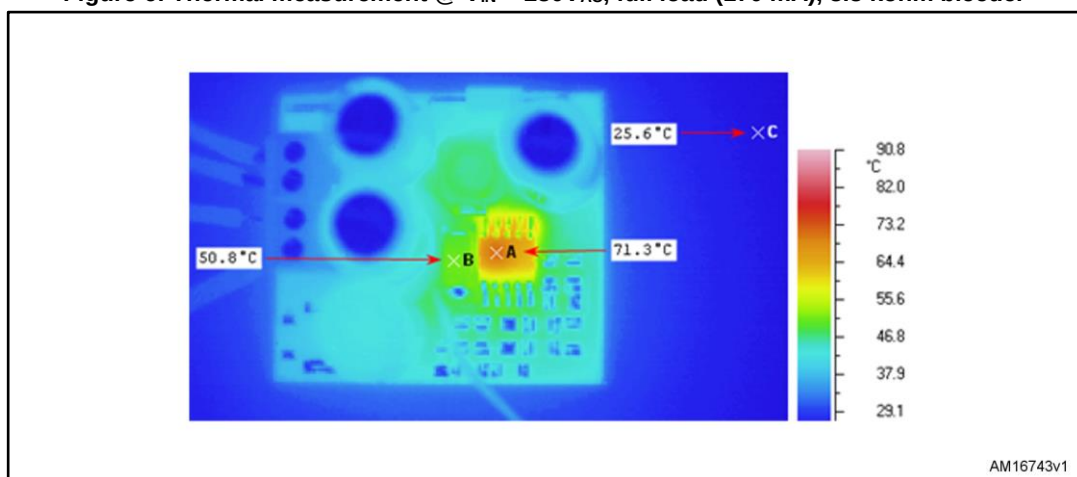
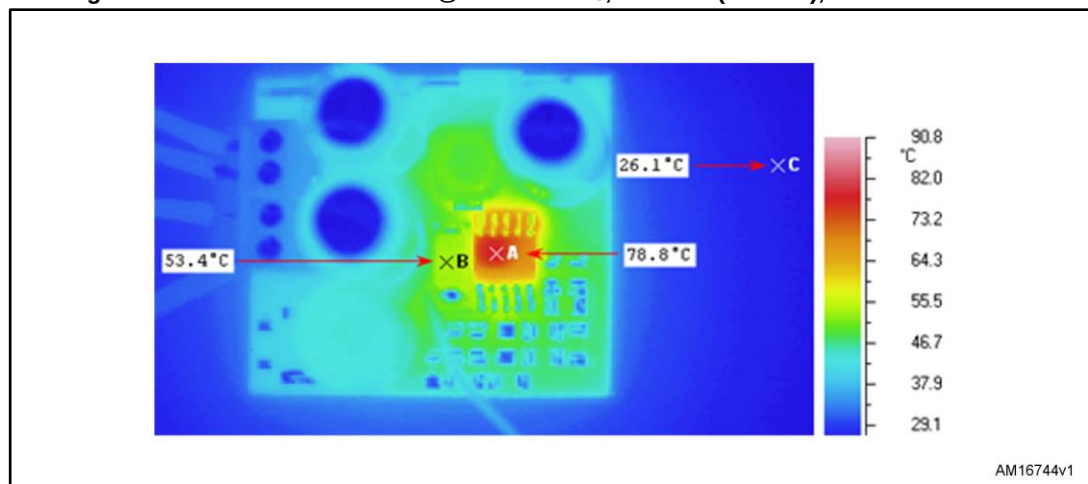
Figure 4: Thermal measurement @ $V_{IN} = 80\text{ V}_{AC}$, full load (170 mA), 3.3 kohm bleederFigure 5: Thermal measurement @ $V_{IN} = 115\text{ V}_{AC}$, full load (170 mA), 3.3 kohm bleederFigure 6: Thermal measurement @ $V_{IN} = 230\text{ V}_{AC}$, full load (170 mA), 3.3 kohm bleeder

Figure 7: Thermal measurement @ $V_{IN} = 265V_{AC}$, full load (170 mA), 3.3 kohm bleeder

3 Revision history

Table 3: Document revision history

Date	Version	Changes
24-Jul-2013	1	Initial release.
06-Sep-2016	2	Updated board photo on the cover page.

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Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург,
Промышленная ул, дом № 19, литера Н,
помещение 100-Н Офис 331