

Silicon Carbide Diode 4 January 2017

Product data sheet

1. General description

Silicon Carbide Schottky diode designed for high frequency switched mode power supplies in a TO252 (DPAK) plastic package.

2. Features and benefits

- Highly stable switching performance
- High forward surge capability I_{FSM}
- Extremely fast reverse recovery time
- Superior in efficiency to Silicon Diode alternatives
- Reduced losses in associated MOSFET
- Reduced EMI
- Reduced cooling requirements
- RoHS compliant

3. Applications

- Power factor correction
- Telecom/Server SMPS
- UPS
- PV inverter
- PC Silverbox
- LED/OLED TV
- Motor Drives

4. Quick reference data

Table 1. Qui	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{RRM}	repetitive peak reverse voltage		-	-	650	V
I _{F(AV)}	average forward current	δ = 0.5 ; T _{mb} ≤ 113 °C; square-wave pulse; <u>Fig. 1; Fig. 2; Fig. 3; Fig. 4</u>	-	-	10	A
Tj	junction temperature		-	-	175	°C
Static chara	acteristics	· · · · · ·				
V _F	forward voltage	I _F = 10 A; T _j = 25 °C; <u>Fig. 6</u>	-	1.5	1.7	V
		I _F = 10 A; T _j = 150 °C; <u>Fig. 6</u>	-	1.8	2.1	V
Dynamic ch	naracteristics	· · · · ·			·	
Q _r	recovered charge	I_F = 10 A; dI _F /dt = 500 A/µs; V _R = 400 V; T _j = 25 °C; <u>Fig. 7</u>	-	15	-	nC

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5. Pinning information

Table 2. F	Pinning inf	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	n.c.	not connected	mb	K A
2	К	cathode[1]		001aaa020
3	А	anode		
mb	К	mounting base; connected to cathode	DPAK (TO252NS)	

[1] It is not possible to connect to pin 2 of the TO252 package.

6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
NXPSC10650D	DPAK	plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)	TO252NS				

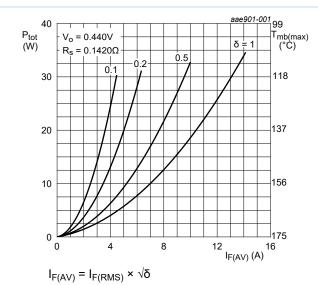
NXPSC10650D

7. Limiting values

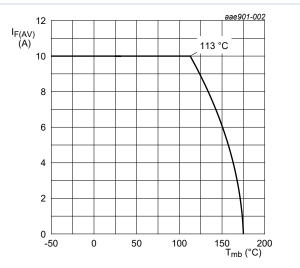
Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{RRM}	repetitive peak reverse voltage		-	650	V
V _{RWM}	crest working reverse voltage		-	650	V
V _R	reverse voltage	DC	-	650	V
I _{F(AV)}	average forward current	δ = 0.5; T _{mb} ≤ 113 °C; square-wave pulse; Fig. 1; Fig. 2; Fig. 3; Fig. 4	-	10	A
I _{FRM}	repetitive peak forward current	δ = 0.5 $\ ; t_p$ = 25 $\mu s;$ square-wave pulse	-	20	A
I _{FSM}	non-repetitive peak	t_p = 10 ms; $T_{j(init)}$ = 25 °C; sine-wave pulse	-	50	А
	forward current	t _p = 10 μs; T _{j(init)} = 25 °C; square-wave pulse	-	450	A
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-	175	°C





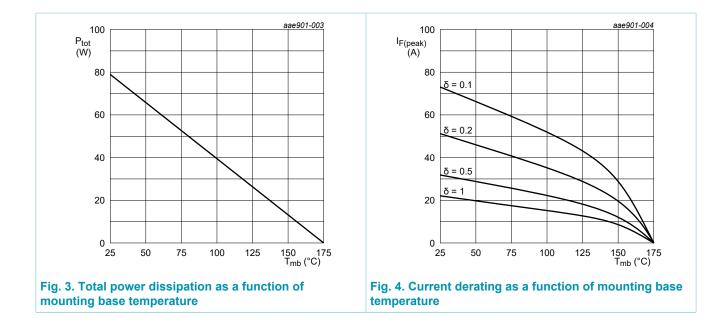




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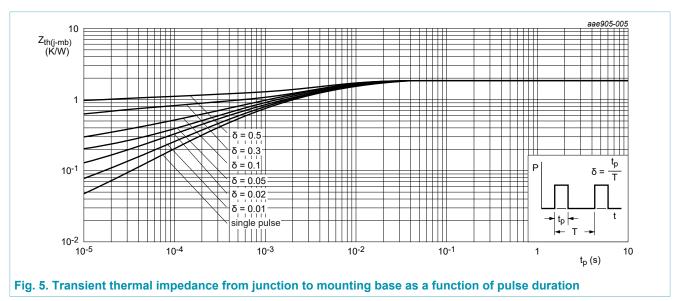


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8. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	<u>Fig. 5</u>	-	-	1.9	K/W
R _{th(j-a)}	thermal resistance from junction to ambient free air	Device mounted on an FR4 Printed- Circuit Board	-	50	-	K/W

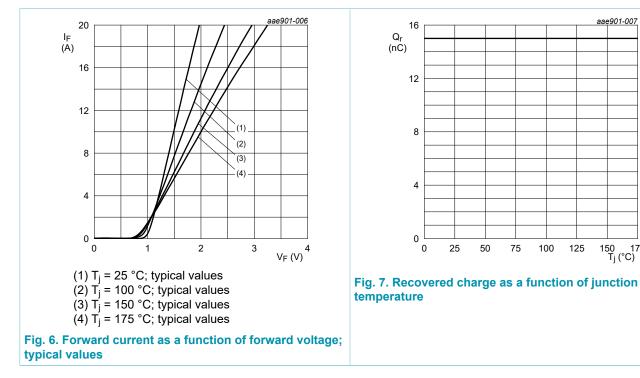


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9. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static chara	acteristics		· · · ·				
V _F	forward voltage	I _F = 10 A; T _j = 25 °C; <u>Fig. 6</u>		-	1.5	1.7	V
		I _F = 10 A; T _j = 150 °C; <u>Fig. 6</u>		-	1.8	2.1	V
I _R	reverse current	V _R = 650 V; T _j = 25 °C		-	-	250	μA
		V _R = 650 V; T _j = 150 °C		-	-	800	μA
Dynamic ch	naracteristics						
Q _r	recovered charge	$I_F = 10 \text{ A}; \text{ d}I_F/\text{d}t = 500 \text{ A}/\mu\text{s};$ $V_R = 400 \text{ V}; \text{ T}_j = 25 ^\circ\text{C}; \text{ Fig. 7}$		-	15	-	nC
C _d	diode capacitance	f = 1 MHz; V _R = 1 V; T _j = 25 °C		-	300	-	pF
		f = 1 MHz; V _R = 300 V; T _j = 25 °C		-	34	-	pF
		f = 1 MHz; V _R = 600 V; T _i = 25 °C		-	28	-	pF



50

75

100

125

150 175 T_i (°C)

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10. Package outline

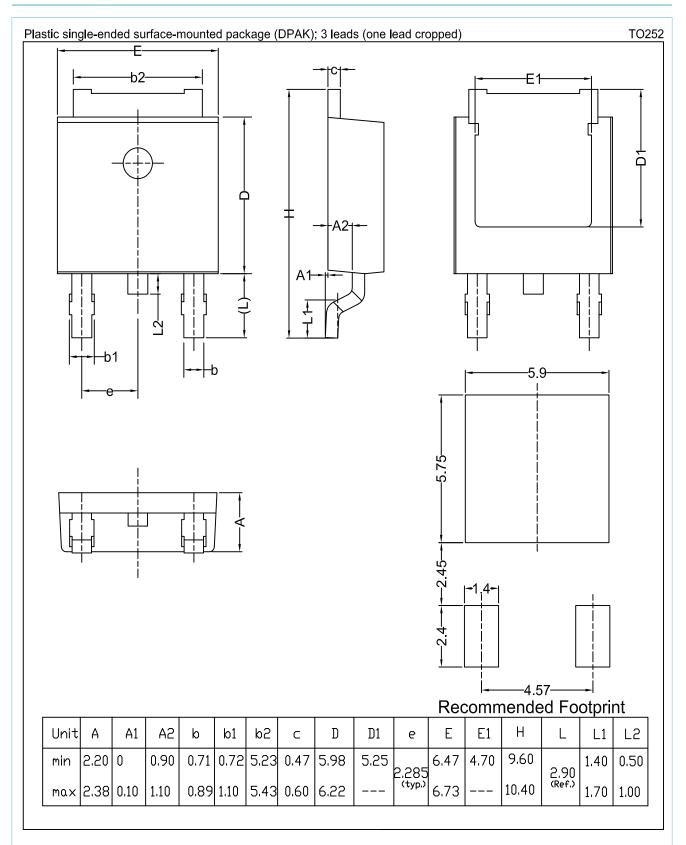


Fig. 8. Package outline DPAK (TO252NS)

Product data sheet

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11. Legal information

Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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