7UL1G02FS

CMOS Digital Integrated Circuits Silicon Monolithic

7UL1G02FS

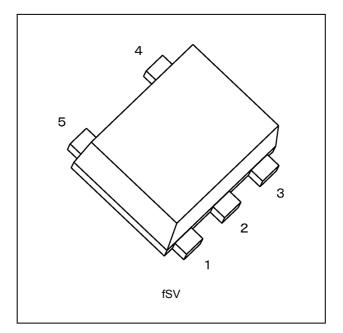
1. Functional Description

• 2-Input NOR Gate

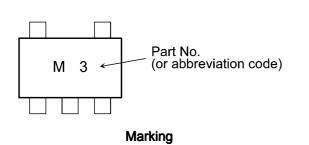
2. Features

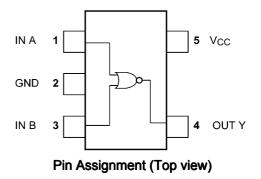
- (1) High output current: ± 8.0 mA (min) at V_{CC} = 3.0 V
- (2) Super high speed operation: t_{pd} = 2.5 ns (typ.) at V_{CC} = 3.3 V, C_{L} = 15 $\rm pF$
- (3) Operation voltage range: $V_{CC} = 0.9$ to 3.6 V
- (4) 3.6 V tolerant inputs
- (5) 3.6 V power down protection output

3. Packaging



4. Marking and Pin Assignment





Start of commercial production 2018-02 2017-12-21 Rev.1.0

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5. IEC Logic Symbol



6. Truth Table

A	В	Y
L	L	Н
L	Н	L
Н	L	L
н	Н	L

7. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 4.6	V
Input voltage	V _{IN}		-0.5 to 4.6	V
DC output voltage	V _{OUT}	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-20	mA
Output diode current	Ι _{ΟΚ}	(Note 3)	-20	mA
DC output current	I _{OUT}		±25	mA
V _{CC} /ground current	I _{CC}		±50	mA
Power dissipation	PD		50	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{CC} = 0 V

Note 2: High (H) or Low (L) state. I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

8. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V _{CC}		—	0.9 to 3.6	V
Input voltage	V _{IN}		_	0 to 3.6	V
Output voltage	V _{OUT}	(Note 1)	_	0 to 3.6	V
		(Note 2)	—	0 to V _{CC}	
Output current	I _{OH} ,I _{OL}		V _{CC} = 3.0 to 3.6 V	±8.0	mA
			V _{CC} = 2.3 to 2.7 V	±4.0	
			V _{CC} = 1.65 to 1.95 V	±3.0	
			V _{CC} = 1.4 to 1.6 V	±1.7	
			V _{CC} = 1.1 to 1.3 V	±0.3	
			V _{CC} = 0.9 V	±0.02	
Operating temperature	T _{opr}		—	-40 to 85	°C
Input rise and fall time	dt/dv		V_{IN} = 0.8 to 2.0 V, V_{CC} = 3.0 V	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 1: $V_{CC} = 0 V$

Note 2: High (H) or Low (L) state.

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9. Electrical Characteristics

9.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Condition	1	V _{CC} (V)	Min	Тур.	Max	Unit
High-level input voltage	VIH			0.9	V _{CC}	_	—	V
				1.1 to 1.3	$V_{CC} \times 0.70$	_	_	1
				1.4 to 1.6	$V_{CC} imes 0.65$	_	—	1
				1.65 to 1.95	$V_{CC} imes 0.65$	_	_	1
				2.3 to 2.7	1.7		_	1
				3.0 to 3.6	2.0	_	_]
Low-level input voltage	VIL	—		0.9	_	_	GND	V
				1.1 to 1.3	_		$V_{CC} \times 0.30$	1
				1.4 to 1.6	_	_	$V_{CC} \times 0.35$	
				1.65 to 1.95	_	_	$V_{CC} \times 0.35$	1
				2.3 to 2.7	_		0.7	1
				3.0 to 3.6	_	_	0.8	
High-level output voltage V _{OH}	V _{OH}	$V_{IN} = V_{IL}$	I _{OH} = -0.02 mA	0.9	0.75	_	_	V
			I _{OH} = -0.3 mA	1.1 to 1.3	$V_{CC} imes 0.75$	_	_]
			I _{OH} = -1.7 mA	1.4 to 1.6	$V_{CC} imes 0.75$	_	_]
			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	_	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	_]
			I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	—	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 0.02 mA	0.9	—	_	0.1	V
			I _{OL} = 0.3 mA	1.1 to 1.3	—	_	$V_{CC} \times 0.25$	
			I _{OL} = 1.7 mA	1.4 to 1.6	—	_	$V_{CC} \times 0.25$	
			I _{OL} = 3.0 mA	1.65 to 1.95	—		0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7	—		0.4	
			I _{OL} = 8.0 mA	3.0 to 3.6	—		0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		0 to 3.6	—	_	±0.1	μA
Power-OFF leakage current	I _{OFF}	V _{IN} = 0 to 3.6 V, V _{OUT} = 0 to 3.6 V		0	—	—	1.0	μA
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		3.6	—		1.0	μA

9.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Condition	n	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	—	0.9	V _{CC}	—	V	
				1.1 to 1.3	$V_{CC} imes 0.70$	—	
				1.4 to 1.6	$V_{CC} imes 0.65$	_	
				1.65 to 1.95	$V_{CC} imes 0.65$	_	
				2.3 to 2.7	1.7	_	
				3.0 to 3.6	2.0	_	
Low-level input voltage	VIL	—		0.9	—	GND	V
				1.1 to 1.3	_	$V_{CC} \times 0.30$	
				1.4 to 1.6	—	$V_{CC} \times 0.35$	
				1.65 to 1.95	—	$V_{CC} \times 0.35$	
				2.3 to 2.7	_	0.7	
				3.0 to 3.6	_	0.8	
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -0.02 mA	0.9	0.75	_	V
			I _{OH} = -0.3 mA	1.1 to 1.3	$V_{CC} \times 0.75$	_	
			I _{OH} = -1.7 mA	1.4 to 1.6	$V_{CC} \times 0.75$	_	
			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	—	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	
			I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 0.02 mA	0.9	—	0.1	V
			I _{OL} = 0.3 mA	1.1 to 1.3	—	$V_{CC} \times 0.25$	
			I _{OL} = 1.7 mA	1.4 to 1.6	—	$V_{CC} \times 0.25$	
			I _{OL} = 3.0 mA	1.65 to 1.95	_	0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7	—	0.4	
			I _{OL} = 8.0 mA	3.0 to 3.6	—	0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		0 to 3.6	_	±0.5	μA
Power-OFF leakage current	I _{OFF}	V _{IN} = 0 to 3.6 V, V _{OUT} = 0 to 3.6 V		0	—	10.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		3.6	—	10.0	μA

9.3. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		$R_L = 1 M\Omega$	0.9	10	_	20.7	_	ns
				1.1 to 1.3		_	10.5	18.4	
				1.4 to 1.6		_	6.1	8.5	
				1.65 to 1.95]	_	4.5	6.2	
				2.3 to 2.7		_	3.0	3.9	
				3.0 to 3.6		_	2.3	3.1	
Propagation delay time	t _{PLH} ,t _{PHL}		$R_L = 1 M\Omega$	0.9	15	_	22.9	—	ns
				1.1 to 1.3		_	11.5	21.5	
				1.4 to 1.6		_	6.7	9.3	
				1.65 to 1.95		_	4.9	6.9	
				2.3 to 2.7		_	3.2	4.4	
				3.0 to 3.6		_	2.5	3.4	
Propagation delay time	t _{PLH} ,t _{PHL}		$R_L = 1 M\Omega$	0.9	30	_	30.6	_	ns
				1.1 to 1.3		_	14.8	29.6	
				1.4 to 1.6		_	8.5	13.1	
				1.65 to 1.95		_	6.3	9.2	
				2.3 to 2.7		_	4.3	5.7	
				3.0 to 3.6	1	_	3.3	4.4	
Input capacitance	C _{IN}		_	3.6	_	_	3	_	pF
Power dissipation capacitance	C _{PD}	(Note 1)	—	0.9 to 3.6	—	_	9	—	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

9.4. AC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C, Input: t_r = t_f = 3 ns)

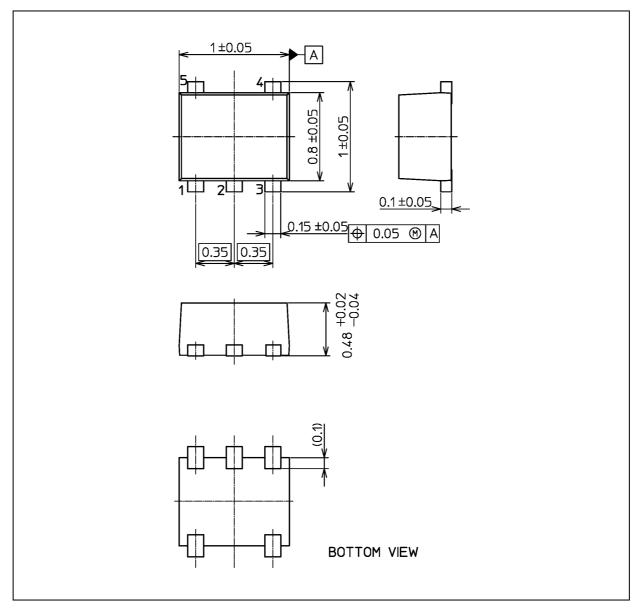
Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	R _L = 1 ΜΩ	0.9	10	_	_	ns
			1.1 to 1.3]	1.0	34.2	
			1.4 to 1.6		1.0	10.0	
			1.65 to 1.95		1.0	6.7	
			2.3 to 2.7]	1.0	4.4	
			3.0 to 3.6		1.0	3.7	
Propagation delay time	t _{PLH} ,t _{PHL}	R _L = 1 ΜΩ	0.9	15		_	ns
			1.1 to 1.3]	1.0	37.2	
			1.4 to 1.6		1.0	11.2	
			1.65 to 1.95		1.0	7.1	
			2.3 to 2.7		1.0	5.0	
			3.0 to 3.6		1.0	3.9	
Propagation delay time	t _{PLH} ,t _{PHL}	R _L = 1 ΜΩ	0.9	30	-	_	ns
			1.1 to 1.3		1.0	56.0	
			1.4 to 1.6		1.0	15.9	
			1.65 to 1.95		1.0	9.6	
			2.3 to 2.7]	1.0	6.1	
			3.0 to 3.6		1.0	4.8	



7UL1G02FS

Package Dimensions

Unit: mm



Weight: 1.0 mg (typ.)

Package Name(s)

Nickname: fSV

TOSHIBA

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