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April 2013

FGH50N3 300 V SMPS IGBT

General Description

Using Fairchild $^{\circledR}$ s planar technology, this IGBT is ideal for many high voltage switching applications operating at high frequencies where low conduction losses are essential. This device has been optimized for medium frequency switch mode power supplies.

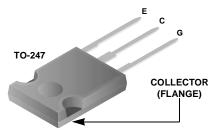
Applications

• SMPS

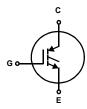
Features

- Low Saturation Voltage: VCE(sat) = 1.4 V max
- Low EOFF = 6.6 uJ/A
- SCWT = 8 us @ = 125 ℃
- 300V Switching SOA Capability
- · Positive Temperature Coefficient above 50 A

Package



Symbol



Device Maximum Ratings T_C= 25°C unless otherwise noted

Symbol	Parameter	Ratings	Unit
BV _{CES}	Collector to Emitter Breakdown Voltage	300	V
I _{C25}	Collector Current Continuous, T _C = 25°C	75	Α
I _{C110}	Collector Current Continuous, T _C = 110°C	75	Α
I _{CM}	Collector Current Pulsed (Note 1)	240	Α
V _{GES}	Gate to Emitter Voltage Continuous	±20	V
V_{GEM}	Gate to Emitter Voltage Pulsed	±30	V
SSOA	Switching Safe Operating Area at T _J = 150°C, Figure 2	150A at 300V	
E _{AS}	Single Pulse Avalanche Energy, I _{CE} = 30A, L = 1.78mH, V _{DD} = 50V	800	mJ
E _{ARV}	Single Pulse Reverse Avalanche Energy, I _{EC} = 30A, L = 1.78mH, V _{DD} = 50V	800	mJ
P _D	Power Dissipation Total T _C = 25°C	463	W
	Power Dissipation Derating T _C > 25°C	3.7	W/°C
T _J	Operating Junction Temperature Range	-55 to 150	°C
T _{STG}	Storage Junction Temperature Range	-55 to 150	°C
t _{SC}	Short Circuit Withstand Time (Note 2)	8	μs

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

NOTE:

^{1.} Pulse width limited by maximum junction temperature.

^{2.} $V_{CE(PK)}$ = 180V, T_J = 125°C, V_{GE} = 12Vdc, R_G = 5Ω

Package Marking and Ordering Information

Dev	vice Marking	Device	Package	Tape Width	Quantity
	FGH50N3	FGH50N3	TO-247	N/A	30

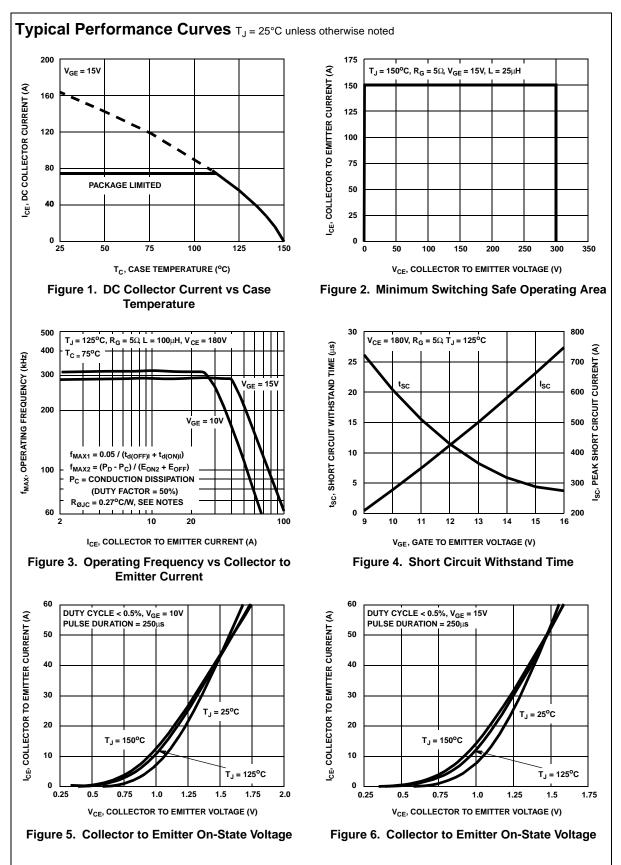
Electrical Characteristics T_J = 25°C unless otherwise noted

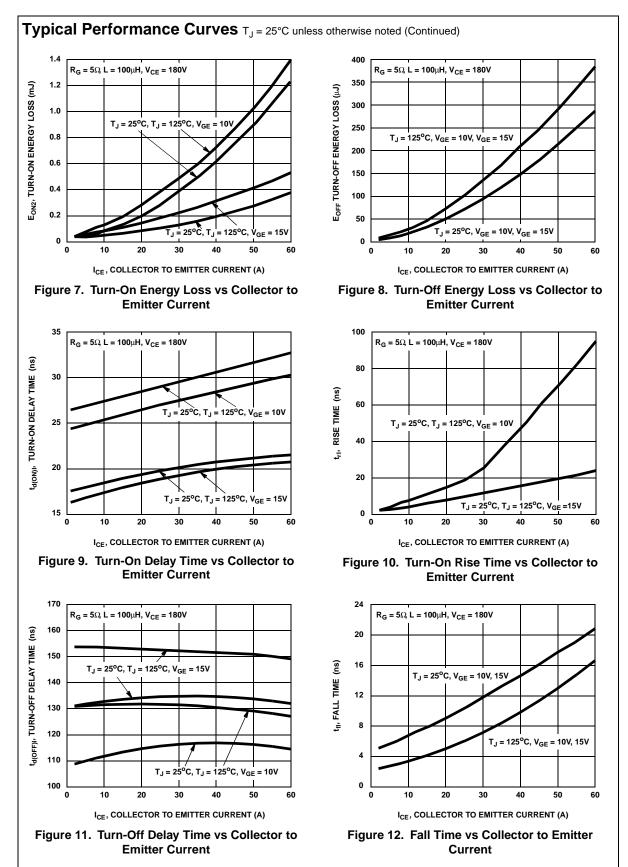
Symbol	Parameter	Test Co	nditions	Min	Тур	Max	Unit
Off State	Characteristics						
BV _{CES}	Collector to Emitter Breakdown Voltage	$I_{CE} = 250 \mu A, V_{CE}$	_{SE} = 0V	300V	-	-	V
BV _{ECS}	Emitter to Collector Breakdown Voltage	$I_{EC} = 10 \text{mA}, V_G$	E = 0V	15V	-	-	V
I _{CES}	Collector to Emitter Leakage Current	V _{CE} = 300V T _J = 25°C		-	-	250	μΑ
			T _J = 125°C	-	-	2.0	mA
I _{GES}	Gate to Emitter Leakage Current	$V_{GE} = \pm 20V$		-	ı	±250	nA
On State	Characteristics						
V _{CE(SAT)}	Collector to Emitter Saturation Voltage	I _{CE} = 30A	T _J = 25°C	-	1.30	1.4	V
OL(OAI)		V _{GE} = 15V	T _{.1} = 125°C	-	1.25	1.4	V
	Characteristics						
Q _{G(ON)}	Gate Charge	I _{CE} = 30A	V _{GE} = 15V	_	180	_	nC
G(ON)	Cate Charge		$V_{GF} = 20V$	-	228	_	nC
V _{GE(TH)}	Gate to Emitter Threshold Voltage	$I_{CE} = 250 \mu A, V_{CE} = V_{GE}$		4.0	4.8	5.5	V
V _{GEP}	Gate to Emitter Plateau Voltage	$I_{CE} = 30A, V_{CE} = 150V$		-	7.0	-	V
SSOA	Switching SOA	$T_J = 150^{\circ}\text{C}, R_G = 5\Omega,$ $V_{GE} = 15V, L = 25\mu\text{H},$		150	-	-	А
-	Current Turn-On Delay Time	Vce = 300V IGBT and Diode at T _{.1} = 25°C,		_	20	_	ns
t _{d(ON)I}	Current Rise Time	IGBT and Diode at T_J = 25 C, I_{CE} = 30A, V_{CE} = 180V, V_{GE} = 15V, R_G = 5 Ω , L = 100 μ H, Test Circuit - Figure 20			15	-	ns
t _{rl}	Current Turn-Off Delay Time				135	<u> </u>	ns
t _{d(OFF)I} t _{fl}	Current Fall Time			_	12	_	ns
E _{ON2}	Turn-On Energy (Note 1)			_	130	_	μJ
E _{OFF}	Turn-Off Energy (Note 2)			_	92	120	μJ
t _{d(ON)I}	Current Turn-On Delay Time	IGBT and Diode at T _{.I} = 125°C,		-	19	-	ns
t _{rl}	Current Rise Time	$I_{CE} = 30A$, $V_{CE} = 180V$, $V_{GE} = 15V$, $R_{G} = 5\Omega$, $L = 100\mu\text{H}$, Test Circuit - Figure 20		_	13	-	ns
t _{d(OFF)I}	Current Turn-Off Delay Time			-	155	190	ns
t _{fl}	Current Fall Time			_	7	15	ns
E _{ON2}	Turn-On Energy (Note 1)			-	225	270	μJ
E _{OFF}	Turn-Off Energy (Note 2)			-	135	200	μJ
	Characteristics	<u> </u>	-		ı	1	
R _{θJC}	Thermal Resistance Junction-Case	TO-247		_	_	0.27	°C/W

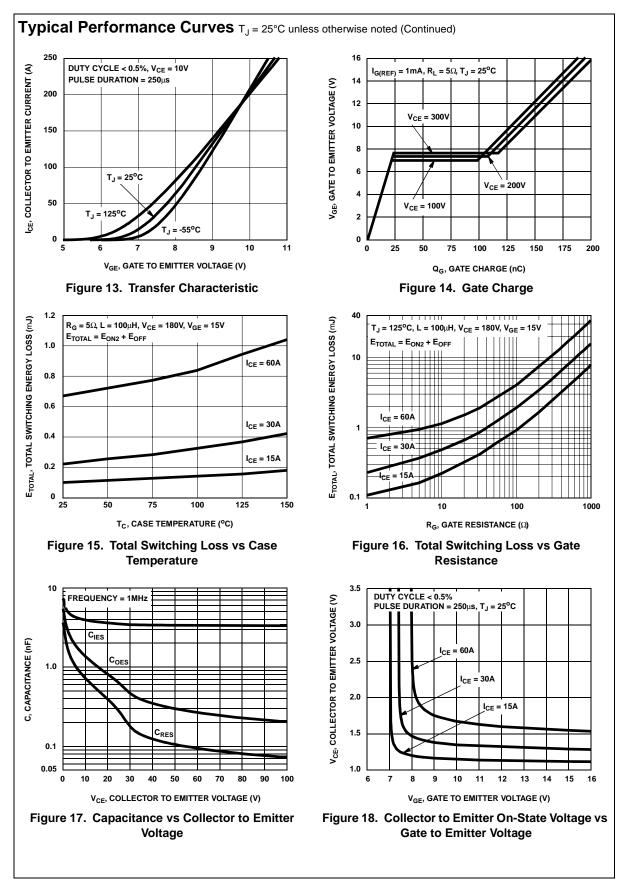
NOTE

 $_{\rm 1.}\,E_{\rm ON2}$ is the turn-on loss when a typical diode is used in the test circuit and the diode is at the same $T_{\rm J}$ as the IGBT. The diode type is specified in figure 20.

^{2.} Turn-Off Energy Loss (E_{OFF}) is defined as the integral of the instantaneous power loss starting at the trailing edge of the input pulse and ending at the point where the collector current equals zero (I_{CF} = 0A). All devices were tested per JEDEC Standard No. 24-1 Method for Measurement of Power Device Turn-Off Switching Loss. This test method produces the true total Turn-Off Energy Loss.







Typical Performance Curves T_J = 25°C unless otherwise noted (Continued)

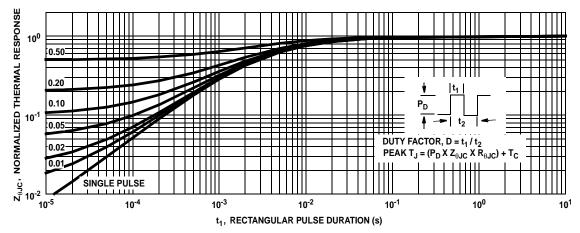


Figure 19. IGBT Normalized Transient Thermal Impedance, Junction to Case

Test Circuit and Waveforms

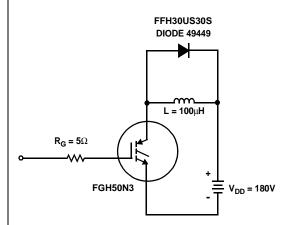


Figure 20. Inductive Switching Test Circuit

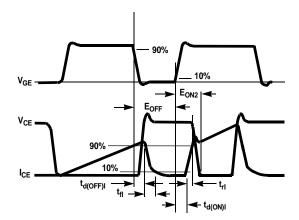


Figure 21. Switching Test Waveforms

Mechanical Dimensions TO-247A03 В 15.87 E Ø 3.65 E Α ⊕ 0.254 M B AM 20.82 E 16.25 E (1.60) 3 5.56 2.66 0.254 M B AM 11.12 Ø 6.85 6.61 Ø 3.65 3.51 NOTES: UNLESS OTHERWISE SPECIFIED. 1.35 0.51 A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004. B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD 13.08 MIN FLASH, AND TIE BAR EXTRUSIONS. C. ALL DIMENSIONS ARE IN MILLIMETERS. D. DRAWING CONFORMS TO ASME Y14.5 - 1994 DOES NOT COMPLY JEDEC STANDARD VALUE E NOTCH MAY BE SQUARE G. DRAWING FILENAME: MKT-TO247A03_REV03 3





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