

# NSR05T40P2

## 500 mA, 40 V Schottky Barrier Diode

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current that offers the most optimal power dissipation in applications. They are housed in spacing saving micro-packaging ideal for space constraint applications.

### Features

- Low Forward Voltage Drop – 580 mV (Typ.) @  $I_F = 500$  mA
- Low Reverse Current – 2.0  $\mu$ A (Typ.) @  $V_R = 40$  V
- 500 mA of Continuous Forward Current
- ESD Rating: – Human Body Model: Class 3B  
– Charged Device Model: Class IV
- High Switching Speed
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### Typical Applications

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping & Protection

### MAXIMUM RATINGS

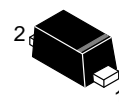
| Rating  | Symbol    | Value      | Unit |
|---|-----------|------------|------|
| Reverse Voltage   | $V_R$     | 40         | V    |
| Forward Current (DC)  | $I_F$     | 500        | mA   |
| Forward Surge Current<br>(60 Hz @ 1 cycle)                                | $I_{FSM}$ | 3.0        | A    |
| Repetitive Peak Forward Current<br>(Pulse Wave = 1 sec, Duty Cycle = 66%) | $I_{FRM}$ | 1.0        | A    |
| ESD Rating: Human Body Model<br>Charged Device Model                      | ESD       | > 8<br>> 1 | kV   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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SOD-923  
CASE 514AA

### MARKING DIAGRAM



YK = Specific Device Code  
M = Month Code  
■ = Pb-Free Package



### ORDERING INFORMATION

| Device        | Package              | Shipping†                      |
|---------------|----------------------|--------------------------------|
| NSR05T40P2T5G | SOD-923<br>(Pb-Free) | 2 mm Pitch<br>8000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NSR05T40P2

## THERMAL CHARACTERISTICS

| Characteristic   | Symbol                   | Min         | Typ | Max        | Unit                     |
|--|--------------------------|-------------|-----|------------|--------------------------|
| Thermal Resistance<br>Junction-to-Ambient (Note 1)<br>Total Power Dissipation @ $T_A = 25^\circ\text{C}$ | $R_{\theta JA}$<br>$P_D$ |             |     | 345<br>360 | $^\circ\text{C/W}$<br>mW |
| Thermal Resistance<br>Junction-to-Ambient (Note 2)<br>Total Power Dissipation @ $T_A = 25^\circ\text{C}$ | $R_{\theta JA}$<br>$P_D$ |             |     | 175<br>715 | $^\circ\text{C/W}$<br>mW |
| Junction and Storage Temperature Range   | $T_J, T_{stg}$           | -55 to +150 |     |            | $^\circ\text{C}$         |

1. Mounted onto a 4 in square FR-4 board 50 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.
2. Mounted onto a 4 in square FR-4 board 650 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.

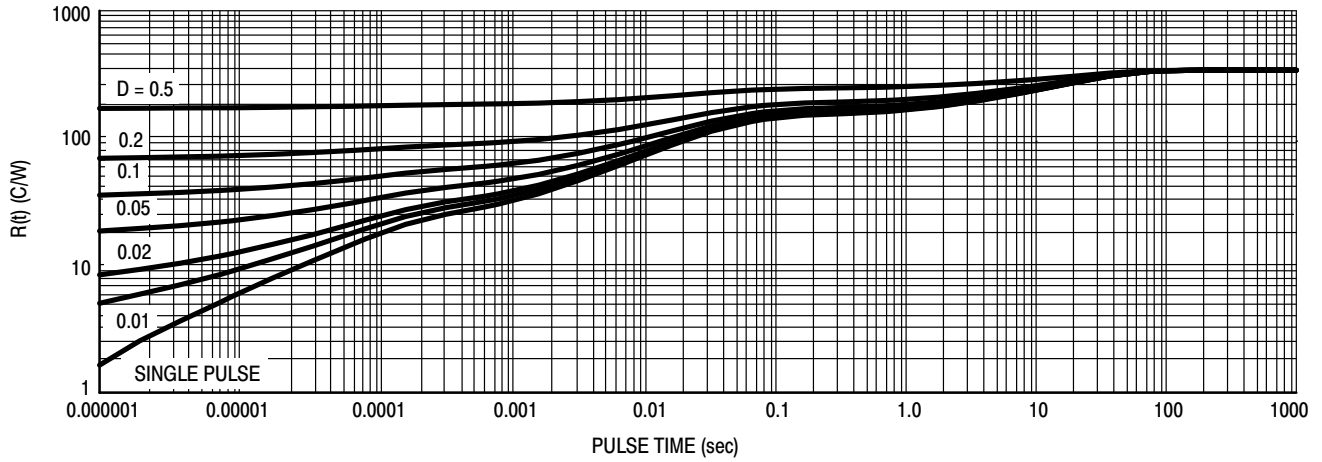


Figure 1. Thermal Response (Note 1)

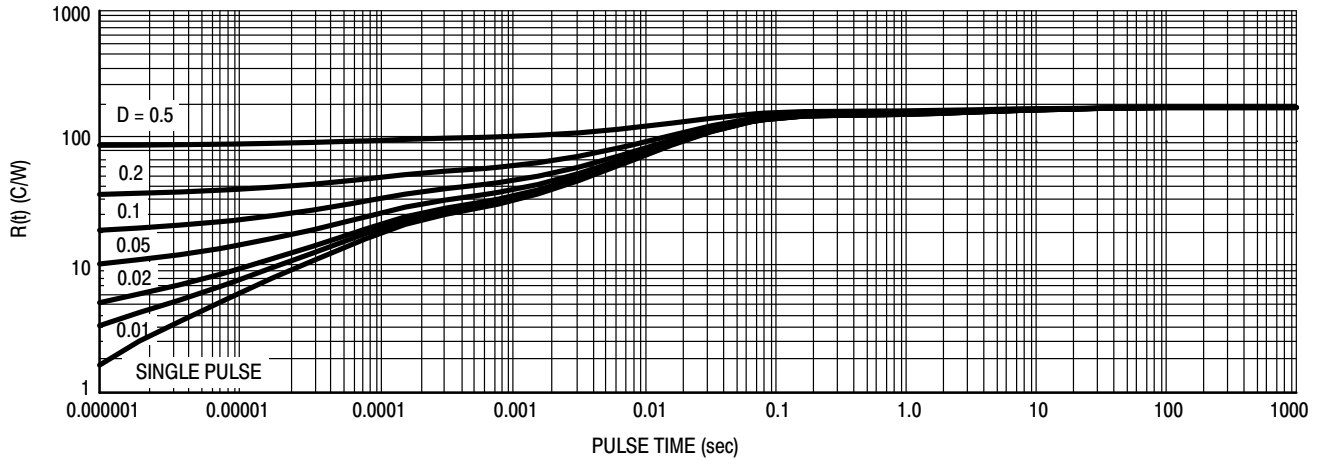
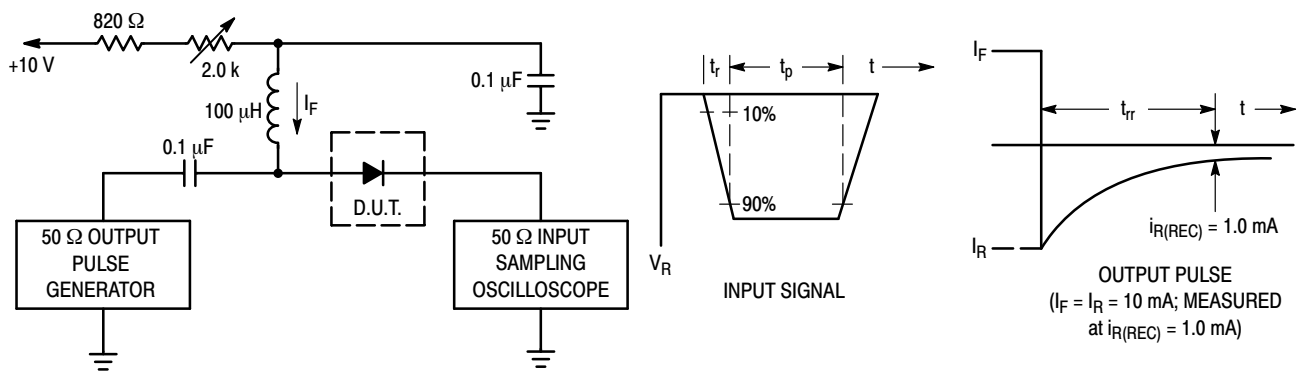


Figure 2. Thermal Response (Note 2)

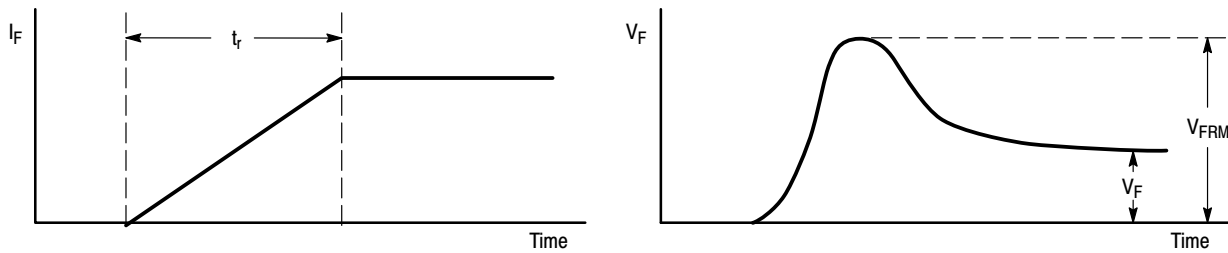
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## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic   | Symbol    | Min | Typ                      | Max                      | Unit          |
|--|-----------|-----|--------------------------|--------------------------|---------------|
| Reverse Leakage<br>( $V_R = 10\text{ V}$ )<br>( $V_R = 40\text{ V}$ )  | $I_R$     |     | 0.2<br>2.0               | 5.0<br>55                | $\mu\text{A}$ |
| Forward Voltage<br>( $I_F = 10\text{ mA}$ )<br>( $I_F = 100\text{ mA}$ )<br>( $I_F = 200\text{ mA}$ )<br>( $I_F = 500\text{ mA}$ ) | $V_F$     |     | 360<br>450<br>490<br>580 | 410<br>500<br>550<br>700 | mV            |
| Total Capacitance<br>( $V_R = 1.0\text{ V}$ , $f = 1.0\text{ MHz}$ )   | $C_T$     |     | 29                       |                          | pF            |
| Reverse Recovery Time<br>( $I_F = I_R = 10\text{ mA}$ , $I_{R(\text{REC})} = 1.0\text{ mA}$ , Figure 3)                            | $t_{rr}$  |     | 8.0                      |                          | ns            |
| Peak Forward Recovery Voltage<br>( $I_F = 100\text{ mA}$ , $t_r = 20\text{ ns}$ , Figure 4)  | $V_{FRM}$ |     | 560                      |                          | mV            |



**Figure 3. Recovery Time Equivalent Test Circuit**



**Figure 4. Peak Forward Recovery Voltage Definition**

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## TYPICAL CHARACTERISTICS

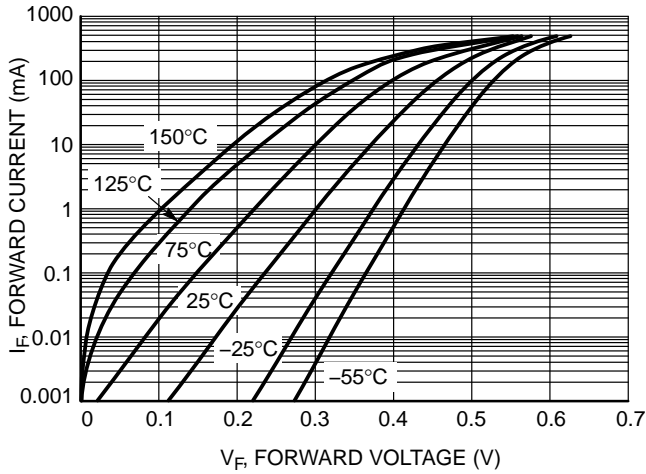


Figure 5. Forward Voltage

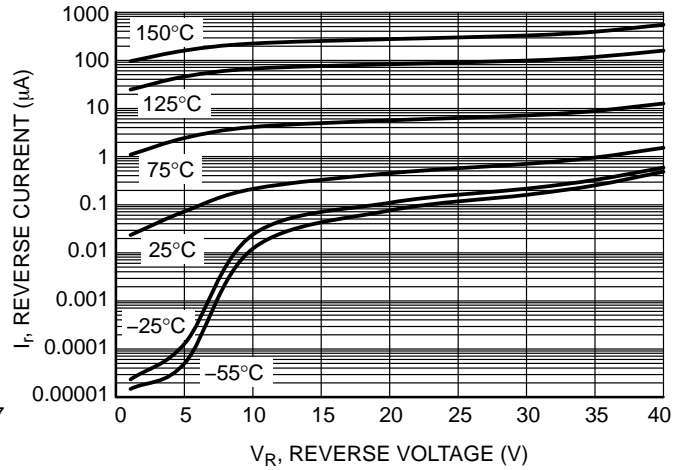


Figure 6. Leakage Current

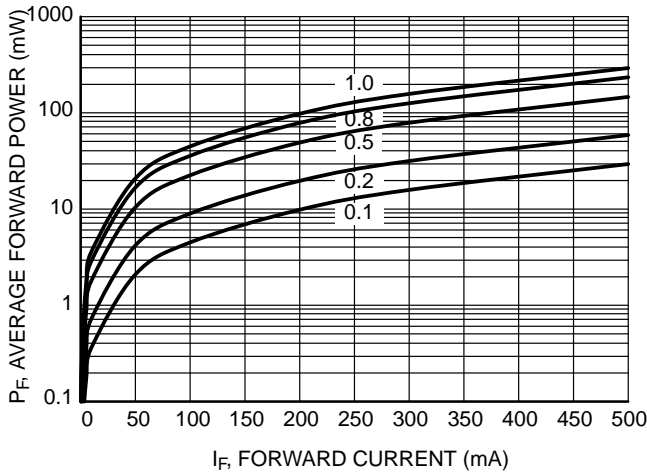


Figure 7. Average Forward Power Dissipation

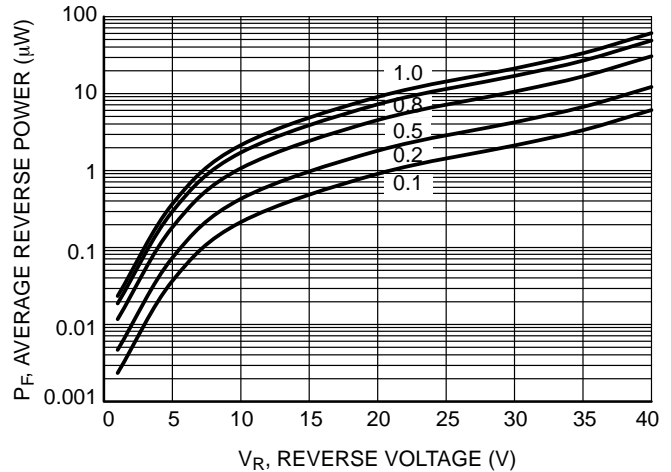


Figure 8. Average Reverse Power Dissipation

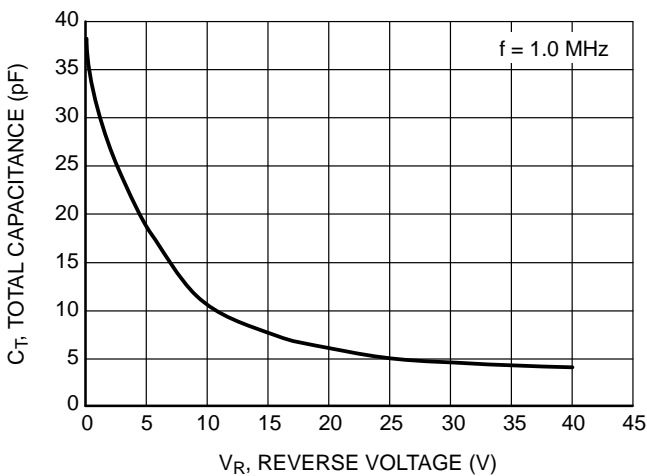


Figure 9. Total Capacitance

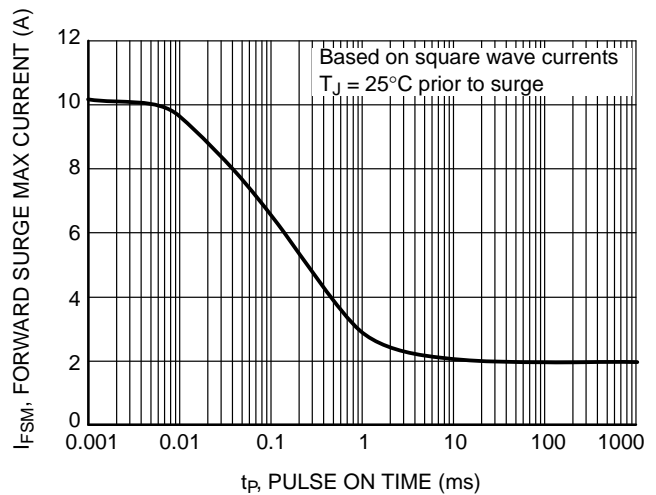
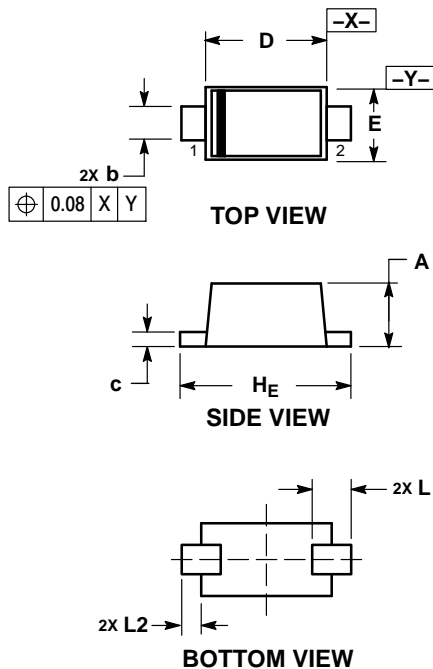


Figure 10. Forward Surge Current

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## PACKAGE DIMENSIONS

**SOD-923**  
CASE 514AA  
ISSUE E

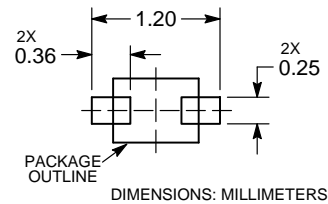


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

| DIM   | MILLIMETERS |      |      | INCHES    |       |       |
|-------|-------------|------|------|-----------|-------|-------|
|       | MIN         | NOM  | MAX  | MIN       | NOM   | MAX   |
| A     | 0.34        | 0.39 | 0.43 | 0.013     | 0.015 | 0.017 |
| b     | 0.15        | 0.20 | 0.25 | 0.006     | 0.008 | 0.010 |
| c     | 0.07        | 0.12 | 0.17 | 0.003     | 0.005 | 0.007 |
| D     | 0.75        | 0.80 | 0.85 | 0.030     | 0.031 | 0.033 |
| E     | 0.55        | 0.60 | 0.65 | 0.022     | 0.024 | 0.026 |
| $H_E$ | 0.95        | 1.00 | 1.05 | 0.037     | 0.039 | 0.041 |
| L     | 0.19 REF    |      |      | 0.007 REF |       |       |
| L2    | 0.05        | 0.10 | 0.15 | 0.002     | 0.004 | 0.006 |

**SOLDERING FOOTPRINT\***



See Application Note AND845/D for more mounting details

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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