

General purpose (dual digital transistor)

Parameter	DTr1 and DTr2	
V _{CEO}	-50V	
I _C	-100mA	
R ₁	10kΩ	

Features

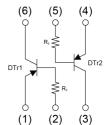
- 1)Two DTA114T chips in a EMT or UMT package.
- 2)Mounting possible with EMT3 or UMT3 automatic mounting machines.
- 3)Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

Outline

SOT-563	SOT-363
(1) (2) (3)	(1) (2) (3)
EMB4 (EMT6)	UMB4N (UMT6)

•Inner circuit

- (1) DTr1 Emitter
- (2) DTr1 Base
- (3) DTr2 Collector
- (4) DTr2 Emitter
- (5) DTr2 Base
- (6) DTr1 Collector



Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

	- assuging operations						
Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
EMB4	SOT-563 (EMT6)	1616	T2R	180	8	8000	B4
UMB4N	SOT-363 (UMT6)	2021	TN	180	8	3000	B4

● Absolute maximum ratings (T_a = 25°C)

<For DTr1 and DTr2 in common>

Parameter		Symbol	Values	Unit		
Collector-base voltage		V_{CBO}	-50	V		
Collector-emitter voltage			V_{CEO}	-50	V	
Emitter-base voltage		V _{EBO}	-5	V		
Collector current		I _C	-100	mA		
Dance dia dia atta a	EMB4		P _D *1*2	150)A//T /	
Power dissipation	UMB4N		P _D *1*2	150	mW/Total	
Junction temperature		T _j	150	°C		
Range of storage temperature		T _{stg}	-55 to +150	°C		

● Electrical characteristics (T_a = 25°C)

<For DTr1 and DTr2 in common>

Davanastav	Cy reads ad	Conditions	Values			Linit
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV _{CBO}	I _C = -50μA	-50	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	BV _{CEO} I _C = -1mA		-	-	V
Emitter-base breakdown voltage	BV _{EBO}	I _E = -50μA	-5	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = -50V	-	-	-500	nA
Emitter cut-off current I _{EBO}		V _{EB} = -4V	-	1	-500	nA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = -10mA, I _B = -1mA	-	-	-300	mV
DC current gain	h _{FE}	$V_{CE} = -5V$, $I_C = -1mA$	100	250	600	-
Input resistance	R ₁	-	7	10	13	kΩ
Transition frequency	f _T *3	V _{CE} = -10V, I _E = 5mA, f = 100MHz	-	250	-	MHz

^{*1} Each terminal mounted on a reference land.

^{*2 120}mW per element must not be exceeded.

^{*3} Characteristics of built-in transistor.

● Electrical characteristic curves (T_a = 25°C)

<For DTr1 and DTr2 in common>

Fig.1 Grounded Emitter Propagation
Characteristics

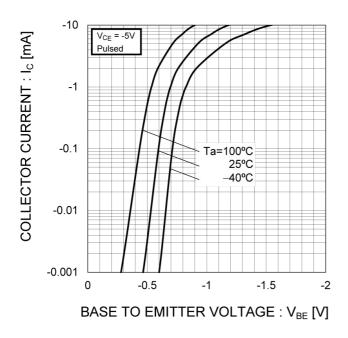


Fig.2 Grounded Emitter Output Characteristics

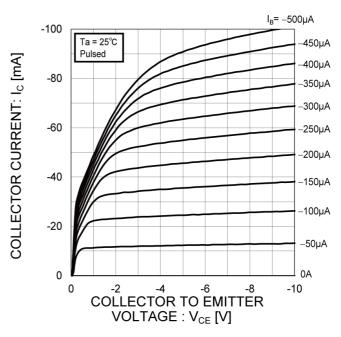


Fig.3 DC Current Gain vs. Collector Current

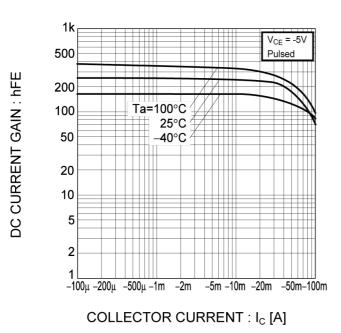
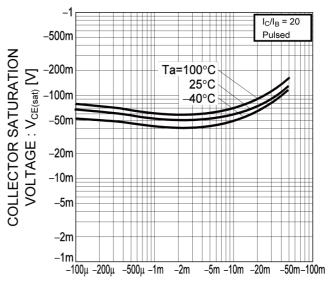
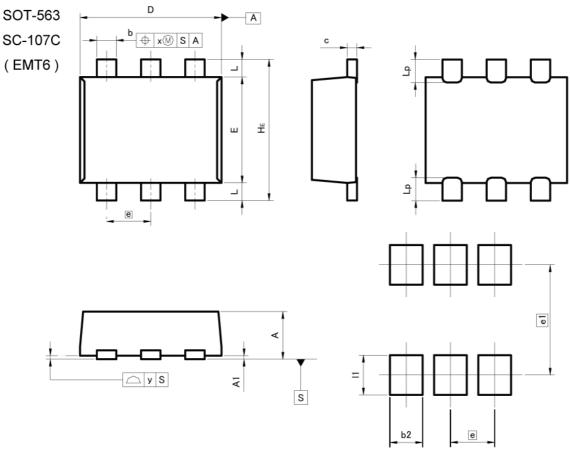


Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

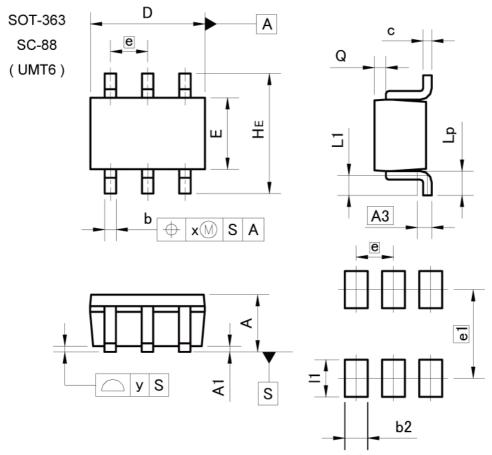
DIM	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.45	0.55	0.018	0.022
A1	0.00	0.10	0.000	0.004
b	0.17	0.27	0.007	0.011
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	1.10	1.30	0.043	0.051
е	0.	50	0.020	
HE	1.50	1.70	0.059	0.067
L	0.10	0.30	0.004	0.012
Lp	-	0.35	-	0.014
х	_	0.10	_	0.004
У	-	0.10	-	0.004

DIM	MILIMETERS		INC	HES		
DIM	MIN	MAX	MIN	MAX		
b2	_	0.37	_	0.015		
e1	1.25		0.0	49		
11	-	0.45	-	0.018		

Dimension in mm/inches



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.3	25	0.0	10
b	0.15	0.30	0.006	0.012
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.026	
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.020
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
х	-	0.10	,-	0.004
У		0.10	e -	0.004

	DIM	MILIMETERS		INCHES		
DIW	MIN	MAX	MIN	MAX		
	b2	- 7	0.40	-	0.016	
	e1	1.55		0.0	61	
	11	-	0.65	-	0.026	

Dimension in mm/inches



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CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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