

ZXCT1023

MICROPOWER FIXED GAIN OF 50 CURRENT MONITOR

Description

The ZXCT1023 is a precision high-side current sense monitor. Using this type of device eliminates the need to disrupt the ground plane when sensing a load current.

The ZXCT1023 has a fixed internal gain of 50 and the only external component required is the external current sense resistor; this combined with its 1.2mm x 1.8mm TDFN package more than quarters the solution size of the ZXCT1010.

The wide input voltage range of 20V down to as low as 2.5V makes it suitable for a range of applications.

The combination of operation down to 2.5V and just 3.5 μ A quiescent current makes it ideal for single cell Li-Ion/polymer battery charge/discharge measurement applications.

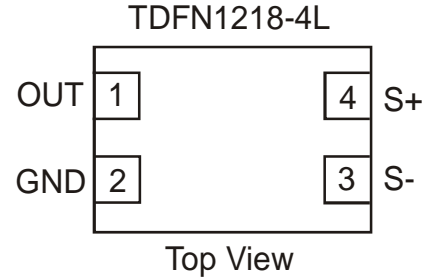
Features

- Accurate high-side current sensing
- Fixed gain of 50 output scaling
- 2.5V – 20V operating range
- 3.5 μ A quiescent current
- TDFN1218 package

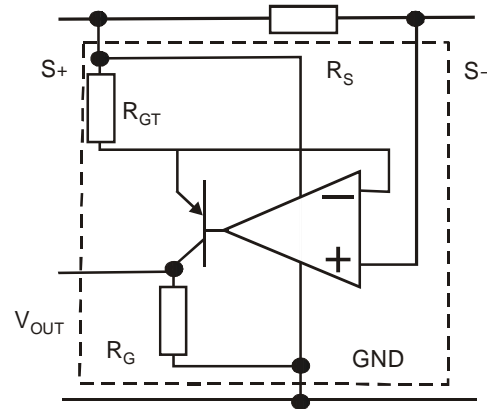
Applications

- Battery capacity measurement
- Battery chargers
- Over-current monitor

Pin Connections



Typical Operating Circuit



Ordering Information

Order Reference	Package	Device Marking	Status	Reel Size (inches)	Quantity per Reel	Tape Width (mm)
ZXCT1023DFGTA	TDFN1218	1023	Active	7	3000	8

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Absolute maximum ratings

Voltage on S+ ¹	-0.5V to 20V
Voltage on S- ^{1,2} , OUT ¹	-0.5V to V _{S+} +0.5V
V _{SENSE} ³	-0.5V to +2.5V
Junction temperature	-40 to 125°C
Storage temperature	-55 to 150°C
Package power dissipation (T _A = 25°C)	
TDFN1812-4	mW

These are stress ratings only. Operation outside the absolute maximum ratings may cause device failure. Operation at the absolute maximum rating for extended periods may reduce device reliability

- Notes: 1. Measured with respect to GND pin
- Subject to absolute maximum V_{SENSE} not being exceeded.
 - V_{SENSE} is defined as the voltage difference across the sense resistor, R_S.
 - The usable V_{SENSE} range is limited by the output voltage range; and as such will be reduced at lower V_{S+} values.

Semiconductor devices are ESD sensitive and may be damaged by exposure to ESD events. Suitable ESD precautions should be taken when handling and transporting these devices.

ESD ratings:

Human body model	2000V
Machine Model	TBD

Recommended operating conditions

Parameter	MIN	MAX	Units
V _{S+} ¹ , Common-mode sense input range	2.5	20	V
V _{SENSE} , Differential Sense Input voltage range	0	380 ⁴	mV
V _{OUT} , Output Voltage range	0	V _{S-} - 1	V
T _A , Ambient temperature range	-40	85	°C

Electrical characteristics

T_A = 25°C, V_{S+} = 3.6V, V_{SENSE} = 50mV, unless otherwise stated

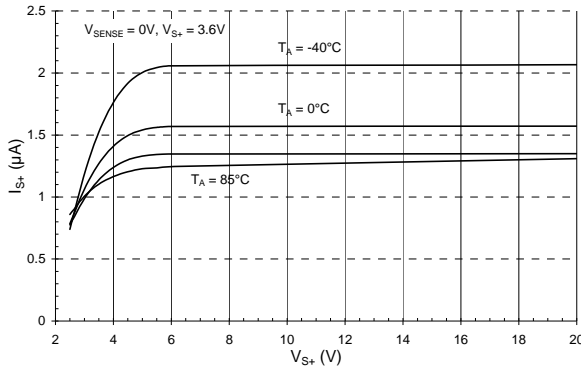
Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V _{OUT}	Output voltage	V _{SENSE} = 0mV		0.3	50	mV
		V _{SENSE} = 10mV	425	500	575	
		V _{SENSE} = 30mV	1.41	1.5	1.59	V
		V _{SENSE} = 50mV	2.425	2.5	2.575	
		V _{SENSE} = 100mV, V _{S+} = 20V	4.85	5	5.15	
TC ⁵	Output voltage temperature coefficient			50	300	ppm
I _Q	Ground pin current	V _{SENSE} = 0V		3.5	8	µA
I _{S-}	SENSE- input current	V _{SENSE} = 0V			100	nA
Acc	Accuracy	V _{SENSE} = 50mV	-3		3	%
Gain	V _{OUT} /V _{SENSE}	V _{SENSE} = 50mV		50		V/V
R _{OUT}	Output resistance			15		kΩ
BW	Bandwidth	V _{SENSE} (DC) = 10mV		300		kHz
		V _{SENSE} (DC) = 50mV		1		MHz
PSRR ⁶	Power supply rejection ratio	V _{SENSE} = 30mV, V _{S+} = 2.5 to 20V	50	60		dB

- Notes 5. TC limits are determined by characterization.
6. PSRR is defined as change in output voltage per change in S+ voltage, V_{S+}.

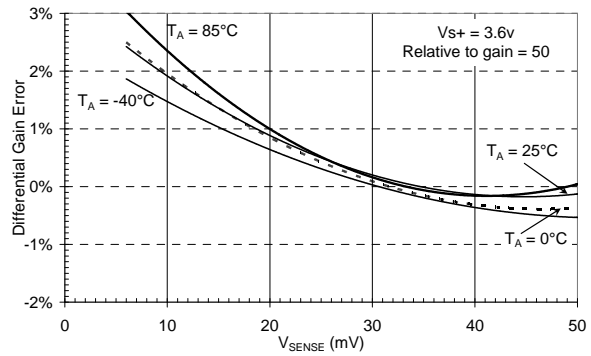
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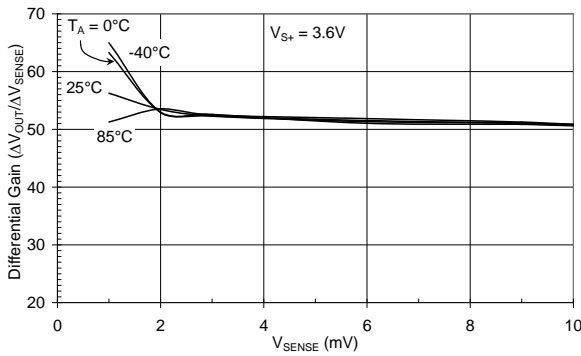
Typical DC characteristics



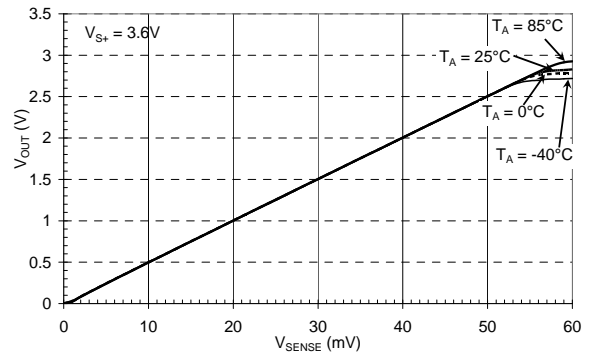
S+ input current vs Supply voltage



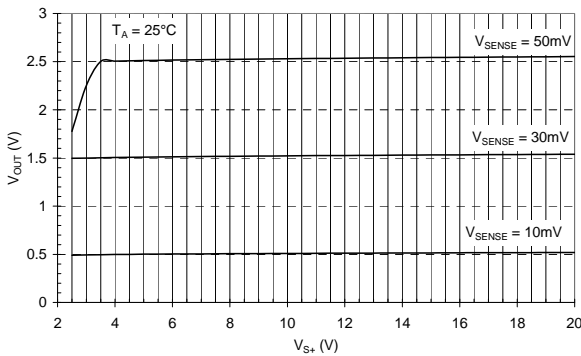
Normalised gain error Vs Sense voltage



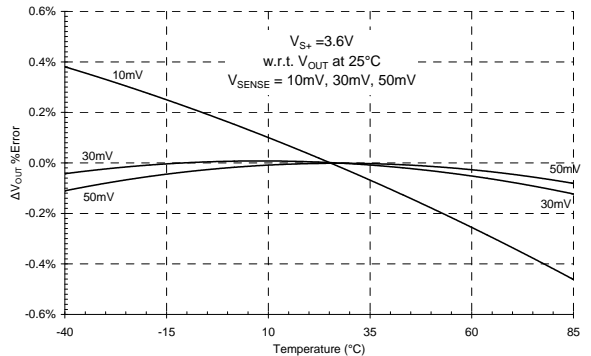
Differential gain Vs Sense voltage



Output Voltage Vs Sense voltage



Output voltage Vs+ Input voltage

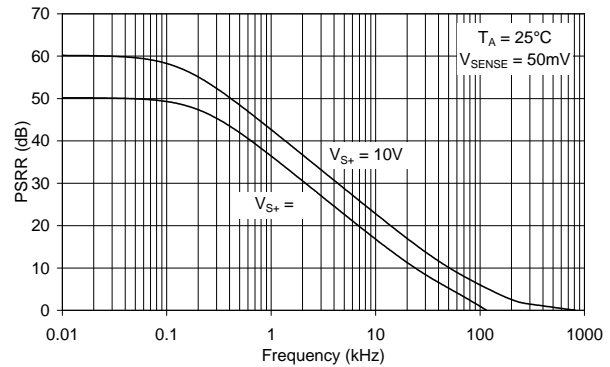
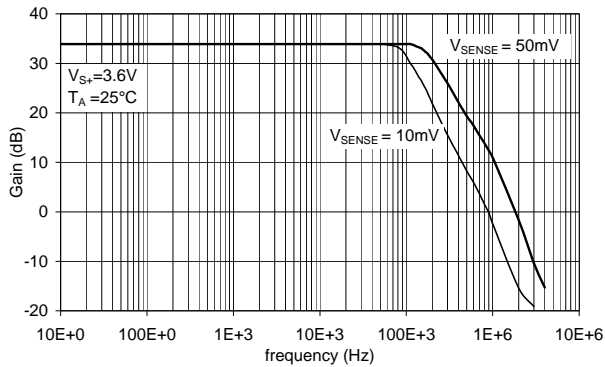


Relative output voltage change Vs Temperature

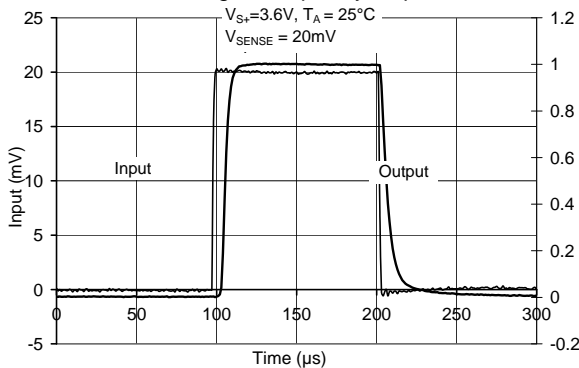
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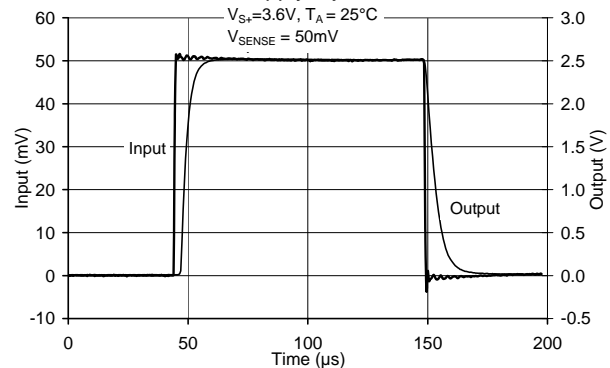
Typical AC characteristics



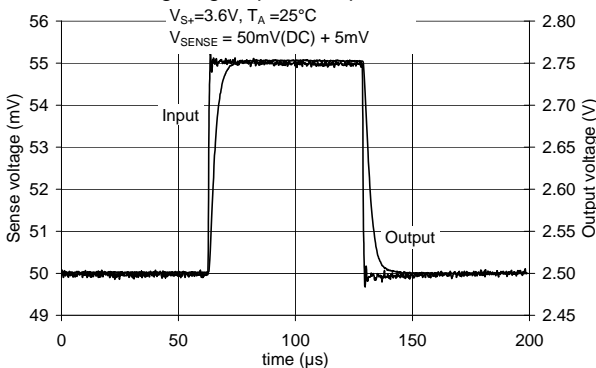
Small signal frequency response



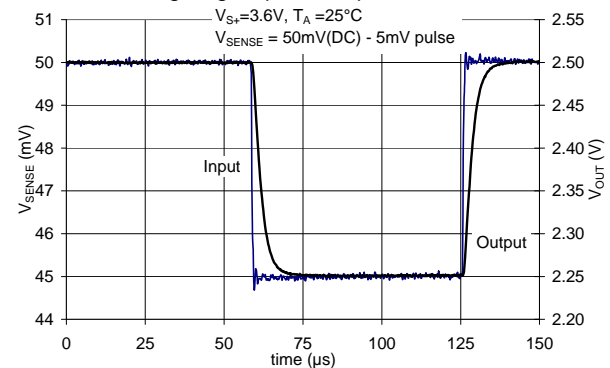
Power supply rejection ratio



Large signal frequency response 20mV



Large signal frequency response 50mV



Small signal positive pulse response

Small signal negative pulse response

Pin out information

Pin No.	Name	Pin Function
1	OUT	Voltage output. The output voltage is referenced to GND. The overall voltage gain is 50, i.e., $V_{OUT} = 50 \times V_{SENSE}$ where $V_{SENSE} = V_{S+} - V_{S-}$.
2	GND	Ground and substrate connection of device.
3	S-	High impedance negative sense voltage input
4	S+	Positive sense input. Also acts as power supply pin to ZXCT1023
	Central Paddle	Substrate. Connect to GND.

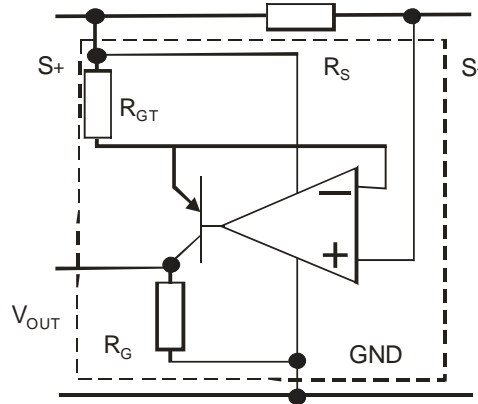
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Application information

The ZXCT1023 is line powered (derives its power from the rail being sensed) this reduces the number of pins used and PCB trace routing. The fixed gain of 50 reduces the PCB area by reducing the number of external components. The only external component required is the sense resistor. This coupled with the 1.2mm x 1.8mm TDFN package makes the solution size very small.

The fixed gain of 50 has been chosen to meet the normal requirements of most applications.



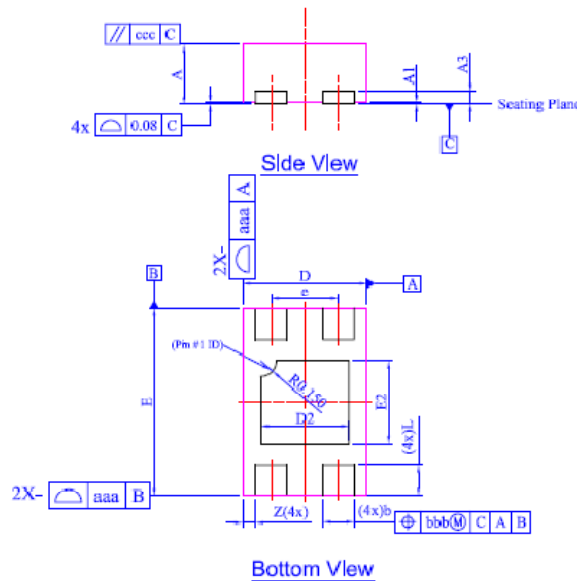
The ZXCT1023 has its gain setting resistor, R_G , set at 15k Ω which further reduces power consumption at larger V_{SENSE} .

Application Examples

Please refer to Zetex AN39 for sample applications.

Package Outline

TDFN1812-4 package

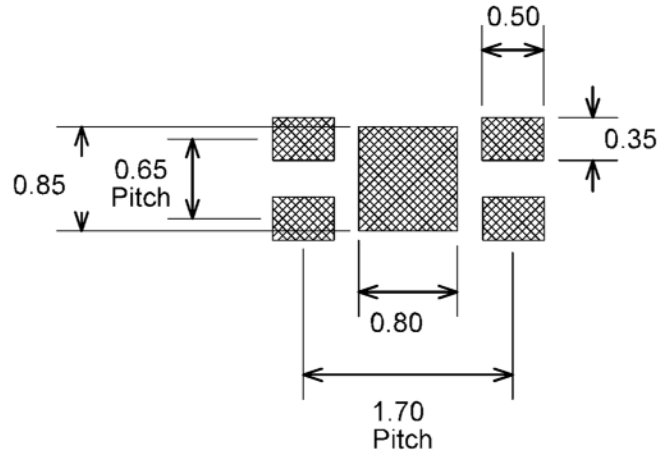


Dim	Min	Max	Typ
D	1.15	1.25	1.20
E	1.75	1.85	1.80
D2	0.75	0.95	0.85
E2	0.70	0.90	0.80
A	0.545	0.605	0.575
A1	0	0.05	0.02
A3	—	—	0.13
b	0.25	0.35	0.30
L	0.25	0.35	0.30
e	—	—	0.65
Z	—	—	0.125
aaa	0.25		
bbb	0.10		
ccc	0.10		

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Recommended PCB Land Pattern



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Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург,
Промышленная ул, дом № 19, литера Н,
помещение 100-Н Офис 331