

## IGBT

High speed 5 IGBT in TRENCHSTOP™ 5 technology

## IGZ100N65H5

650V IGBT high speed series fifth generation

Data sheet

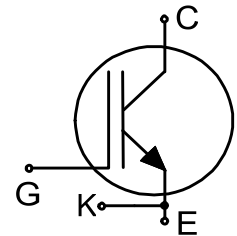
Industrial Power Control

### High speed 5 IGBT in TRENCHSTOP™ 5 technology

#### Features and Benefits:

High speed H5 technology offering

- Ultra low loss switching thanks to Kelvin emitter pin in combination with TRENCHSTOP™ 5
- Best-in-class efficiency in hard switching and resonant topologies
- Plug and play replacement of previous generation IGBTs
- 650V breakdown voltage
- Low gate charge  $Q_G$
- Maximum junction temperature 175°C
- Qualified according to JEDEC for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models:  
<http://www.infineon.com/igbt/>



#### Applications

- Uninterruptible power supplies
- Welding converters
- Mid to high range switching frequency converters
- Solar string inverters

#### Package pin definition:

- Pin C & backside - collector
- Pin E - emitter
- Pin K - Kelvin emitter
- Pin G - gate

Please note: The emitter and Kelvin emitter pins are not exchangeable. Their exchange might lead to malfunction.



#### Key Performance and Package Parameters

Type	$V_{CE}$	$I_C$	$V_{CEsat}, T_{vj}=25^\circ C$	$T_{vjmax}$	Marking	Package
IGZ100N65H5	650V	100A	1.65V	175°C	G100EH5	PG-TO247-4

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**Maximum Ratings**

For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25^{\circ}\text{C}$	$V_{CE}$	650	V
DC collector current, limited by $T_{vjmax}$ $T_C = 25^{\circ}\text{C}$ $T_C = 100^{\circ}\text{C}$	$I_C$	161.0 101.0	A
Pulsed collector current, $t_p$ limited by $T_{vjmax}^{1)}$	$I_{Cpuls}$	400.0	A
Turn off safe operating area $V_{CE} \leq 650\text{V}$ , $T_{vj} \leq 175^{\circ}\text{C}$ , $t_p = 1\mu\text{s}^{1)}$	-	400.0	A
Gate-emitter voltage Transient Gate-emitter voltage ( $t_p \leq 10\mu\text{s}$ , $D < 0.010$ )	$V_{GE}$	$\pm 20$ $\pm 30$	V
Power dissipation $T_C = 25^{\circ}\text{C}$ Power dissipation $T_C = 100^{\circ}\text{C}$	$P_{tot}$	536.0 268.0	W
Operating junction temperature	$T_{vj}$	-40...+175	$^{\circ}\text{C}$
Storage temperature	$T_{stg}$	-55...+150	$^{\circ}\text{C}$
Soldering temperature, wave soldering 1.6mm (0.063in.) from case for 10s		260	$^{\circ}\text{C}$
Mounting torque, M3 screw Maximum of mounting processes: 3	$M$	0.6	Nm

**Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value	Unit
<b>Characteristic</b>				
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		0.28	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		40	K/W

**Electrical Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0\text{V}$ , $I_C = 0.20\text{mA}$	650	-	-	V
Collector-emitter saturation voltage	$V_{CEsat}$	$V_{GE} = 15.0\text{V}$ , $I_C = 100.0\text{A}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 100^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$	- - -	1.65 1.82 1.90	2.10 - -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 1.00\text{mA}$ , $V_{CE} = V_{GE}$	3.2	4.0	4.8	V
Zero gate voltage collector current	$I_{CES}$	$V_{CE} = 650\text{V}$ , $V_{GE} = 0\text{V}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	- -	- 1100.0	100.0 -	$\mu\text{A}$
Gate-emitter leakage current	$I_{GES}$	$V_{CE} = 0\text{V}$ , $V_{GE} = 20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE} = 20\text{V}$ , $I_C = 100.0\text{A}$	-	200.0	-	S

<sup>1)</sup> Defined by design. Not subject to production test.

**Electrical Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Dynamic Characteristic</b>						
Input capacitance	$C_{ies}$	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$	-	6560	-	pF
Output capacitance	$C_{oes}$		-	97	-	
Reverse transfer capacitance	$C_{res}$		-	21	-	
Gate charge	$Q_G$	$V_{CC} = 520\text{V}, I_C = 100.0\text{A}, V_{GE} = 15\text{V}$	-	210.0	-	nC
Internal emitter inductance <sup>1)</sup> measured 5mm (0.197 in.) from case	$L_E$		-	13.0	-	nH

**Switching Characteristic, Inductive Load**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

**IGBT Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$** 

Turn-on delay time	$t_{d(on)}$	$T_{vj} = 25^{\circ}\text{C}, V_{CC} = 400\text{V}, I_C = 50.0\text{A}, V_{GE} = 0.0/15.0\text{V}, R_{G(on)} = 8.0\Omega, R_{G(off)} = 18.0\Omega, L\sigma = 30\text{nH}, C\sigma = 25\text{pF}$ $L\sigma, C\sigma$ from Fig. E	-	30	-	ns	
Rise time	$t_r$		-	9	-	ns	
Turn-off delay time	$t_{d(off)}$		Energy losses include "tail" and diode reverse recovery. Diode from IKZ75N65EH5.	-	421	-	ns
Fall time	$t_f$		-	15	-	ns	
Turn-on energy	$E_{on}$		-	0.85	-	mJ	
Turn-off energy	$E_{off}$		-	0.77	-	mJ	
Total switching energy	$E_{ts}$		-	1.62	-	mJ	

**Switching Characteristic, Inductive Load**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

**IGBT Characteristic, at  $T_{vj} = 150^{\circ}\text{C}$** 

Turn-on delay time	$t_{d(on)}$	$T_{vj} = 150^{\circ}\text{C}, V_{CC} = 400\text{V}, I_C = 50.0\text{A}, V_{GE} = 0.0/15.0\text{V}, R_{G(on)} = 8.0\Omega, R_{G(off)} = 18.0\Omega, L\sigma = 30\text{nH}, C\sigma = 25\text{pF}$ $L\sigma, C\sigma$ from Fig. E	-	28	-	ns	
Rise time	$t_r$		-	12	-	ns	
Turn-off delay time	$t_{d(off)}$		Energy losses include "tail" and diode reverse recovery. Diode from IKZ75N65EH5.	-	468	-	ns
Fall time	$t_f$		-	17	-	ns	
Turn-on energy	$E_{on}$		-	1.43	-	mJ	
Turn-off energy	$E_{off}$		-	0.76	-	mJ	
Total switching energy	$E_{ts}$		-	2.19	-	mJ	

<sup>1)</sup> The internal emitter inductance does not affect the gate control circuitry if bypassed by using the emitter sense pin.

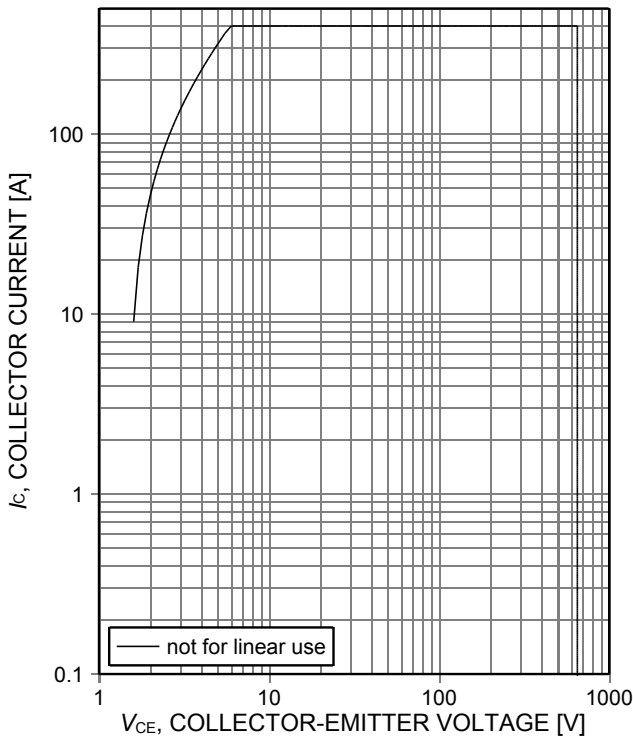


Figure 1. **Forward bias safe operating area**  
 ( $D=0$ ,  $T_C=25^\circ\text{C}$ ,  $T_{vj}\leq 175^\circ\text{C}$ ,  $V_{GE}=15\text{V}$ ,  $t_p=1\mu\text{s}$ ,  
 $I_{Cmax}$  defined by design - not subject to  
 production test)

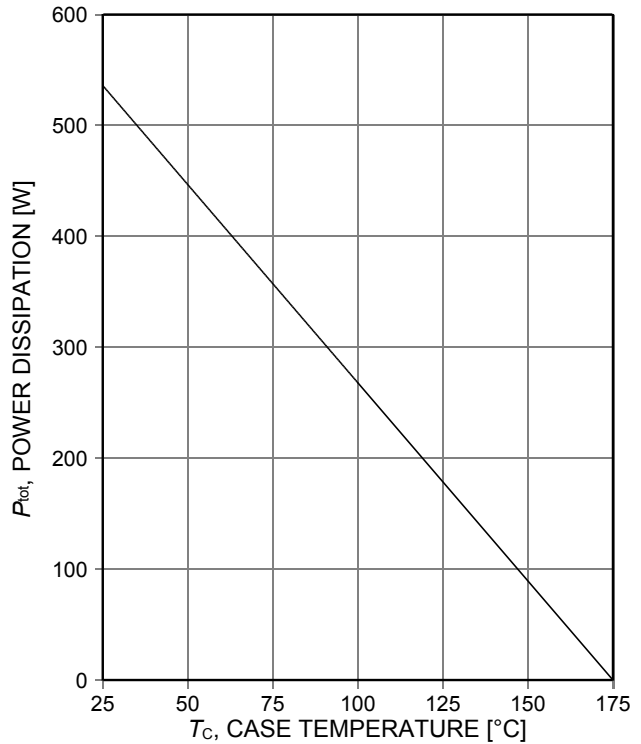


Figure 2. **Power dissipation as a function of case temperature**  
 ( $T_{vj}\leq 175^\circ\text{C}$ )

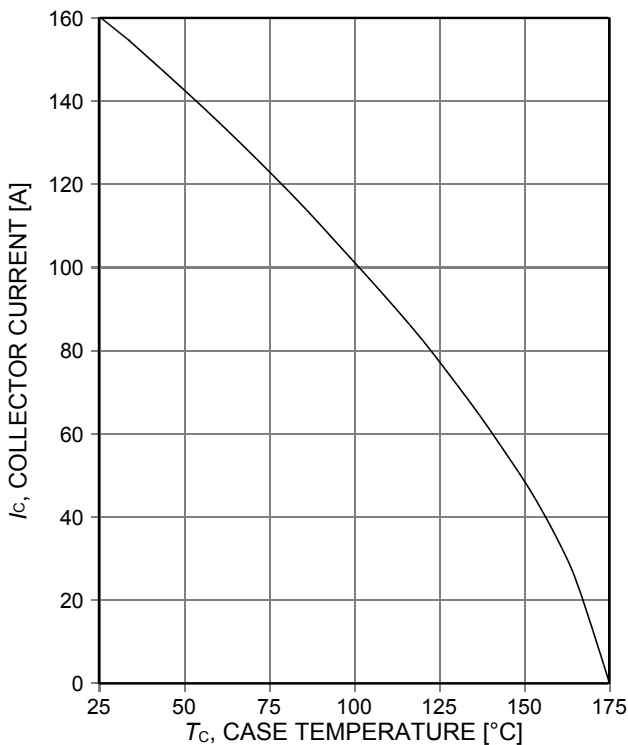


Figure 3. **Collector current as a function of case temperature**  
 ( $V_{GE}\geq 15\text{V}$ ,  $T_{vj}\leq 175^\circ\text{C}$ )

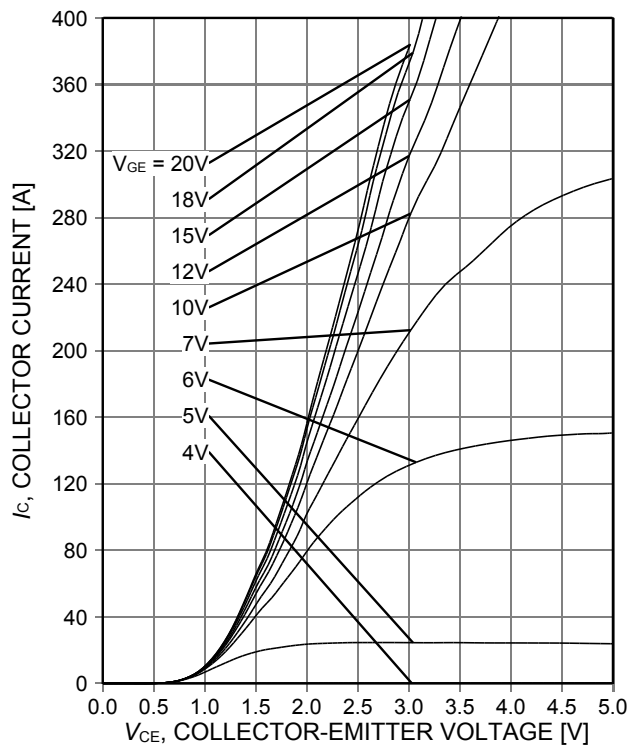


Figure 4. **Typical output characteristic**  
 ( $T_{vj}=25^\circ\text{C}$ )

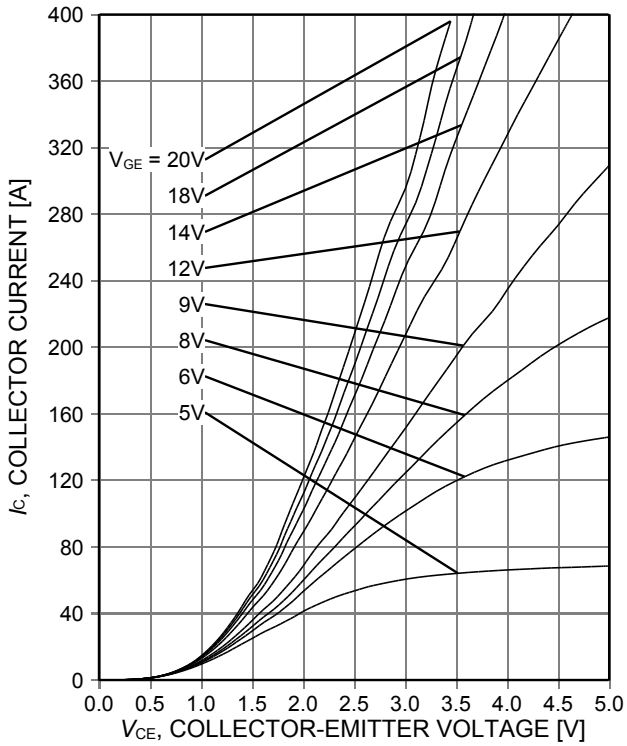


Figure 5. **Typical output characteristic**  
( $T_{vj}=175^{\circ}\text{C}$ )

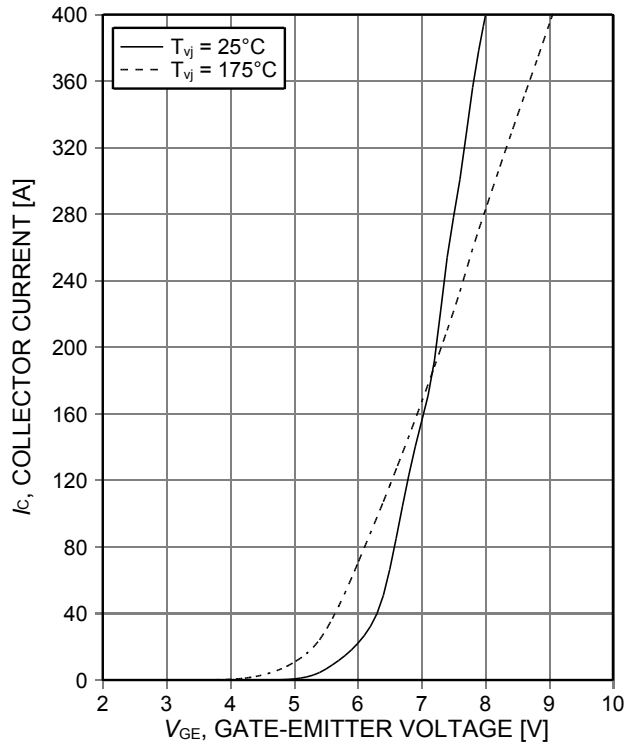


Figure 6. **Typical transfer characteristic**  
( $V_{CE}=20\text{V}$ )

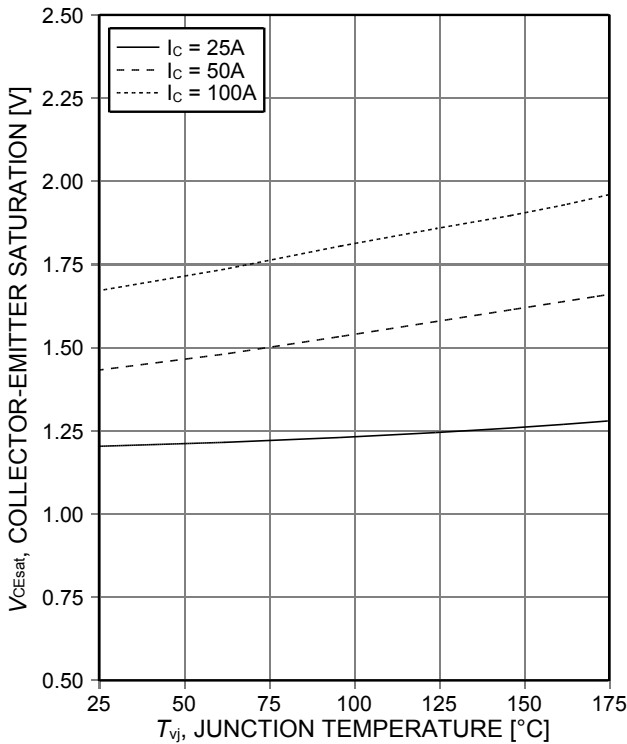


Figure 7. **Typical collector-emitter saturation voltage as a function of junction temperature**  
( $V_{GE}=15\text{V}$ )

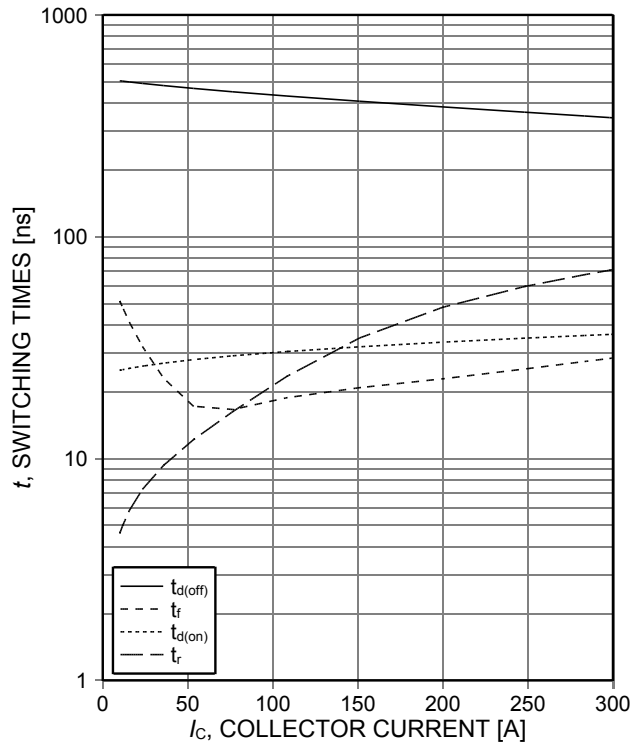


Figure 8. **Typical switching times as a function of collector current**  
(inductive load,  $T_{vj}=150^{\circ}\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $R_{G(on)}=8\Omega$ ,  $R_{G(off)}=18\Omega$ , dynamic test circuit in Figure E)

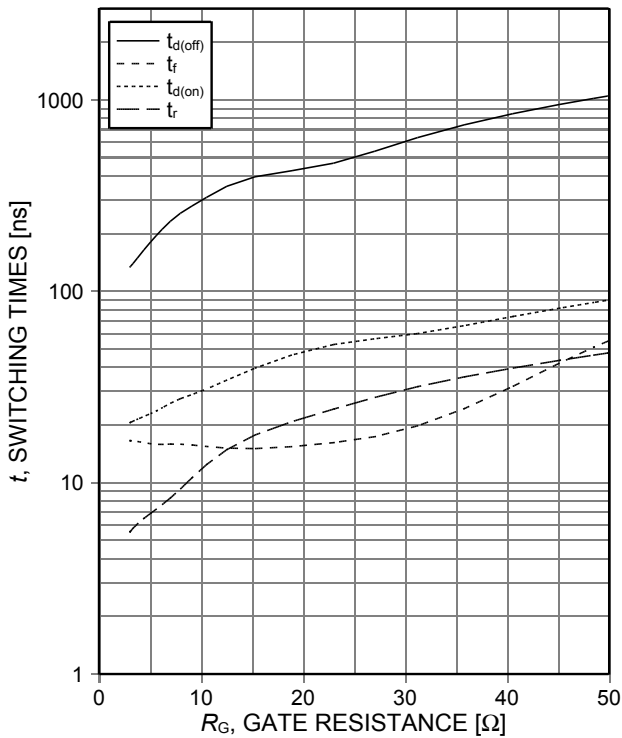


Figure 9. **Typical switching times as a function of gate resistance**  
 (inductive load,  $T_{vj}=150^{\circ}\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=50\text{A}$ , dynamic test circuit in Figure E)

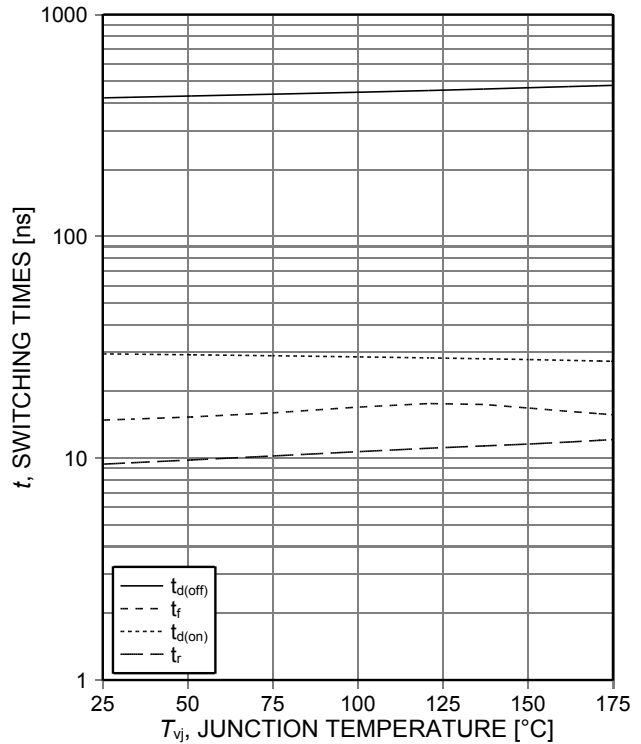


Figure 10. **Typical switching times as a function of junction temperature**  
 (inductive load,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=50\text{A}$ ,  $R_{G(on)}=8\Omega$ ,  $R_{G(off)}=18\Omega$ , dynamic test circuit in Figure E)

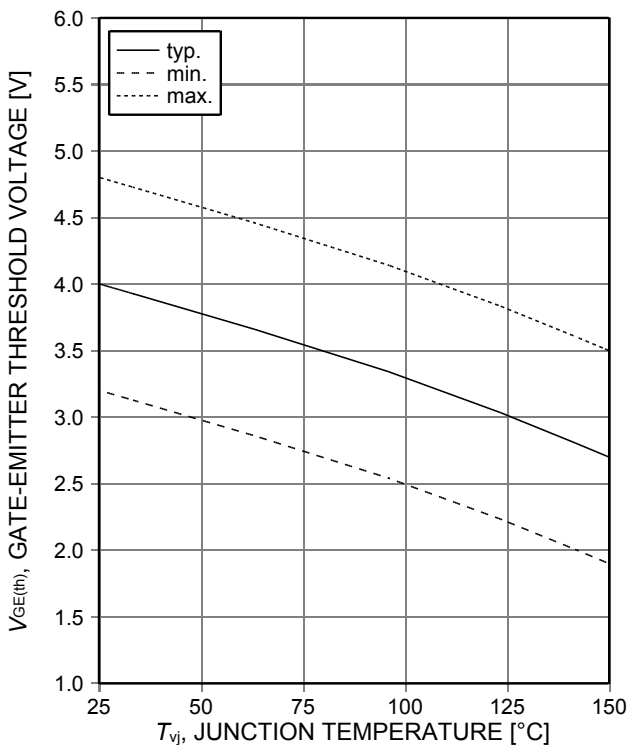


Figure 11. **Gate-emitter threshold voltage as a function of junction temperature**  
 ( $I_C=1\text{mA}$ )

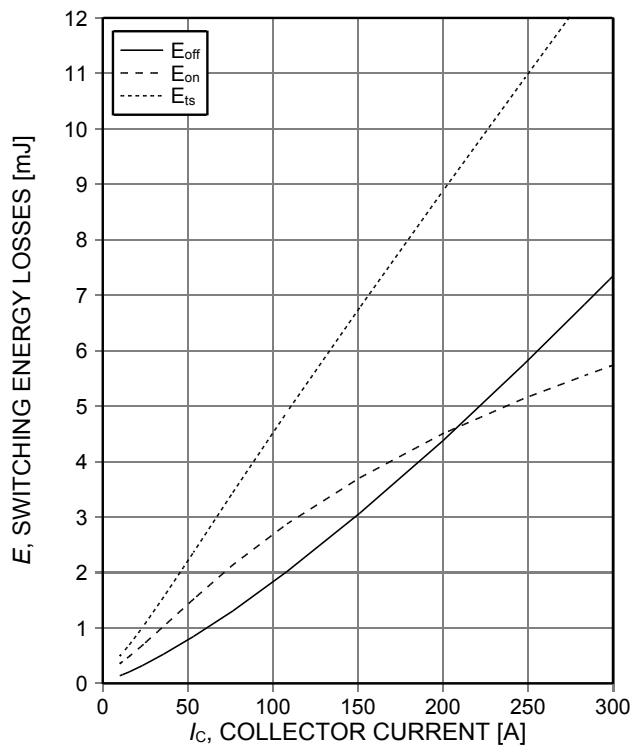


Figure 12. **Typical switching energy losses as a function of collector current**  
 (inductive load,  $T_{vj}=150^{\circ}\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $R_{G(on)}=8\Omega$ ,  $R_{G(off)}=18\Omega$ , dynamic test circuit in Figure E)



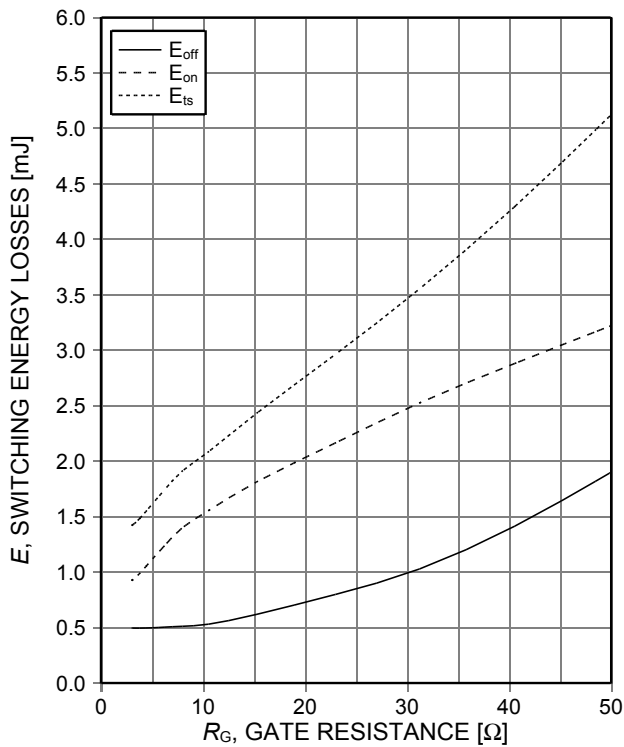


Figure 13. **Typical switching energy losses as a function of gate resistance**  
 (inductive load,  $T_{vj}=150^{\circ}\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=50\text{A}$ , dynamic test circuit in Figure E)

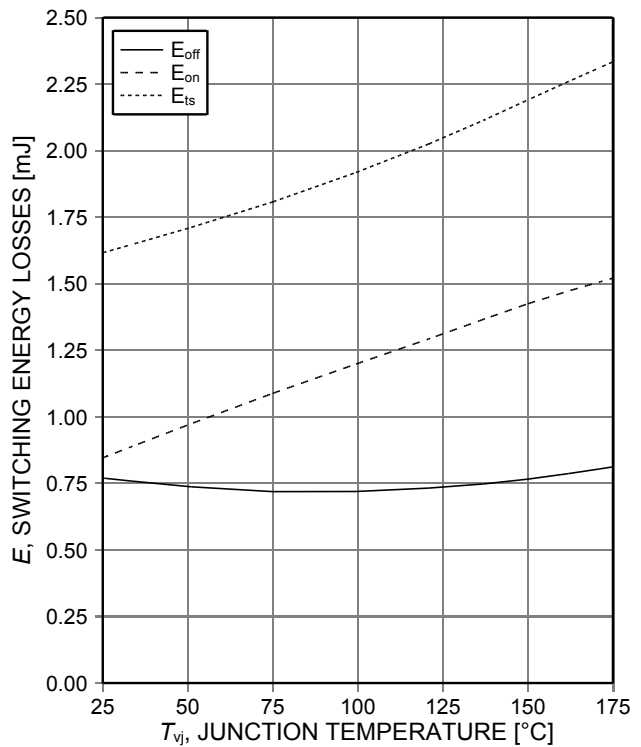


Figure 14. **Typical switching energy losses as a function of junction temperature**  
 (inductive load,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=50\text{A}$ ,  $R_{G(on)}=8\Omega$ ,  $R_{G(off)}=18\Omega$ , dynamic test circuit in Figure E)

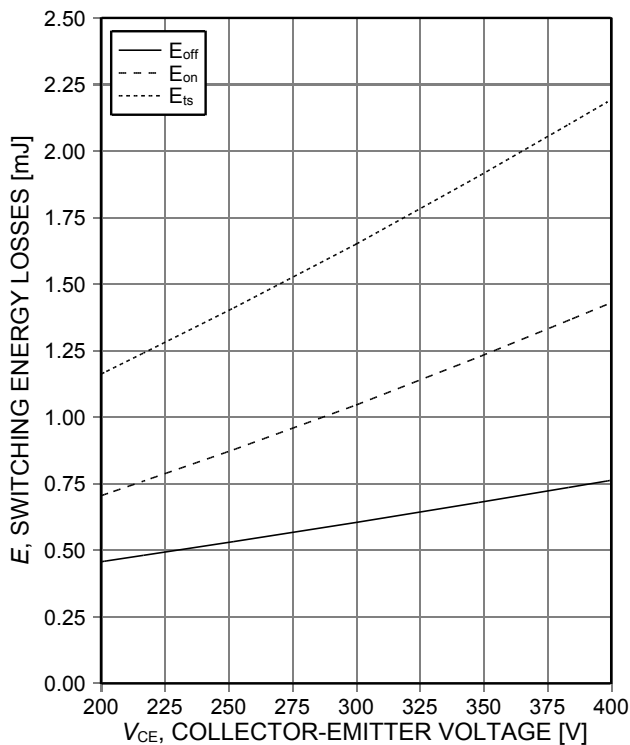


Figure 15. **Typical switching energy losses as a function of collector emitter voltage**  
 (inductive load,  $T_{vj}=150^{\circ}\text{C}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=50\text{A}$ ,  $R_{G(on)}=8\Omega$ ,  $R_{G(off)}=18\Omega$ , dynamic test circuit in Figure E)

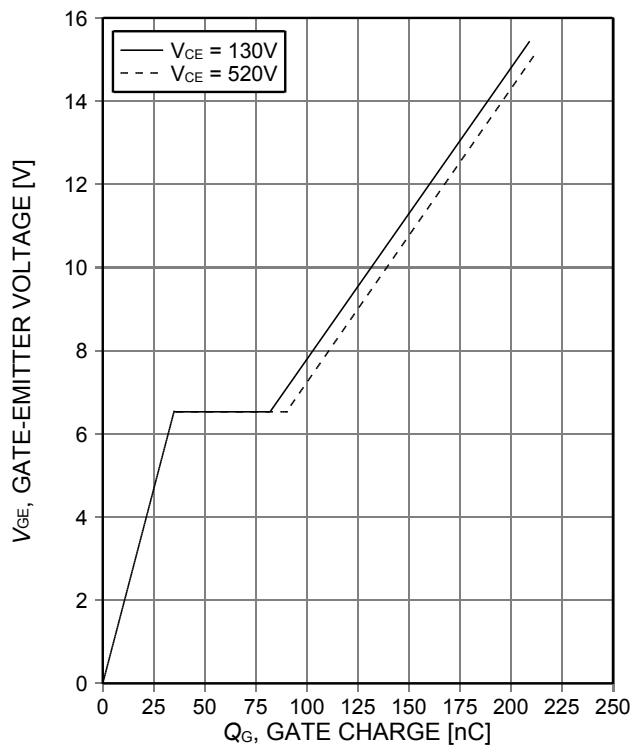


Figure 16. **Typical gate charge**  
 ( $I_C=100\text{A}$ )

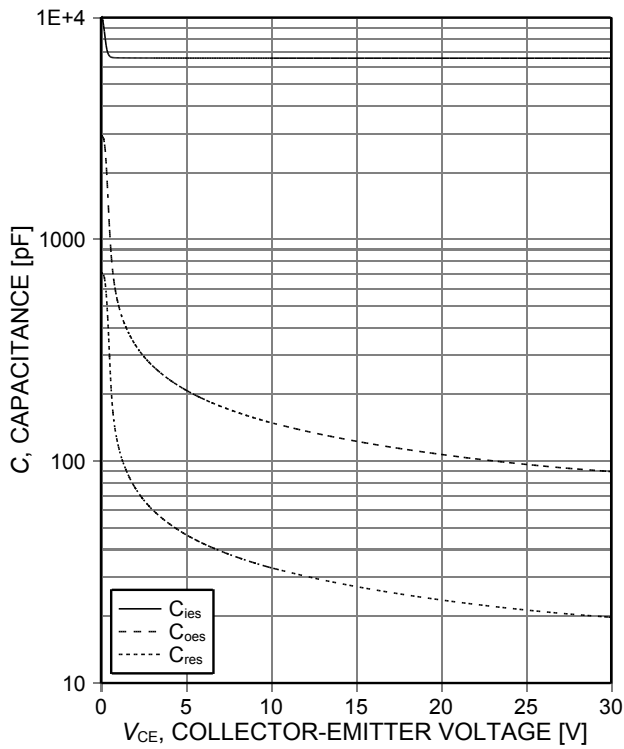


Figure 17. Typical capacitance as a function of collector-emitter voltage ( $V_{GE}=0V$ ,  $f=1MHz$ )

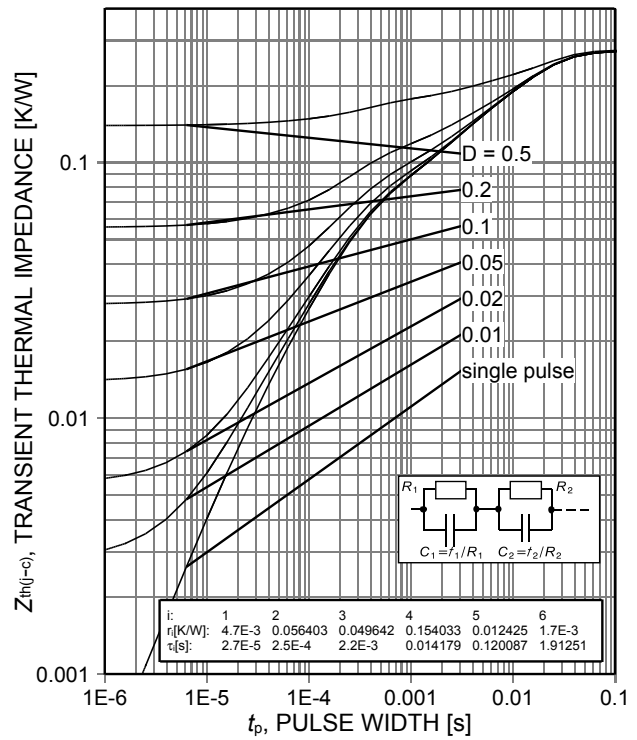


Figure 18. IGBT transient thermal impedance ( $D=t_p/T$ )

PG-TO247-4



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.83	5.21	0.190	0.205
A1	2.29	2.54	0.090	0.100
A2	1.90	2.16	0.075	0.085
b	1.07	1.33	0.042	0.052
b1	1.10	1.70	0.043	0.067
c	0.50	0.70	0.020	0.028
D	20.80	21.10	0.819	0.831
D1	16.25	17.65	0.640	0.695
D2	0.95	1.35	0.037	0.053
E	15.70	16.13	0.618	0.635
E1	13.10	14.15	0.516	0.557
E2	3.68	5.10	0.145	0.201
E3	1.00	2.60	0.039	0.102
e	2.54 (BSC)		0.100 (BSC)	
e1	5.08		0.200	
N	4		4	
L	19.72	20.32	0.776	0.800
L1	4.02	4.40	0.158	0.173
øP	3.50	3.70	0.138	0.146
øP1	7.00	7.40	0.276	0.291
Q	5.49	6.00	0.216	0.236
S	6.04	6.30	0.238	0.248

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**SCALE**

**EUROPEAN PROJECTION**

**ISSUE DATE**  
29-01-2013

**REVISION**  
1

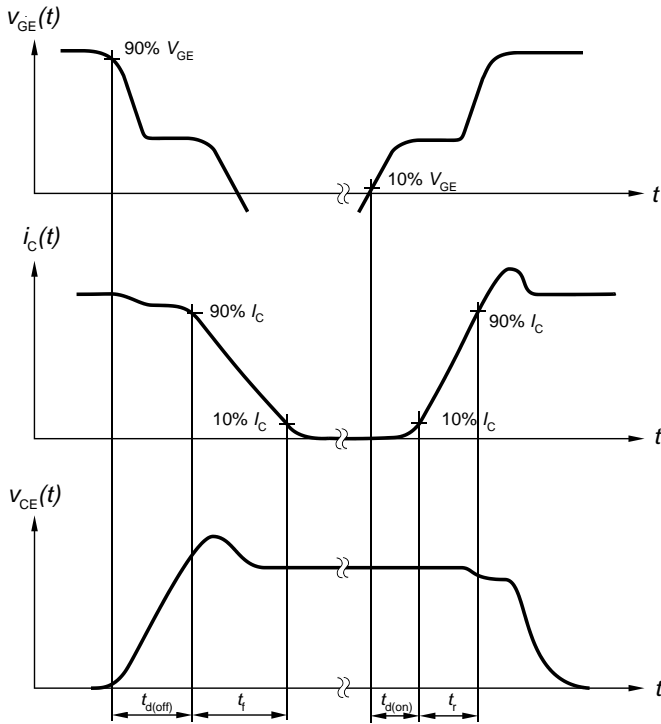


Figure A. Definition of switching times

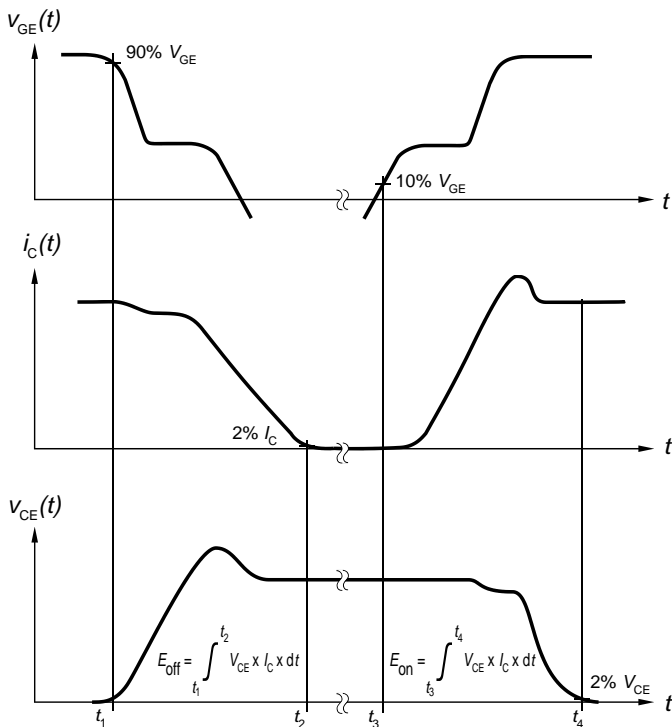


Figure B. Definition of switching losses

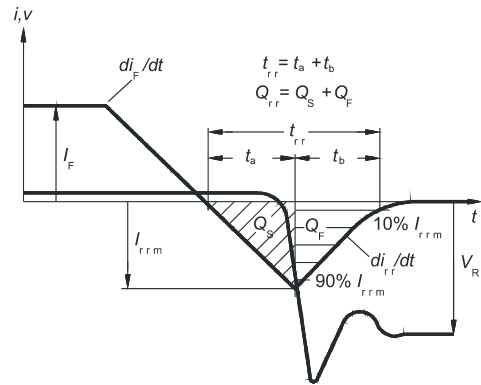


Figure C. Definition of diodes switching characteristics

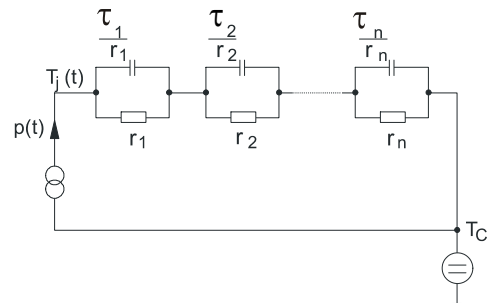


Figure D. Thermal equivalent circuit

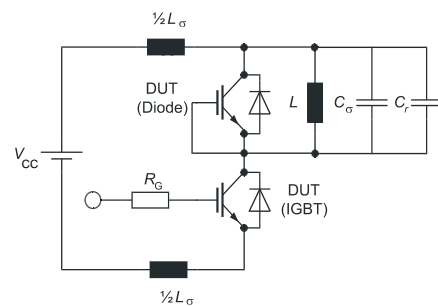


Figure E. Dynamic test circuit  
Parasitic inductance  $L_\sigma$ ,  
parasitic capacitor  $C_\sigma$ ,  
relief capacitor  $C_r$   
(only for ZVT switching)

## Revision History

IGZ100N65H5

Revision: 2014-10-31, Rev. 2.1

## Previous Revision

Revision	Date	Subjects (major changes since last revision)
1.1	2014-10-17	Preliminary data sheet
2.1	2014-10-31	Final data sheet

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Infineon Technologies AG

81726 Munich, Germany

81726 München, Germany

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### Наши контакты:

**Телефон:** +7 812 627 14 35

**Электронная почта:** [sales@st-electron.ru](mailto:sales@st-electron.ru)

**Адрес:** 198099, Санкт-Петербург,  
Промышленная ул, дом № 19, литера Н,  
помещение 100-Н Офис 331